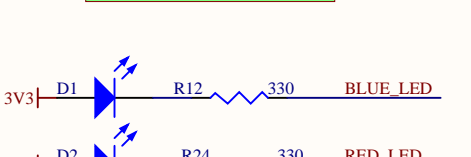
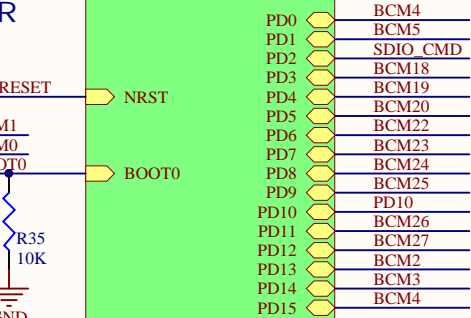
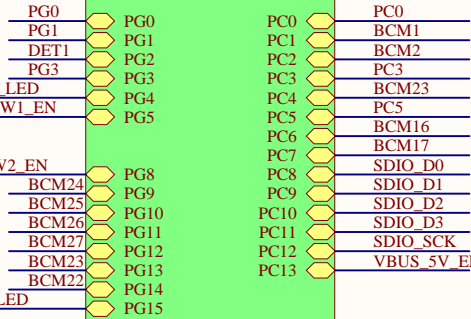
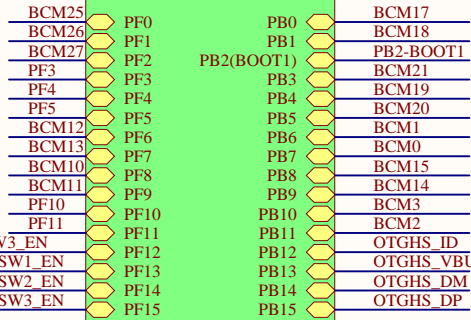
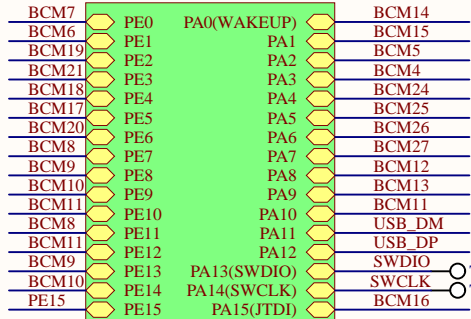


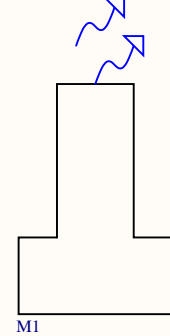
MCU



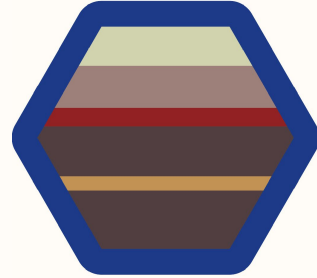
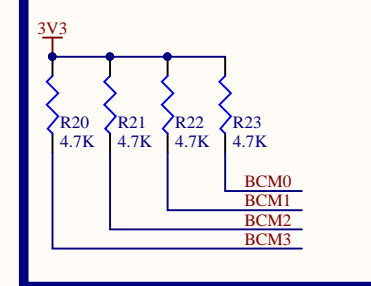
#	PORT	ANALOG	TIMER	SERIAL	SPI	MISC	NET
27	PB7		TIM4_CH2	USART1_RX		I2C2_SDA	BCM0
28	PB6, PC1	ADC1,2,3_IN11	TIM4_CH1	USART1_TX	SPI2_MISO	I2C2_SCL	BCM1
3	PB11, PC2, PD13	ADC1,2,3_IN12	TIM4_CH2, TIM2_CH4	USART3_RX	SPI2_MOSI	I2C2_SDA	BCM2
5	PB10, PD14		TIM4_CH3, TIM2_CH3	USART3_TX	SPI2_SCK	I2C2_SCL	BCM3
7	PA3, PD15	ADC1,2,3_IN3	TIM4_CH4, TIM2_CH4	USART2_RX		CAN1_RX	BCM4
29	PA2	ADC1,2,3_IN2	TIM2_CH3	USART2_TX		CAN1_TX	BCM5
31	PE1			UART8_TX			BCM6
26	PE0			UART8_RX			BCM7
24	PE7, PE11		TIM1_CH2	UART7_RX	SPI4_CE		BCM8
21	PE8, PE13		TIM1_CH3, TIM1_CH1_N	UART7_TX	SPI4_MISO		BCM9
19	PE9, PE14, PF8	ADC3_IN6	TIM1_CH4, TIM1_CH1	UART7_RTS	SPI4_MOSI	SAI1_SCK_B	BCM10
23	PA10, PE10, PE12, PF9	ADC3_IN7	TIM1_CH2_N, TIM1_CH3	UART7_CTS	SPI4_SCK	SAI1_FS_B	BCM11
32	PA8, PF6	ADC3_IN4	TIM1_CH1	UART7_RX	SPI5_CE	SAI1_SD_B	BCM12
33	PA9, PF7	ADC3_IN5	TIM1_CH2	UART7_TX	SPI5_SCK	SAI1_MCK_B	BCM13
8	PA0, PB9	ADC1,2,3_IN0	TIM4_CH4, TIM2_CH1	UART4_TX	SPI5_MISO	CAN1_TX	BCM14
10	PA1, PB8	ADC1,2,3_IN1	TIM4_CH3, TIM2_CH2	UART4_RX	SPI5_MOSI	CAN1_RX	BCM15
36	PA15, PC6, PF8		TIM3_CH1, TIM2_CH1	USART6_TX			BCM16
11	PB0, PC7, PE5	ADC1,2_IN8	TIM3_CH2, TIM1_CH2	USART6_RX		SAI1_SCK_A	BCM17
12	PB1, PD3, PE4	ADC1,2_IN9	TIM1_CH3	USART2_CTS		SAI1_FS_A	BCM18
35	PB4, PD4, PE2		TIM3_CH1	USART2_RTS	SPI3_MISO		BCM19
38	PB5, PD5, PE6		TIM3_CH2	USART2_TX	SPI3_MOSI	SAI1_SD_A	BCM20
40	PB3, PE3		TIM2_CH2		SPI3_SCK	SAI1_SD_B	BCM21
15	PD6, PG14			USART2_RX		SDIO2_SCK	BCM22
16	PC4, PD7, PG13	ADC1,2_IN14				SDIO2_CMD	BCM23
18	PA4, PD8, PG9	ADC1,2_IN4, DAC_OUT1		USART3_TX	SPI1_CE	SDIO2_D0	BCM24
22	PA5, PD9, PG10, PF0	ADC1,2_IN5, DAC_OUT2	TIM2_CH1	USART3_RX	SPI1_SCK	SDIO2_D1	BCM25
37	PA6, PD11, PF1, PG11	ADC1,2_IN6		USART3_CTS	SPI1_MISO	SDIO2_D2	BCM26
13	PA7, PD12, PF2, PG12	ADC1,2_IN7	TIM1_CH1_N	USART3_RTS	SPI1_MOSI	SDIO2_D3	BCM27



LIGHT PIPE



I2C PULLUPS



Stratify Toolbox

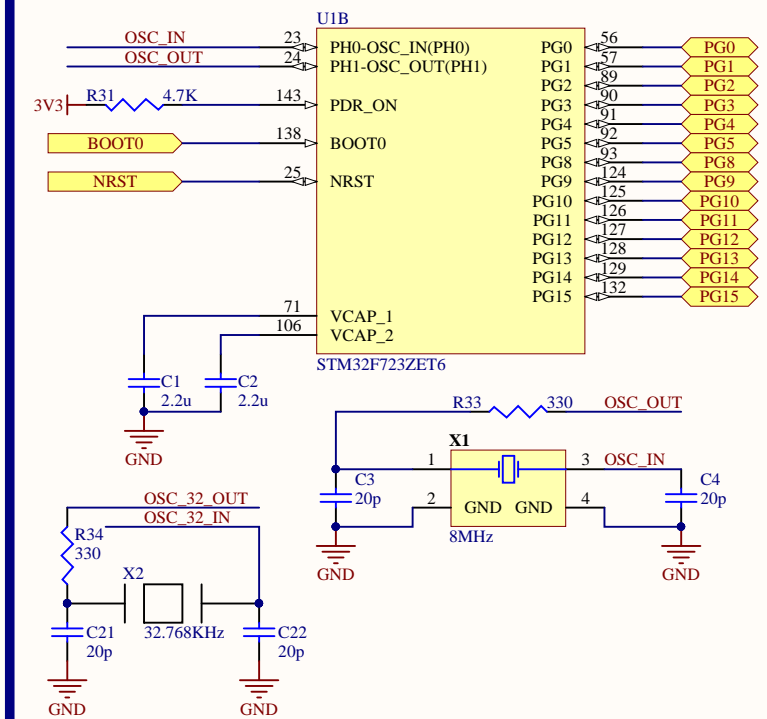
TOP

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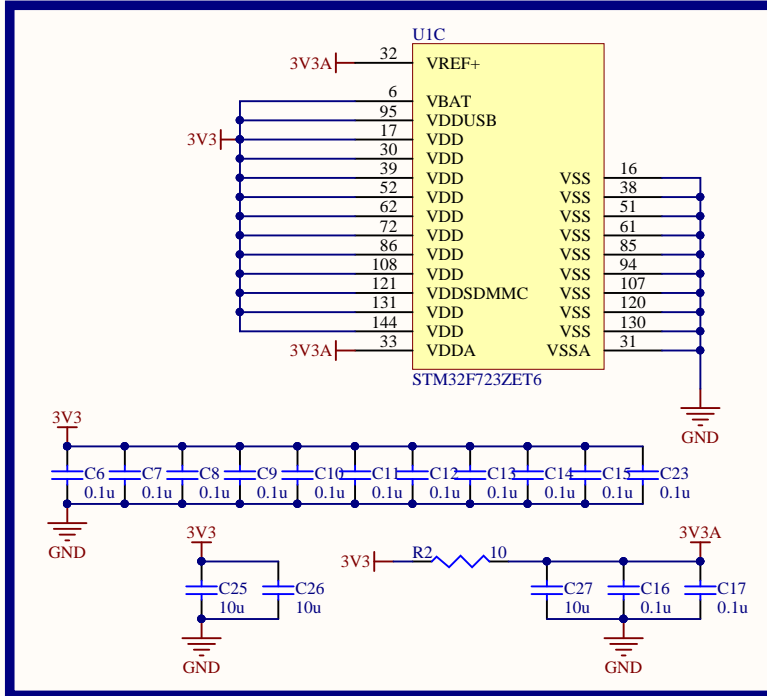
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Scale	NA	Sheet	1 of 3

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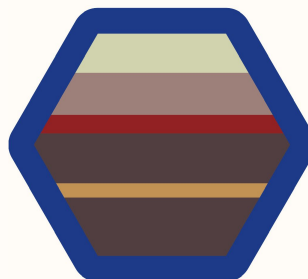
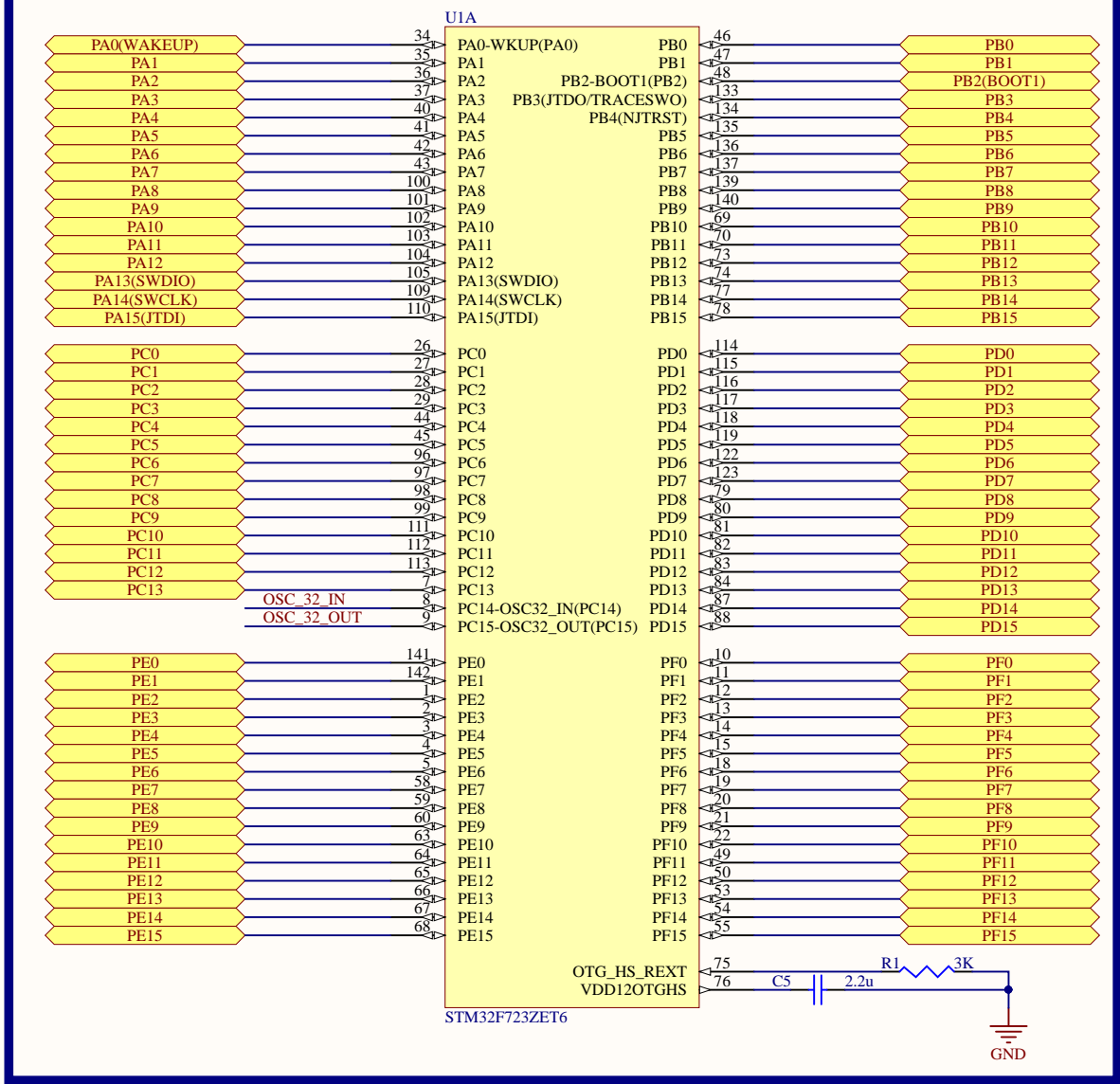
CLOCK/BOOT/PORTG



POWER



IO



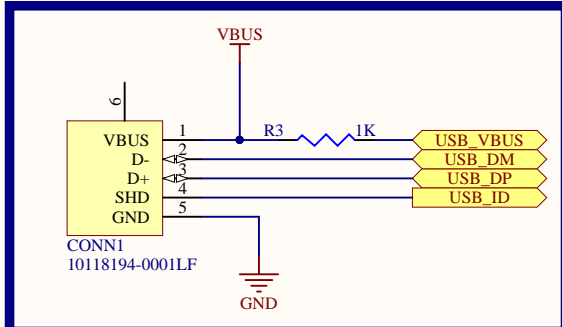
Stratify Toolbox

MCU

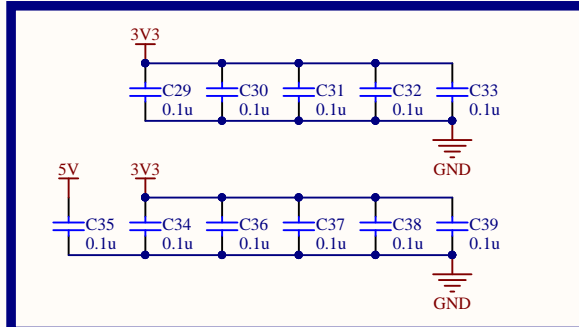
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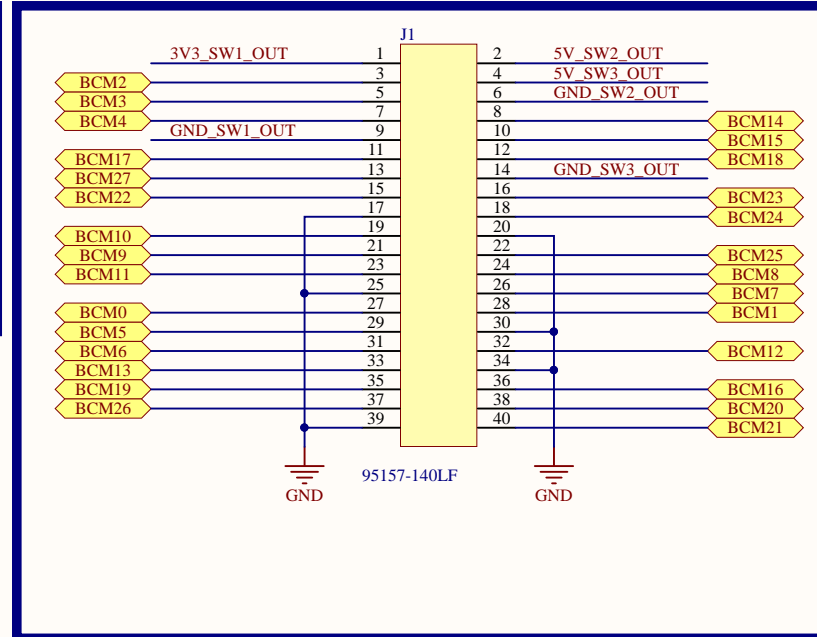
USB 2.0 HIGH SPEED



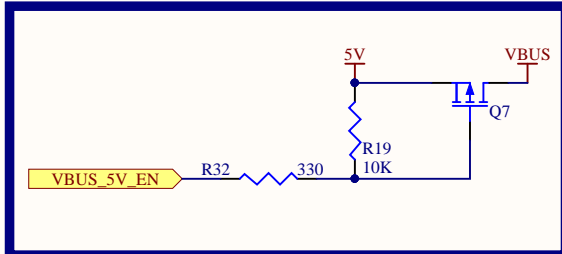
SYSTEM DECOUPLING



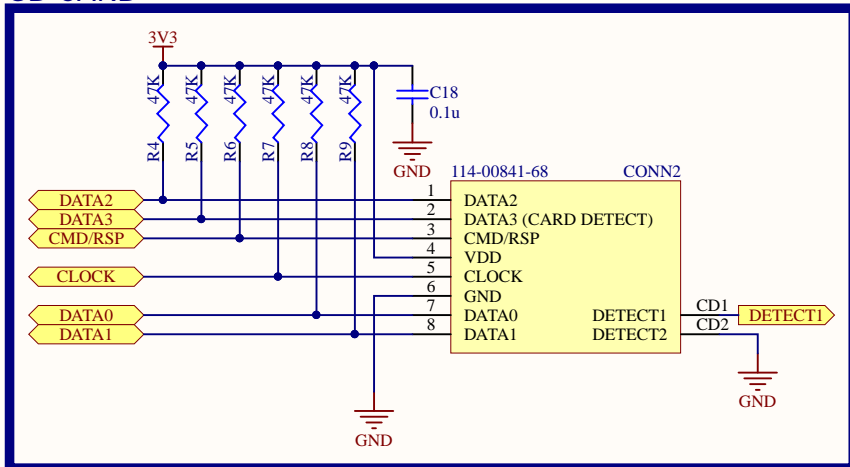
40 PIN PI-COMPATIBLE HEADER



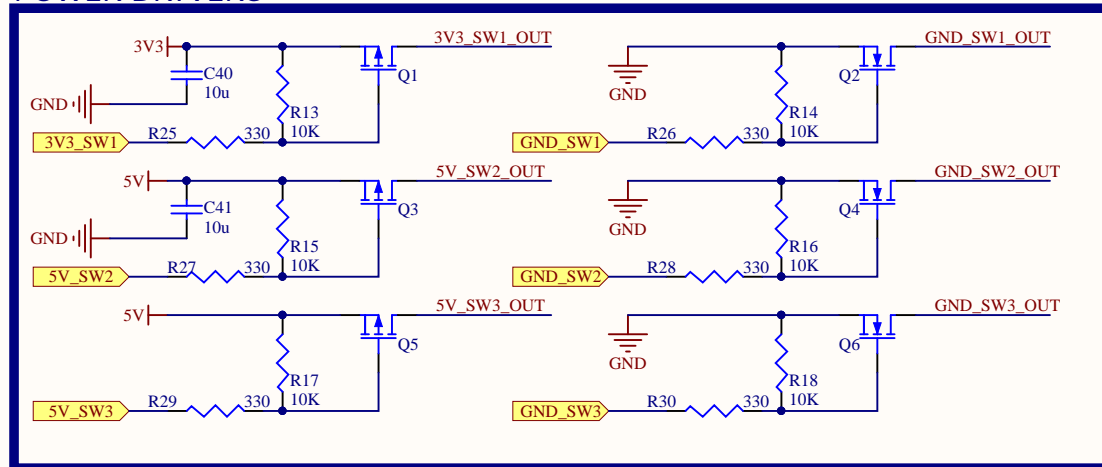
VBUS 5V SWITCH



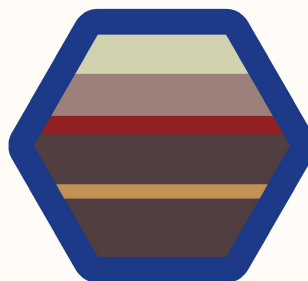
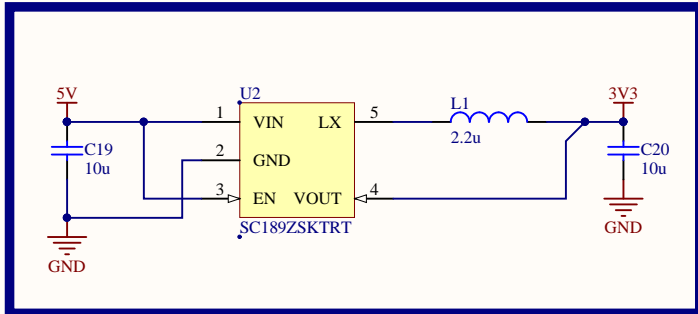
SD CARD



POWER DRIVERS



POWER SUPPLY



Stratify Toolbox

IO

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Size Letter	FCSM No.	DWG No.	Rev
			X1
Scale	NA	Sheet	3 of 3

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