MUL (register)

AX = AL * Reg. Memonic (8-bit)

(DX AX) = AX * Reg Mem (16)

AX 0002 → 0200

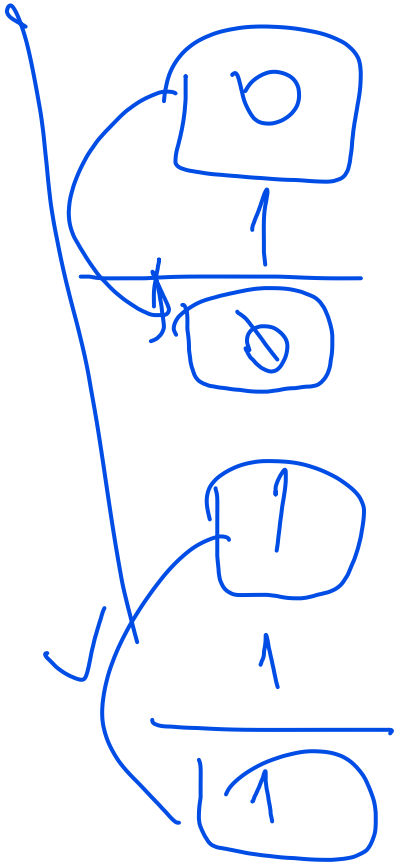
DX AX → 0000 0200

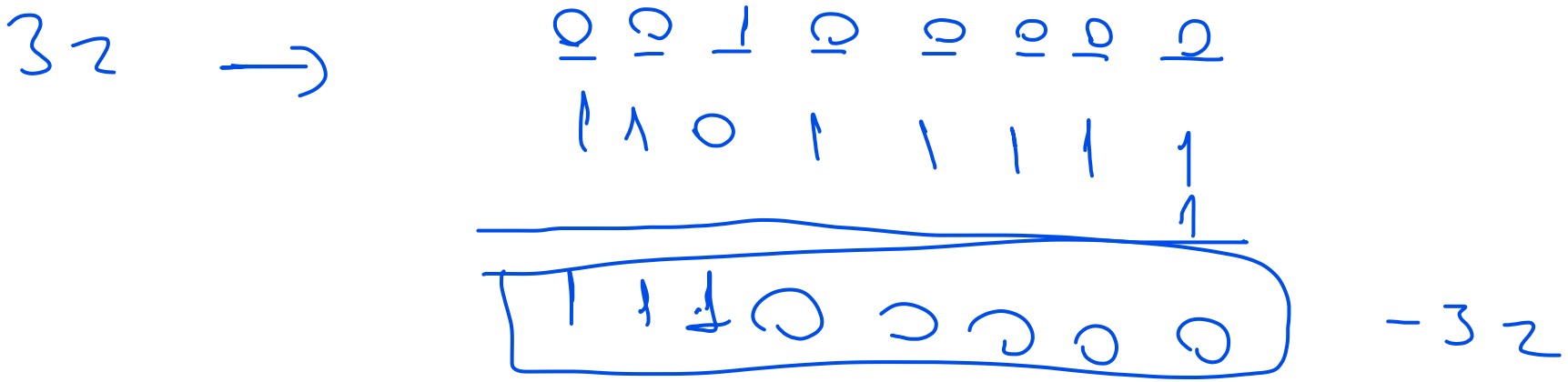
0 1 1 1 1 1 1 1 (+127) positif

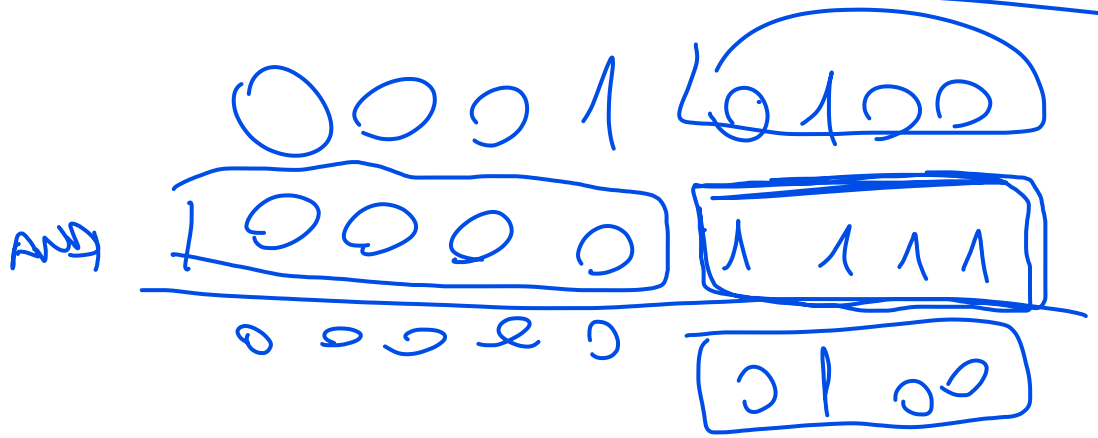
1 0 0 0 0 0 0 0 (-128)

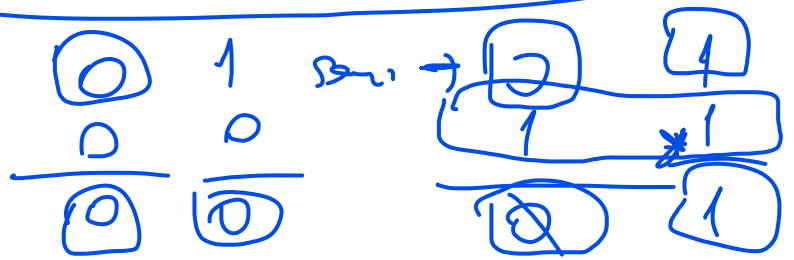
0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 +32 767

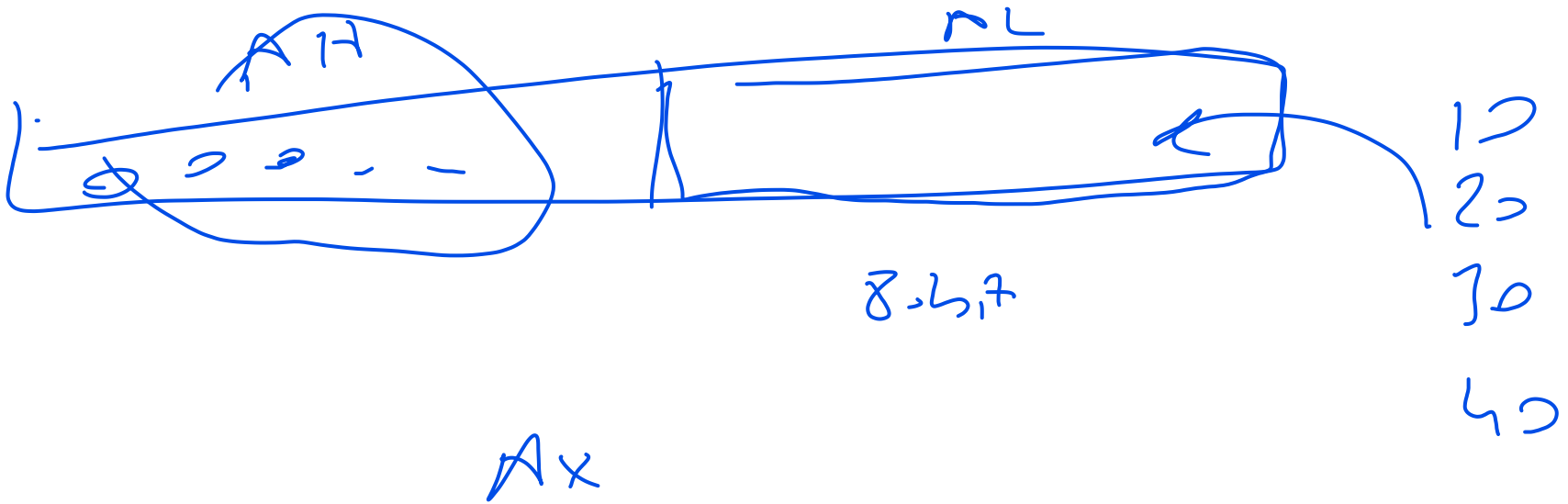
1 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 -32 768




3₂
-3₂


AND




Δ17
ΔL
8.417
10
20
30
40
Δx

2.3f

0 1 1 1 1 1 1 1 +127

1 0 0 0 0 0 0 0 −128

0 1 1 1 1 1 1 1 +32767

1 0 0 0 0 0 0 0 −32768

0001 1200

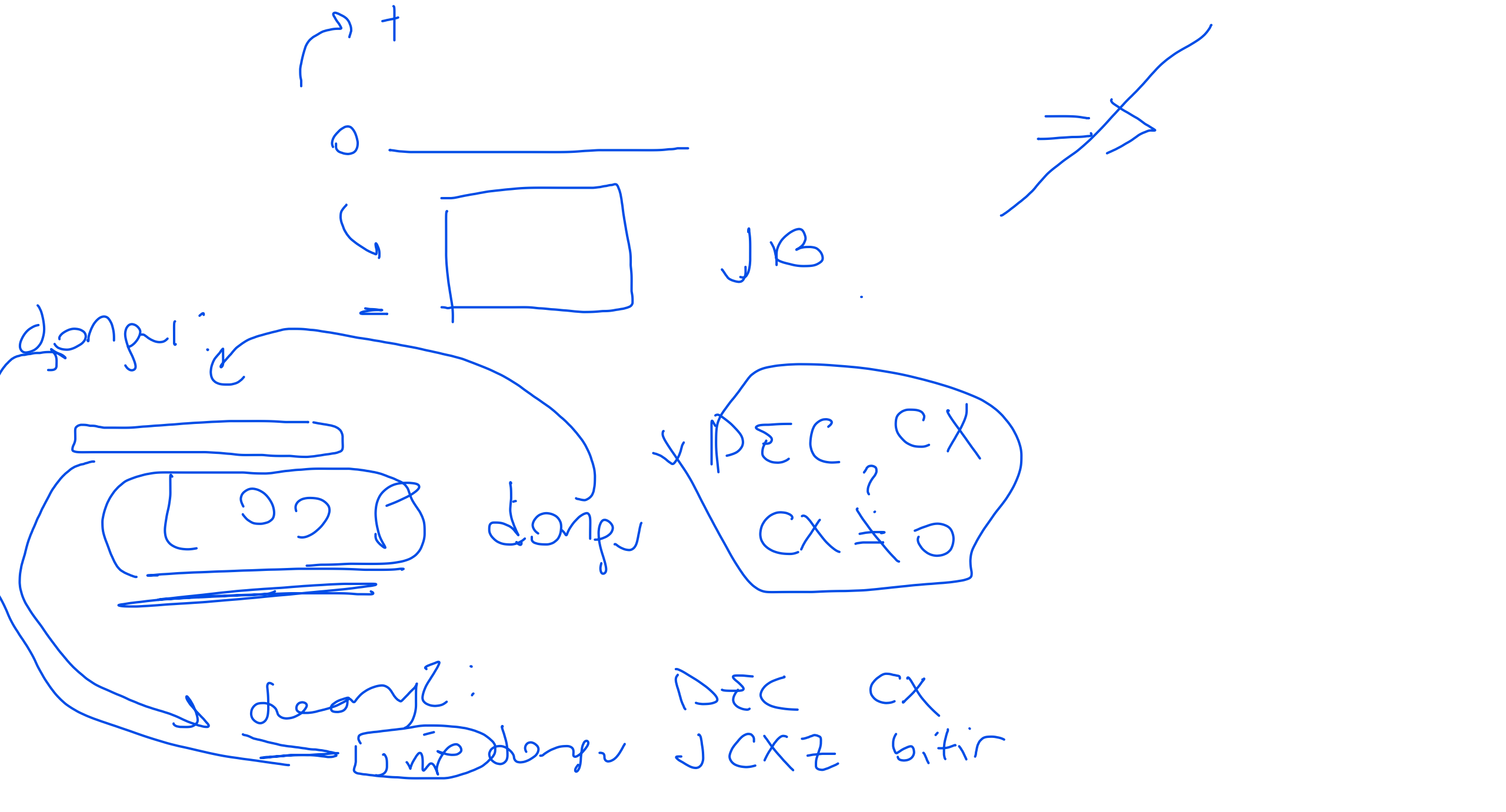
DX AX

0 0 1 0 0 0 0 0 32

1 1 0 1 1 1 1 1

+1

1 1 1 0 0 0 0 0 -32


t
JB
donguı:
DEC CX
CX ≠ 0
donguı
donguı:
DEC CX
JCXZ bitir