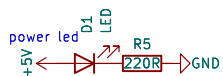
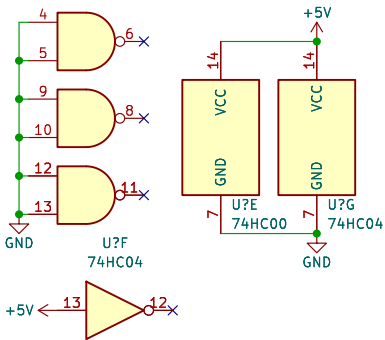
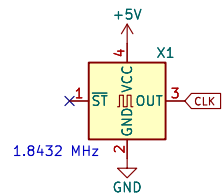
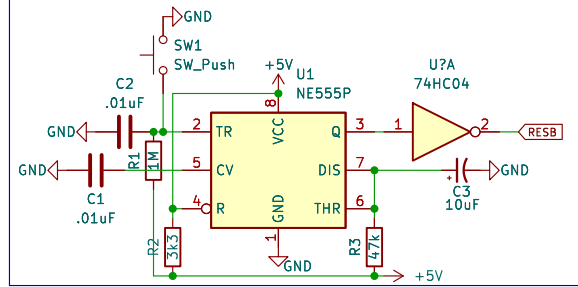
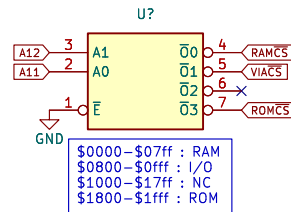
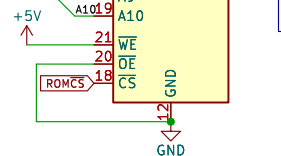
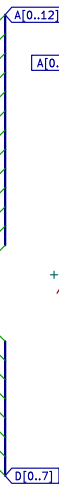
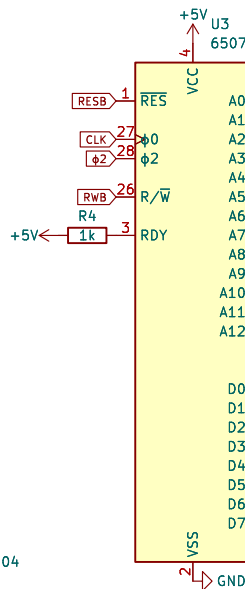
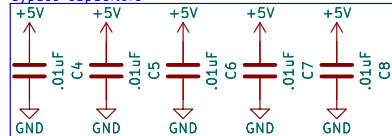


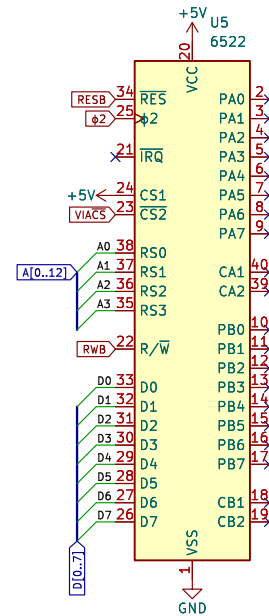
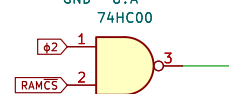
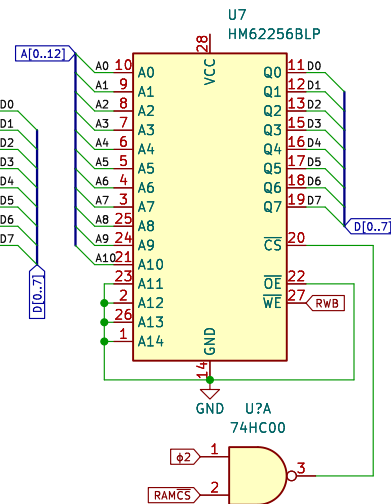
reset circuit (<https://wilsonminesco.com/6502primer/RSTregs.html>)



bypass capacitors



\$0000-\$07ff : RAM  
\$0800-\$0fff : I/O  
\$1000-\$17ff : NC  
\$1800-\$1fff : ROM



6507 single board computer  
<https://github.com/StringLess80/6507-SBC>

Sheet: /

File: 6507.kicad\_sch

**Title: 6507 SBC**

Size: A4 Date: 2023-08-30

KiCad E.D.A. kicad 7.0.7

**Rev: 2**

Id: 1/1