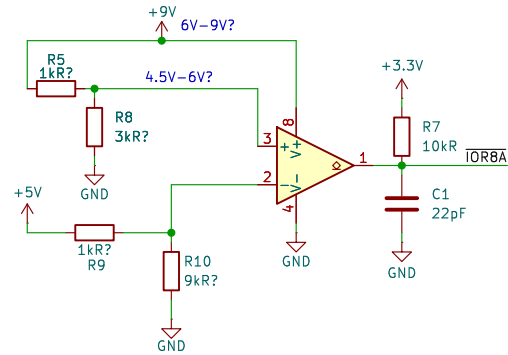
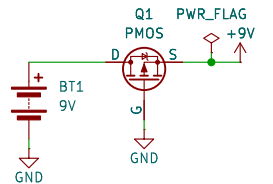


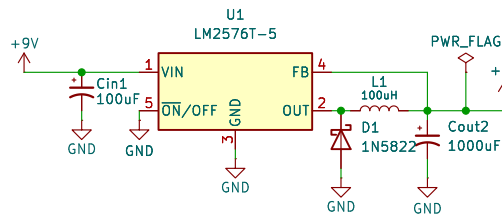
Battery low voltage detector



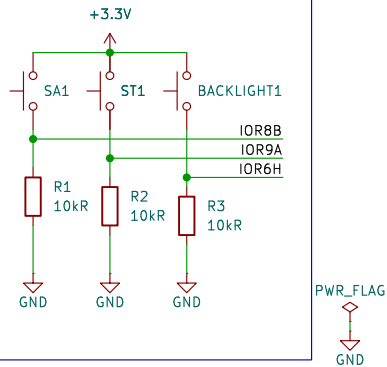
Battery with reverse added polarity protection



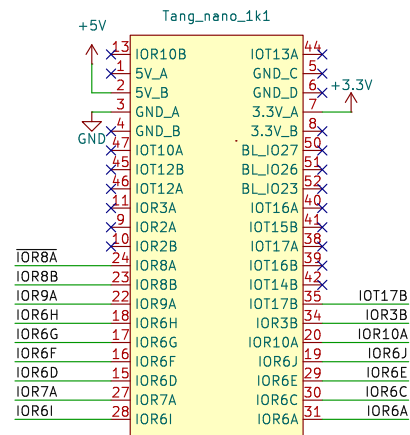
9-5 volt step down



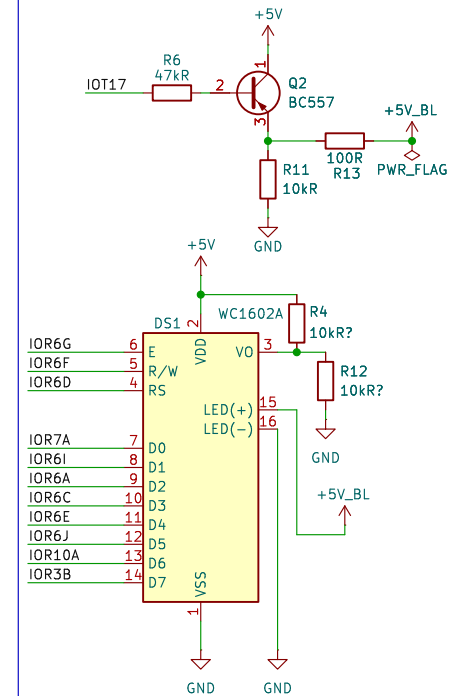
Active high button input



FPGA board



LCD display



Sheet: /  
File: Tang-Nano-1k-FPGA-alarm-clock-LCD.kicad\_sch

**Title:**

Size: A4

Date:

KiCad E.D.A. 9.0.5

**Rev:**

Id: 1/1