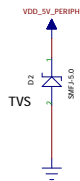
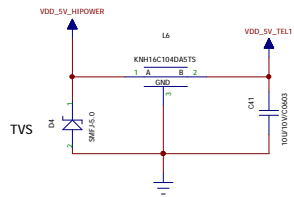
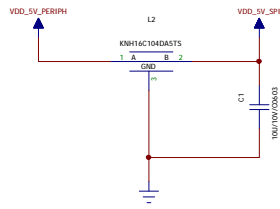
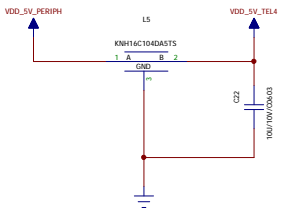
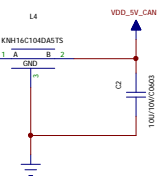
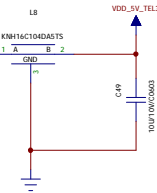
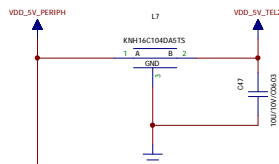
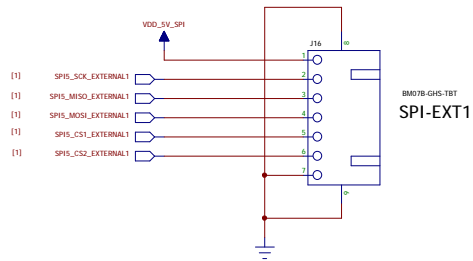
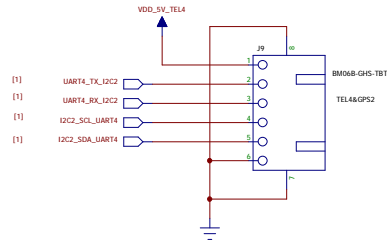
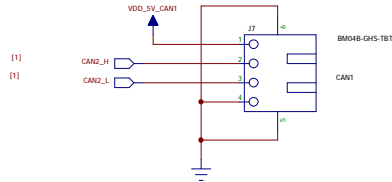
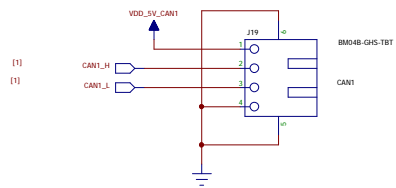
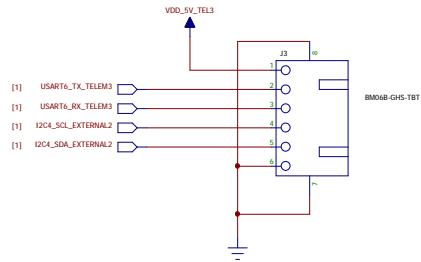
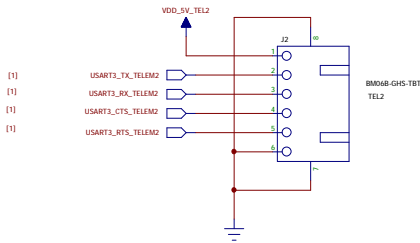
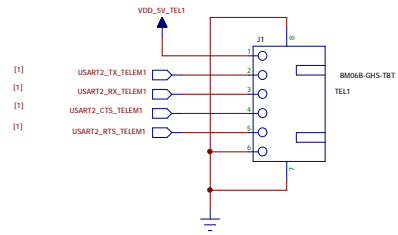


REVISION RECORD			



COMPANY: Holybro			
TITLE: PIX32-V5-BASE-mini			
DRAWN: Jamming	DATED: 2020/02/25	CODE: 028	SIZE: A2
CHECKED: Jamming	DATED: 2020/02/28	Sheet: UART-I2C-CAN	
QUALITY CONTROL:	DATED:	REV: RC01	
RELEASED: Jamming	DATED: 2020/04/23	SCALE: 1:1	
		SHEET: 2 of 5	

6

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4

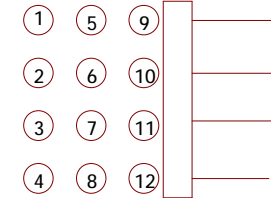
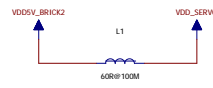
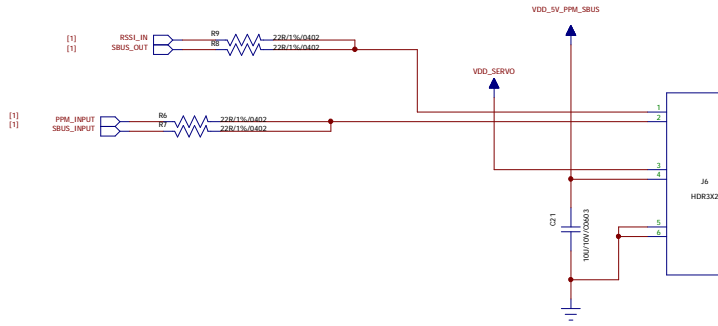
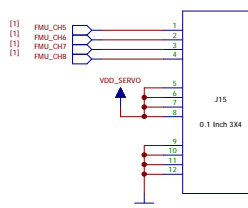
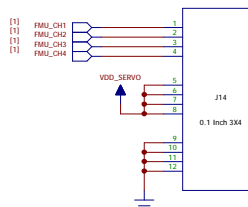
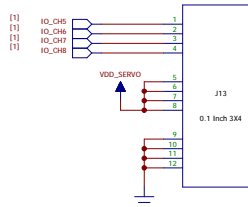
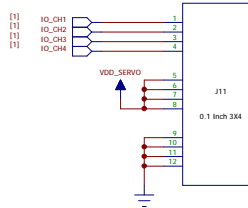
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REVISION RECORD			
LTR	ECD NO:	APPROVED:	DATE:

ESD Componets are placed on FMU Board
No additional ESD protect is required here



D

C

B

A

D

C

B

A

DRAWN:	Jamming	DATED:	2020/02/25
CHECKED:	Jamming	DATED:	2020/02/28
QUALITY CONTROL:		DATED:	
RELEASED:	Jamming	DATED:	2020/04/23

COMPANY: Holybro			
TITLE: PIX32-V5-BASE-mini			
CODE:	SIZE:	DRAWING NO:	REV:
028	A2	Servo Out	RC01
SCALE: 1:1			SHEET: 4 of 5

6

5

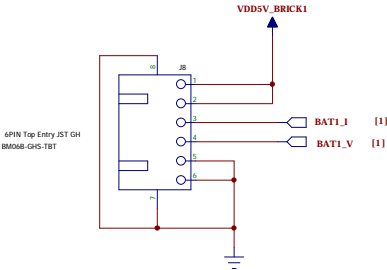
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1

REVISION RECORD			



M1



MARK

M4



MARK

M2



MARK

M5



MARK

M3



MARK

COMPANY: <i>Holybro</i>			
TITLE: PIX32-V5-BASE-mini			
CODE:	SIZE:	SHEET:	REV:
028	A2	BAT-SENSE	RC01
SCALE: 1:1		SHEET: 5 OF 5	

DRAWN:	Jamming	DATED:	2020/02/25
CHECKED:	Jamming	DATED:	2020/02/28
QUALITY CONTROL:		DATED:	
RELEASED:	Jamming	DATED:	2020/04/23