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(54) **SYSTEMS, APPARATUSES, AND METHODS  
FOR FILTERING HIGH VOLTAGE NOISE  
INDUCED BY PULSED ELECTRIC FIELD  
ABLATION**

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continuation of application No. 17/091,289, filed on  
Nov. 6, 2020, now Pat. No. 11,033,236, which is a  
continuation of application No. PCT/US2019/  
030922, filed on May 6, 2019.

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7, 2018.

#### **Publication Classification**

(51) **Int. Cl.**

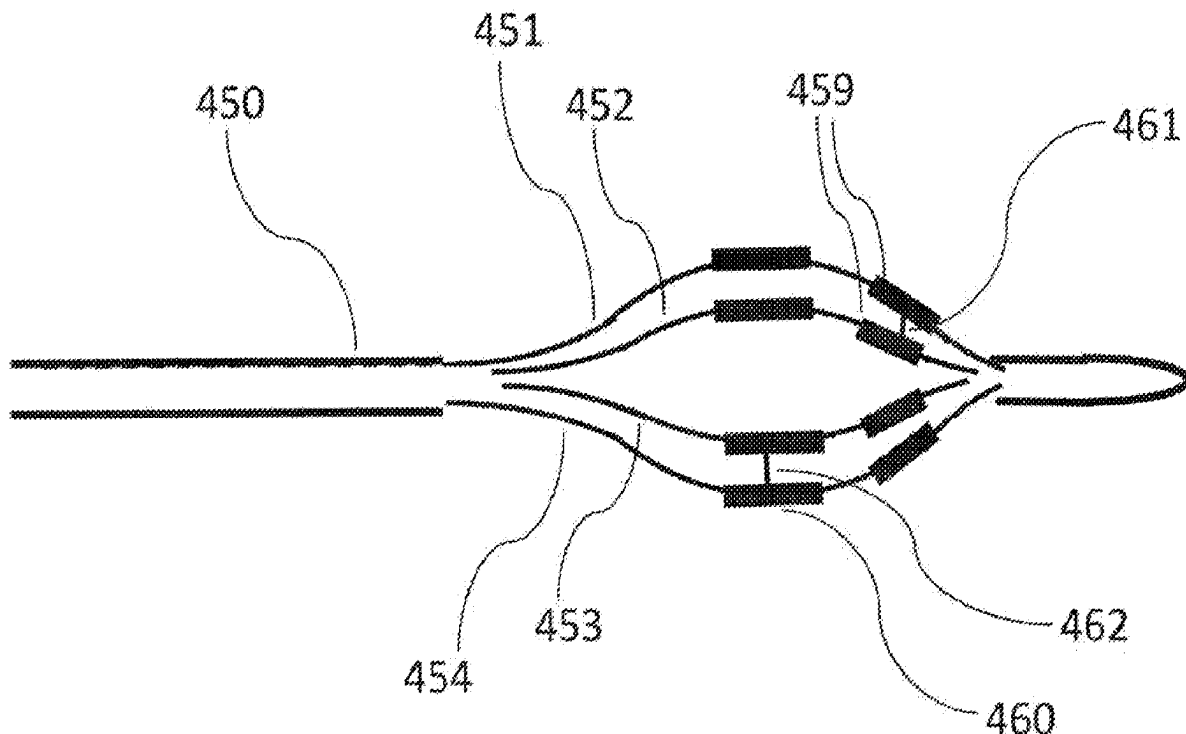
*A61B 5/00* (2006.01)

*A61B 5/305* (2021.01)

(57)

#### **ABSTRACT**

Systems, apparatuses, and methods for electroporation ablation therapy are disclosed, with a protection device for protecting electronic circuitry, devices, and/or other components from induced currents and voltages generated during a cardiac ablation procedure. A system can include an ablation device near cardiac tissue of a heart. The system can further include a signal generator configured to generate a pulse waveform, where the signal generator coupled to the ablation device and configured to repeatedly deliver the pulse waveform to the ablation device in synchrony with a set of cardiac cycles of the heart. The system can further include a protection device configured to suppress induced current and voltage in an electronic device coupled to the protection device.



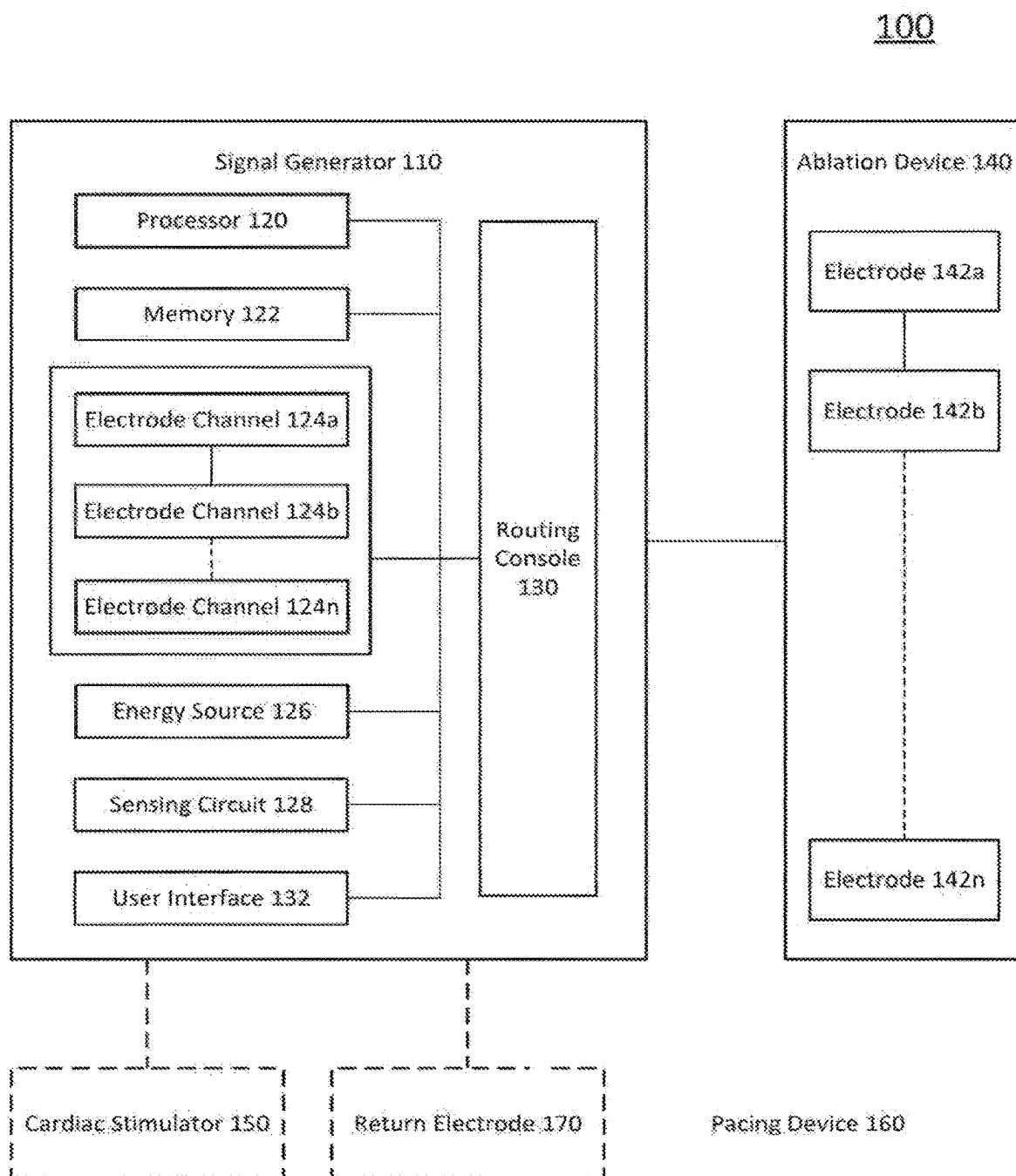


FIG. 1

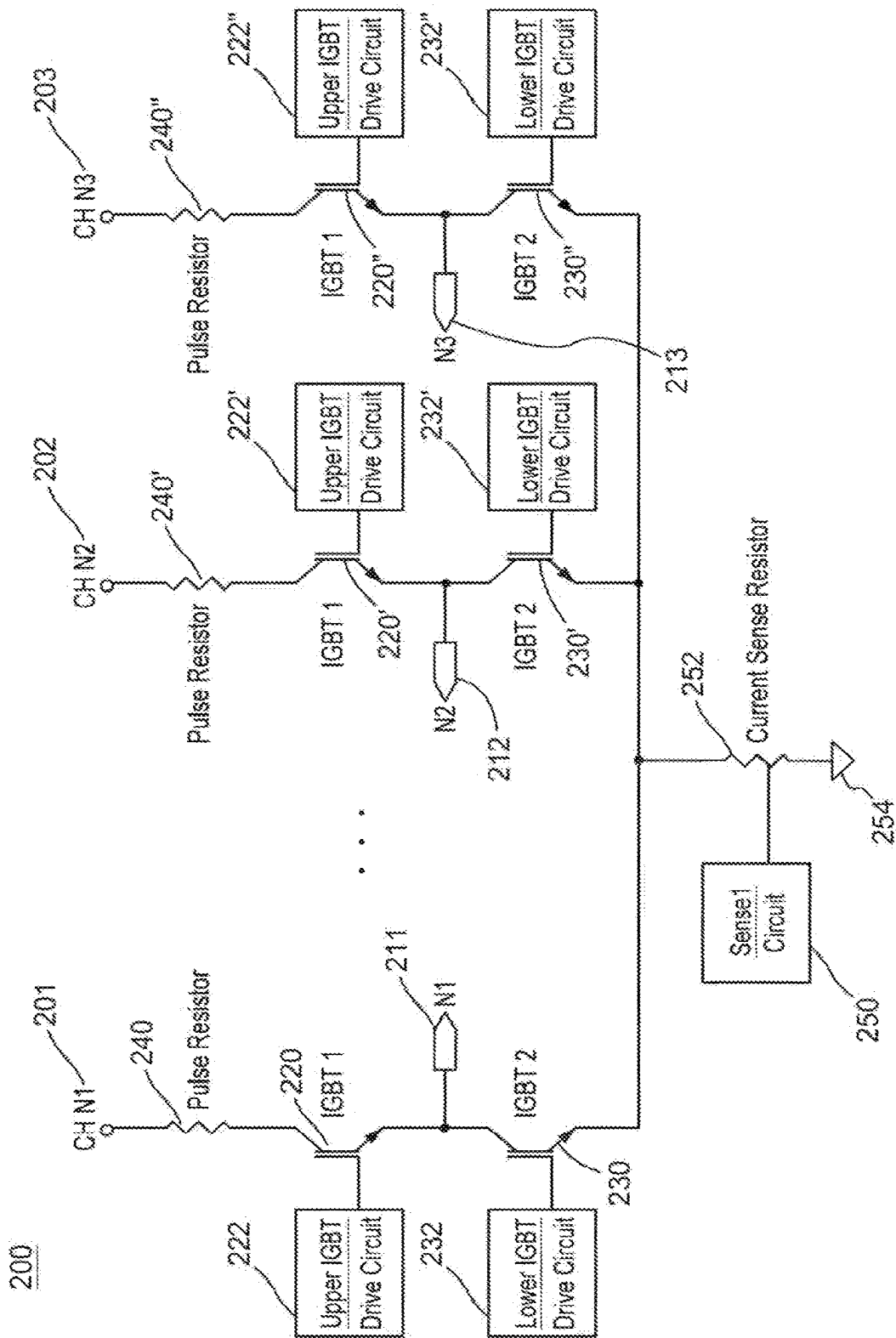


FIG. 2

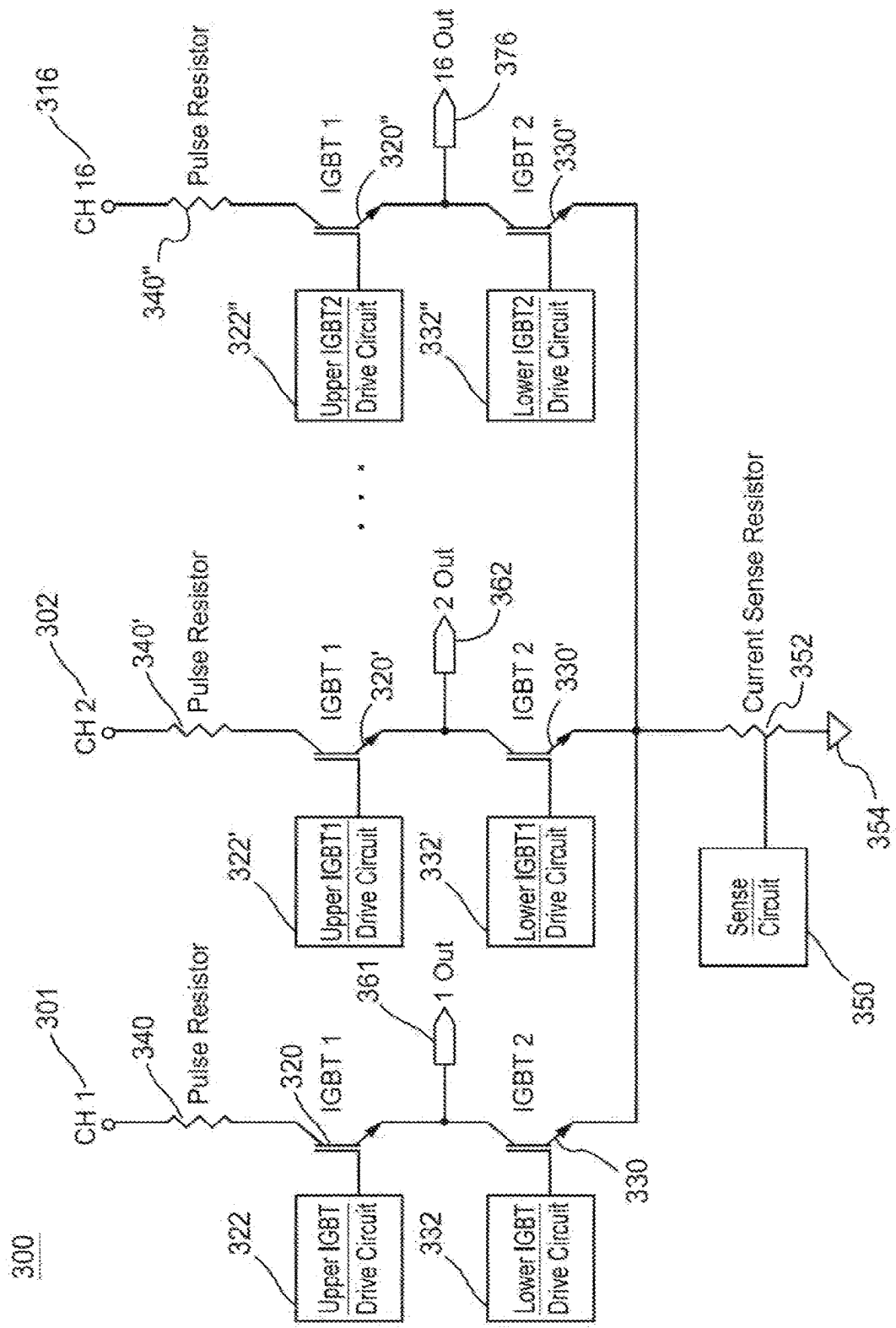


FIG. 3

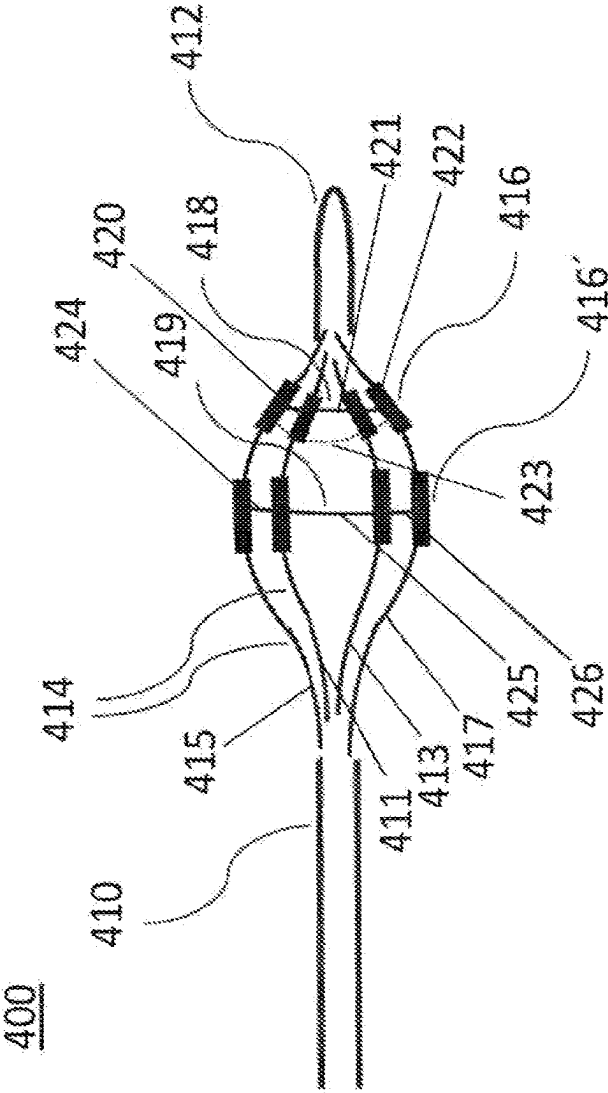


FIG. 4A

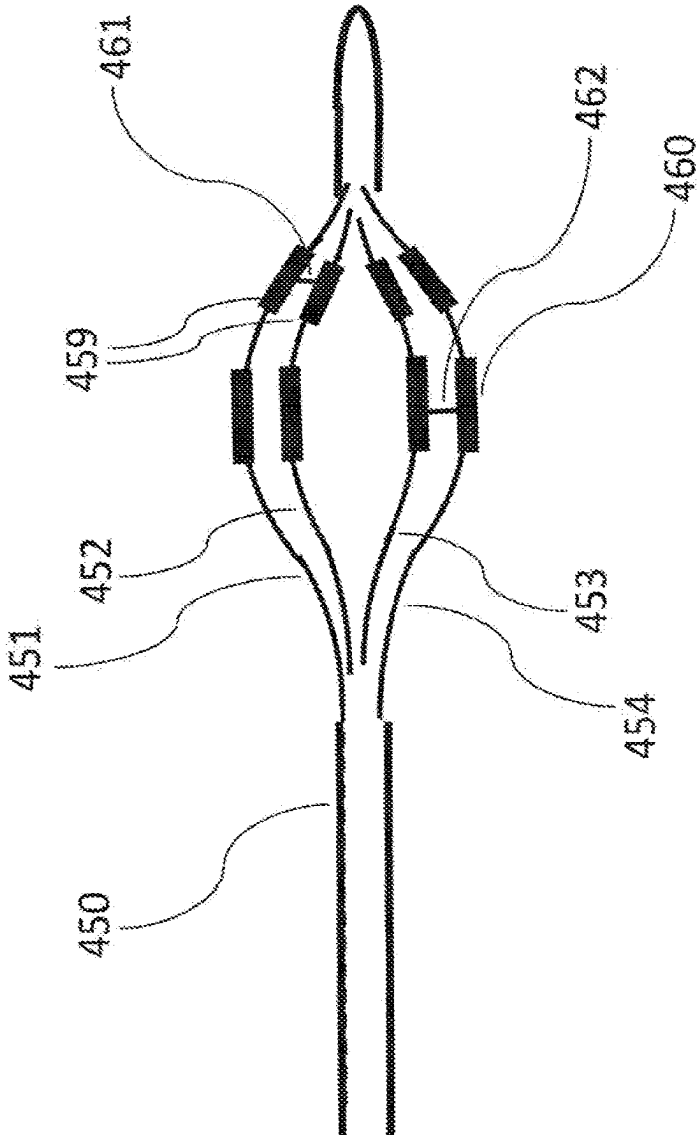


FIG. 4B

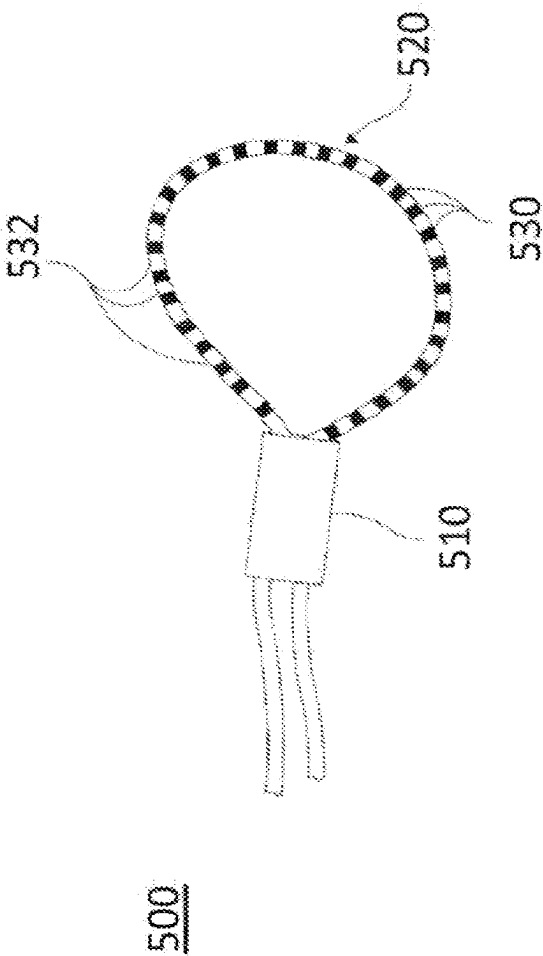


FIG. 5

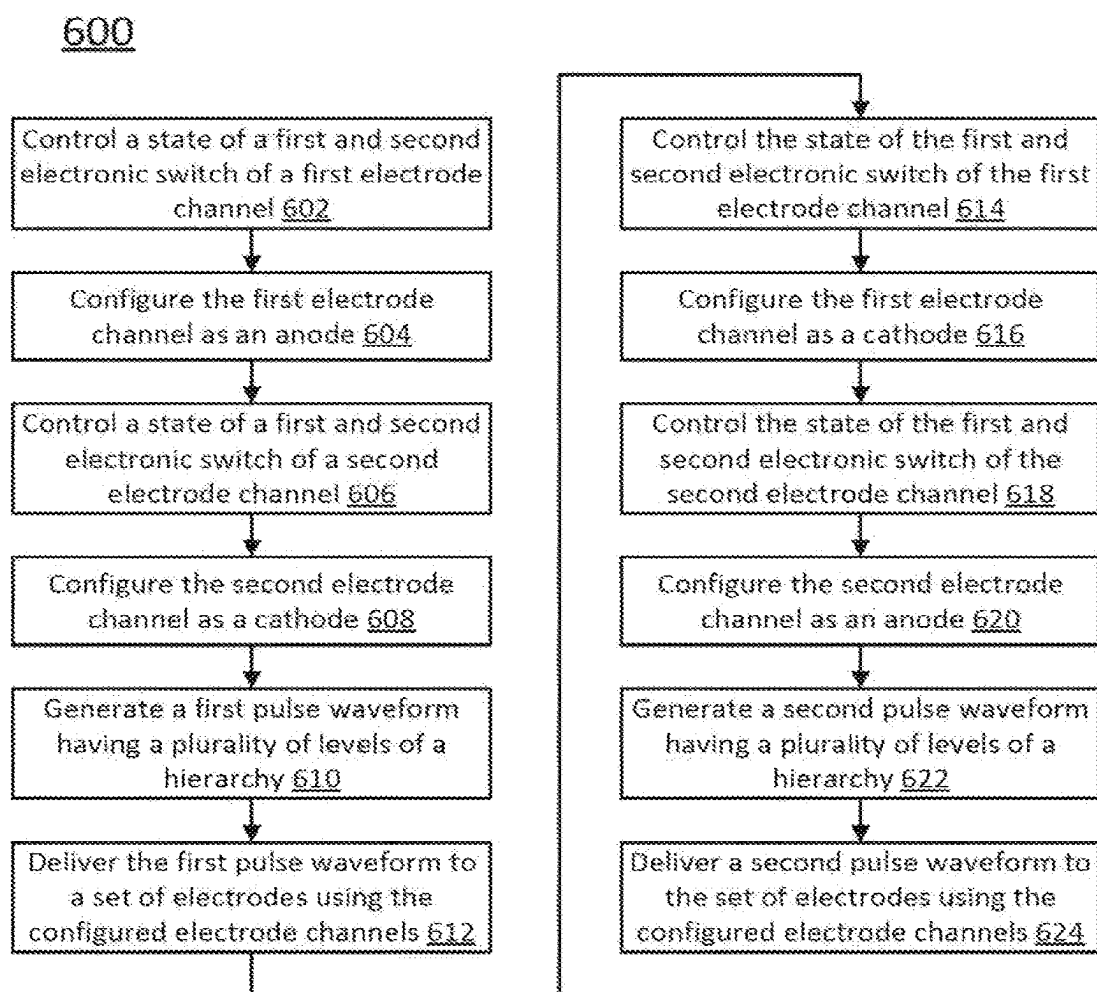


FIG. 6



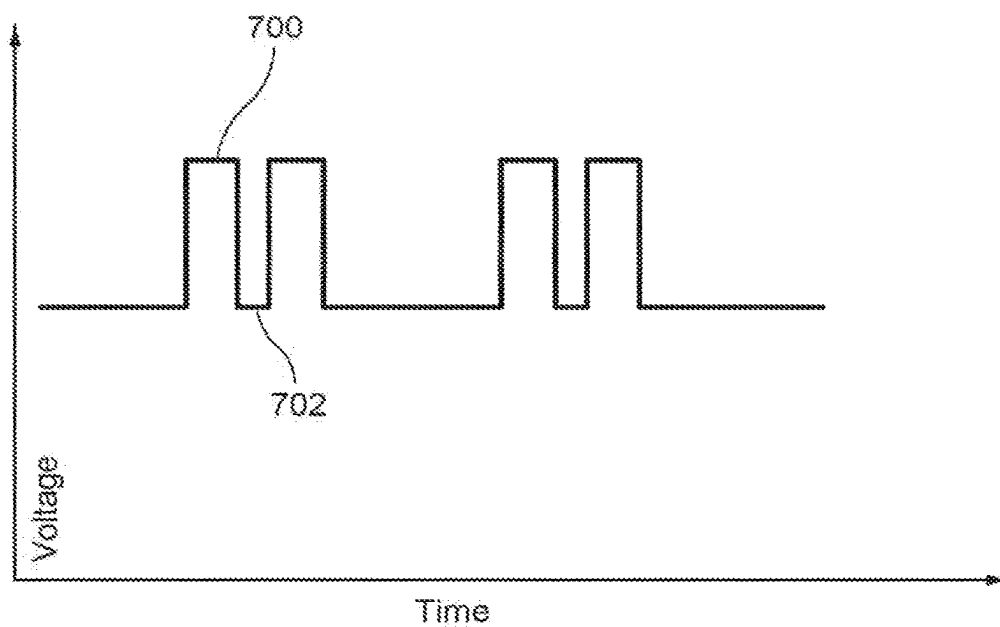


FIG. 7

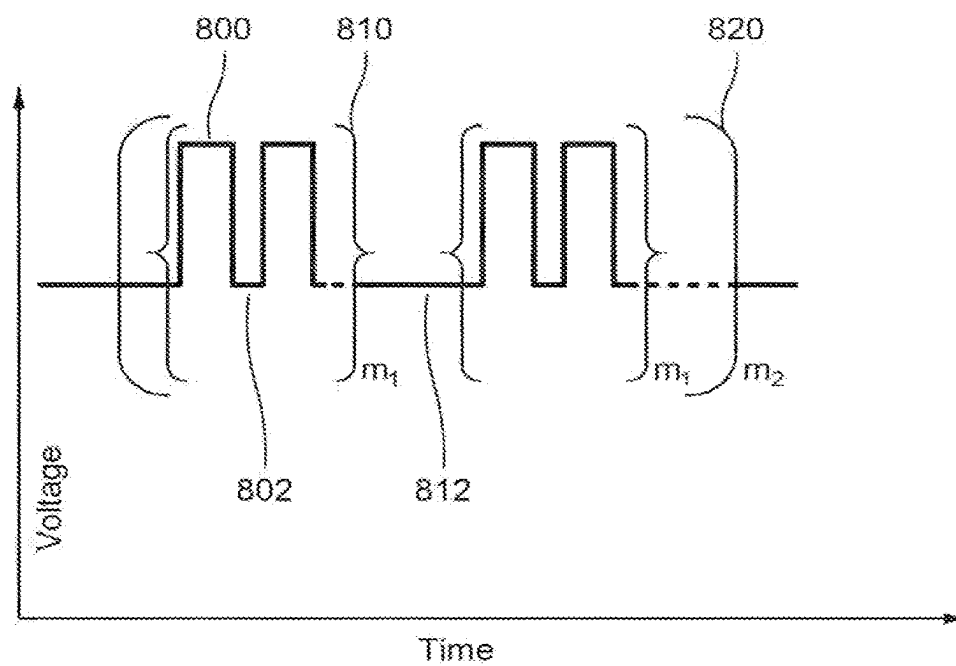


FIG. 8

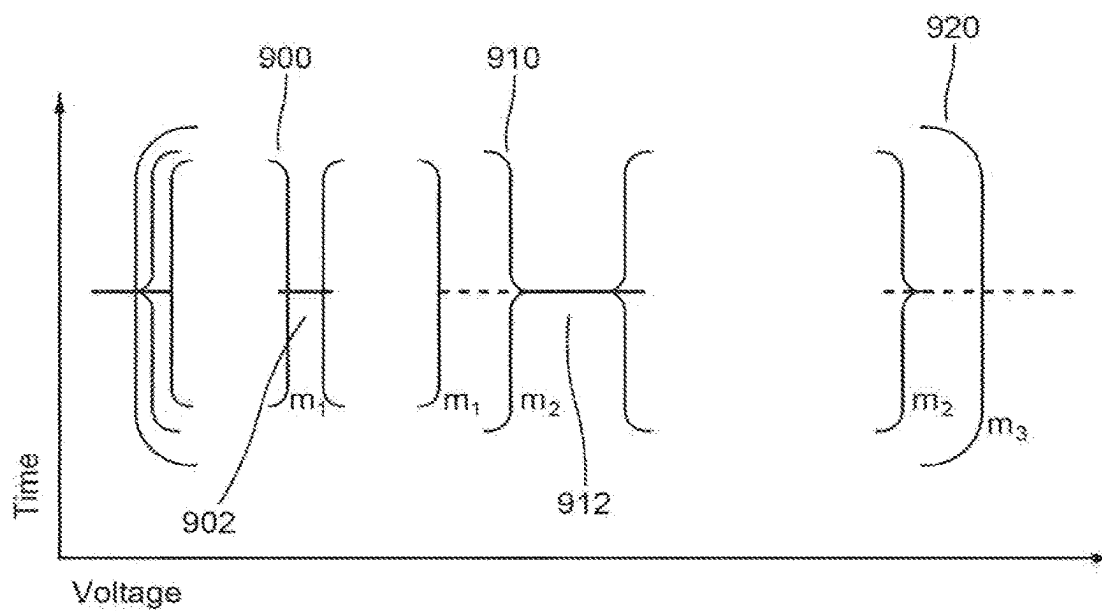


FIG. 9

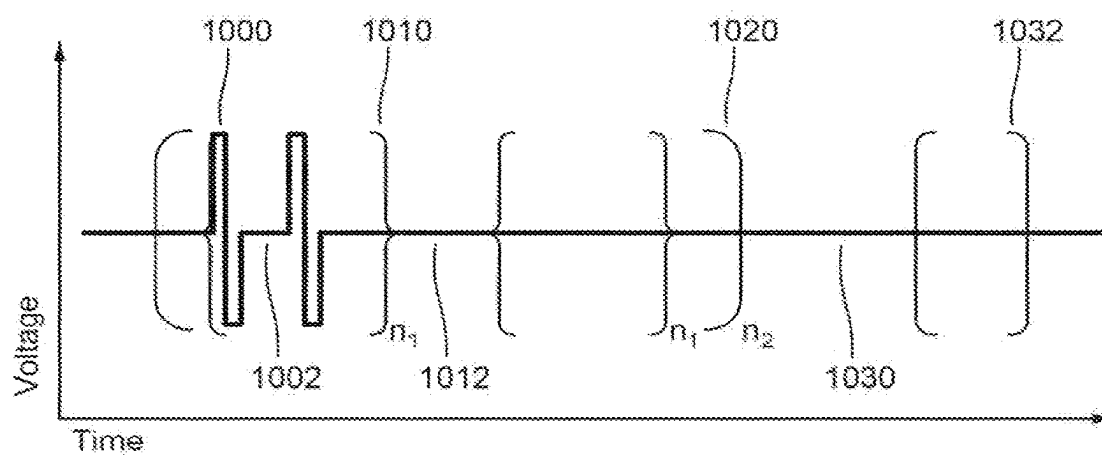


FIG. 10

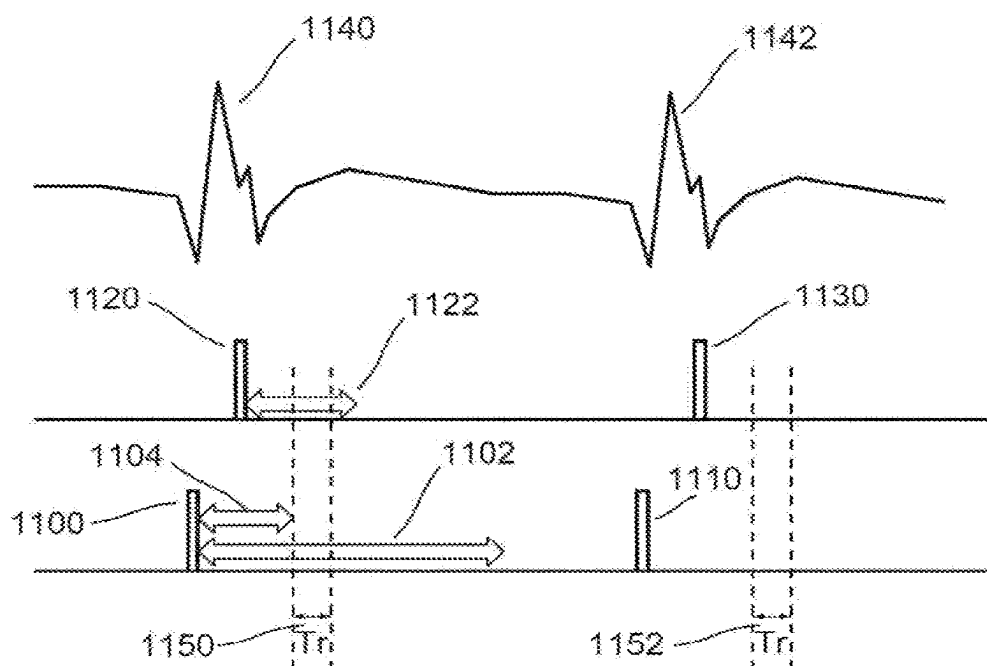
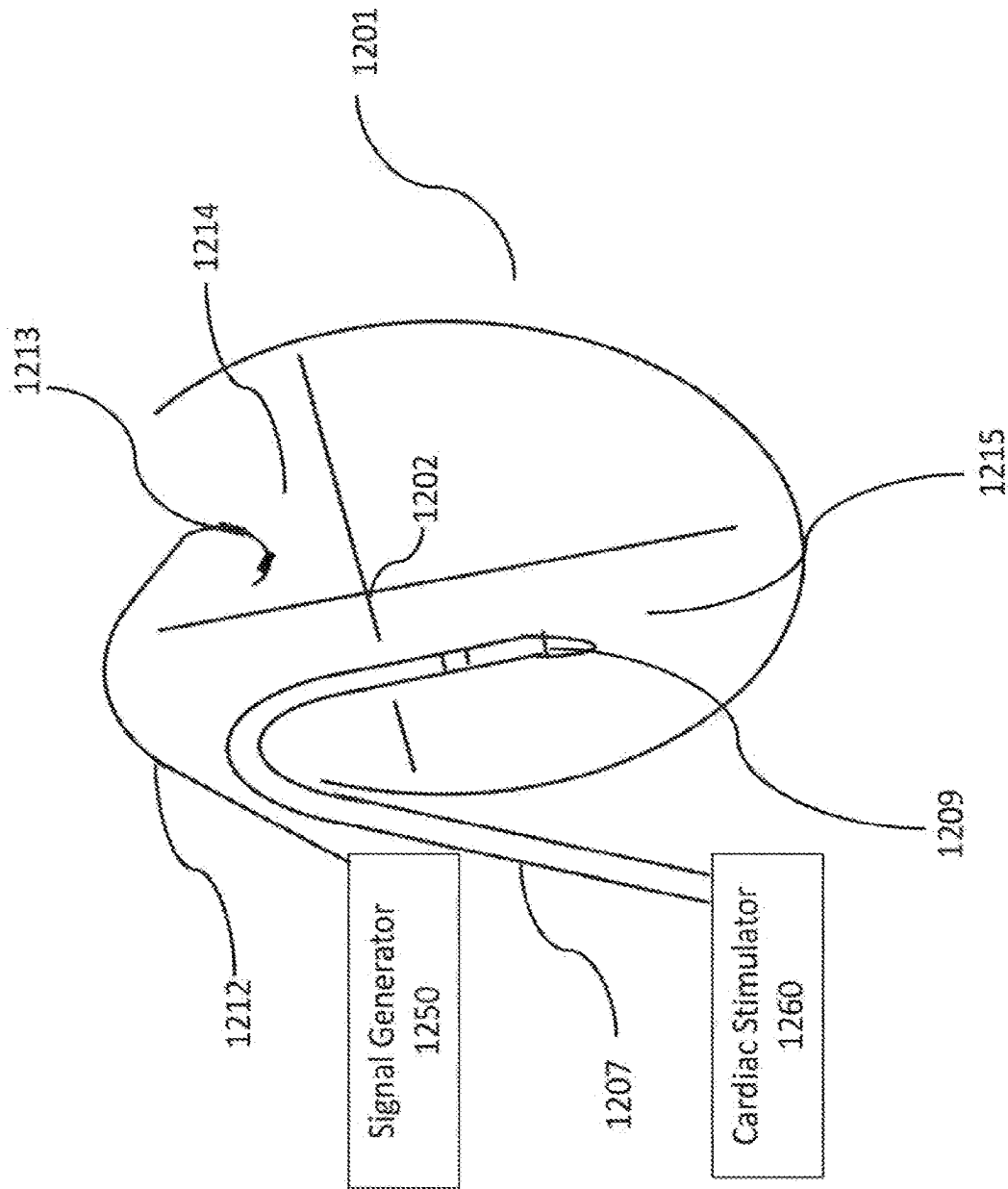


FIG. 11



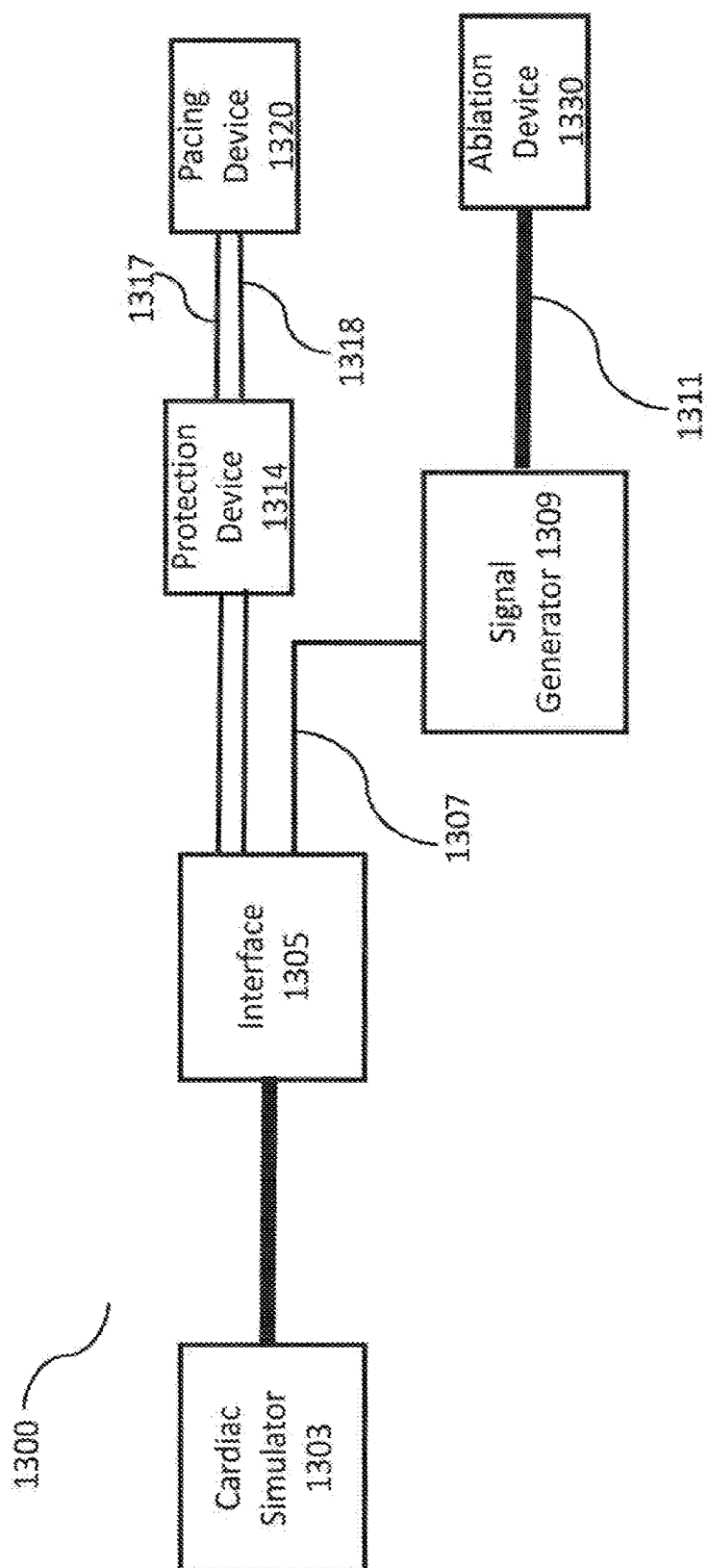


FIG. 13

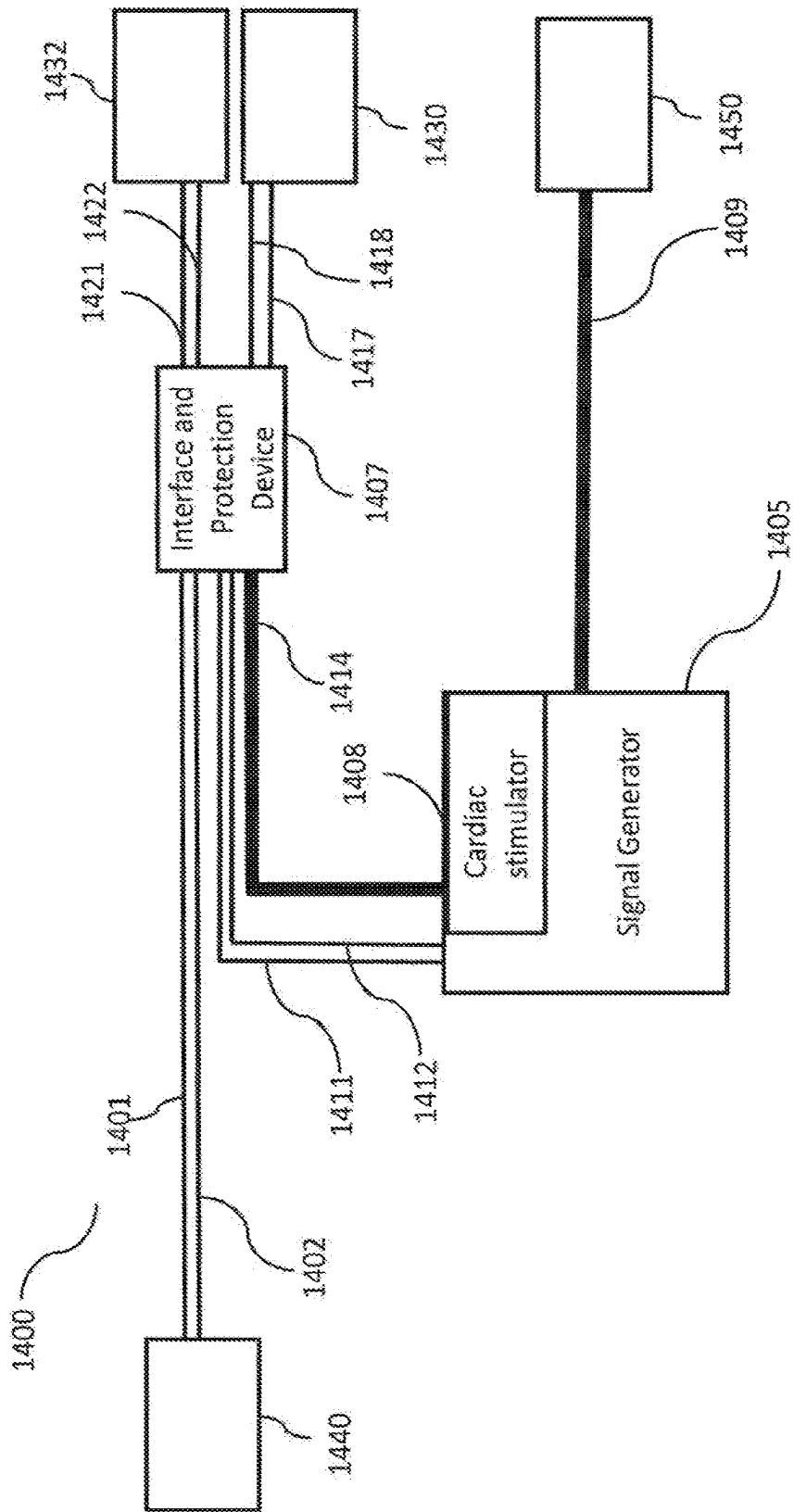


FIG. 14

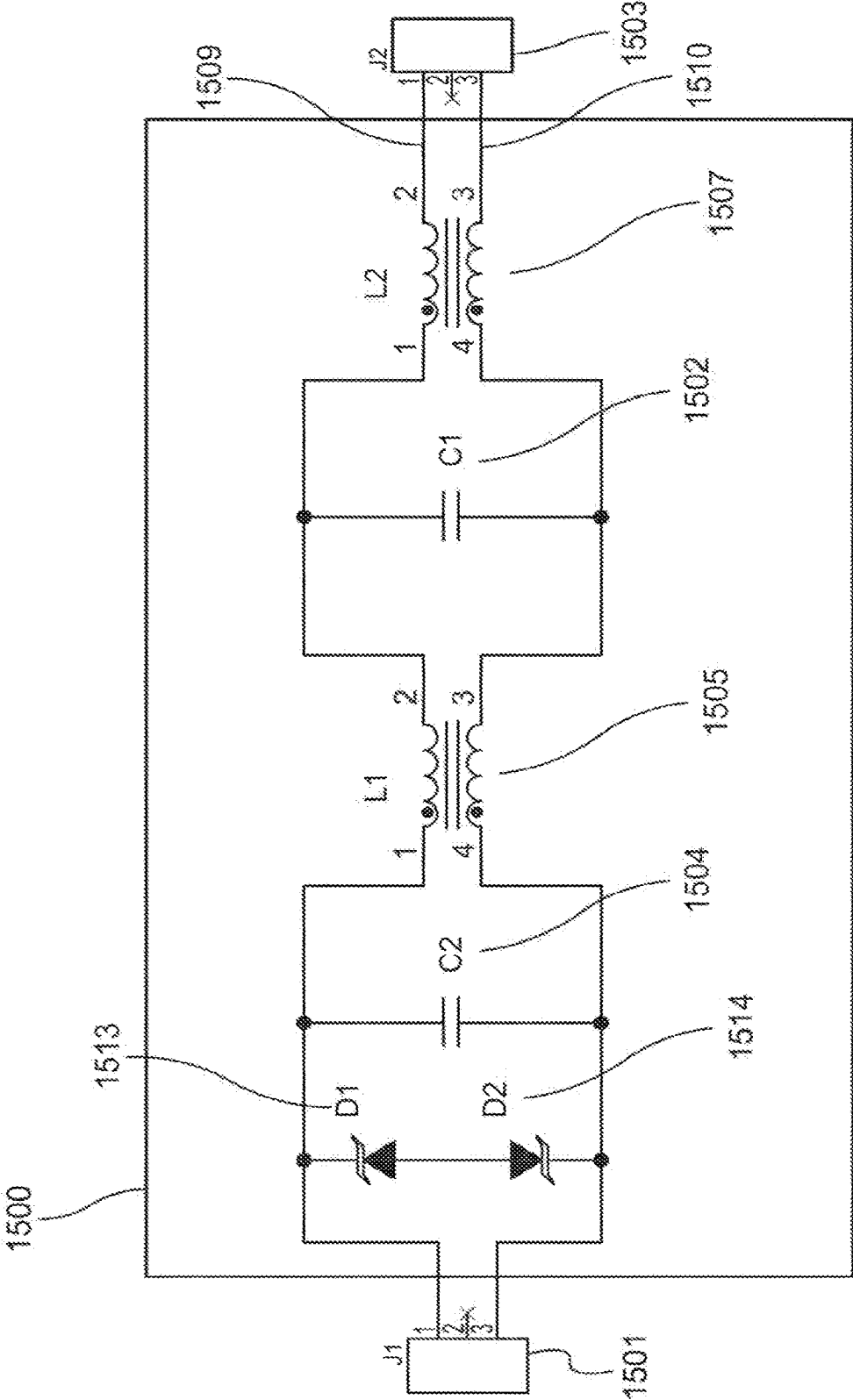


FIG. 15

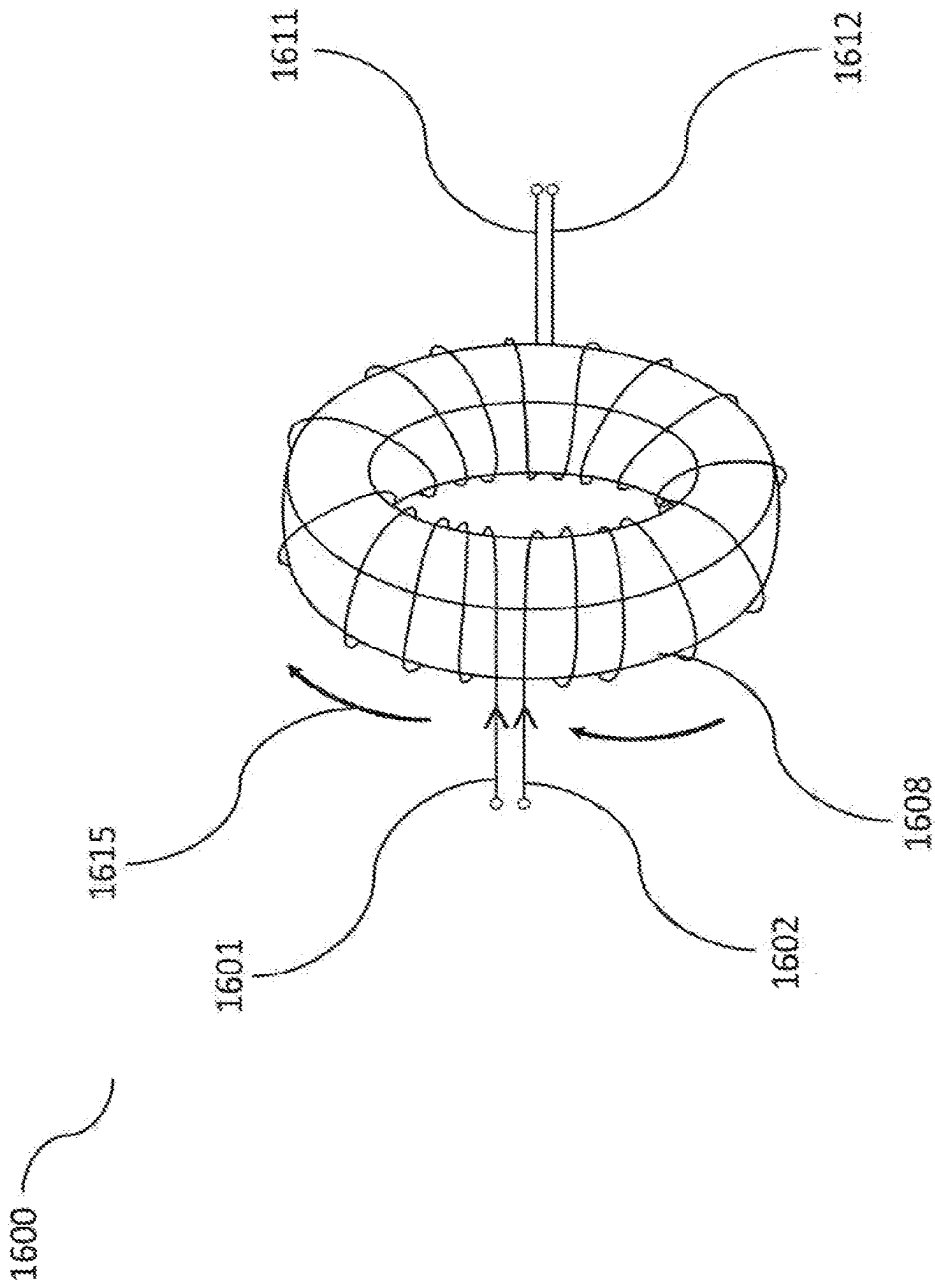


FIG. 16



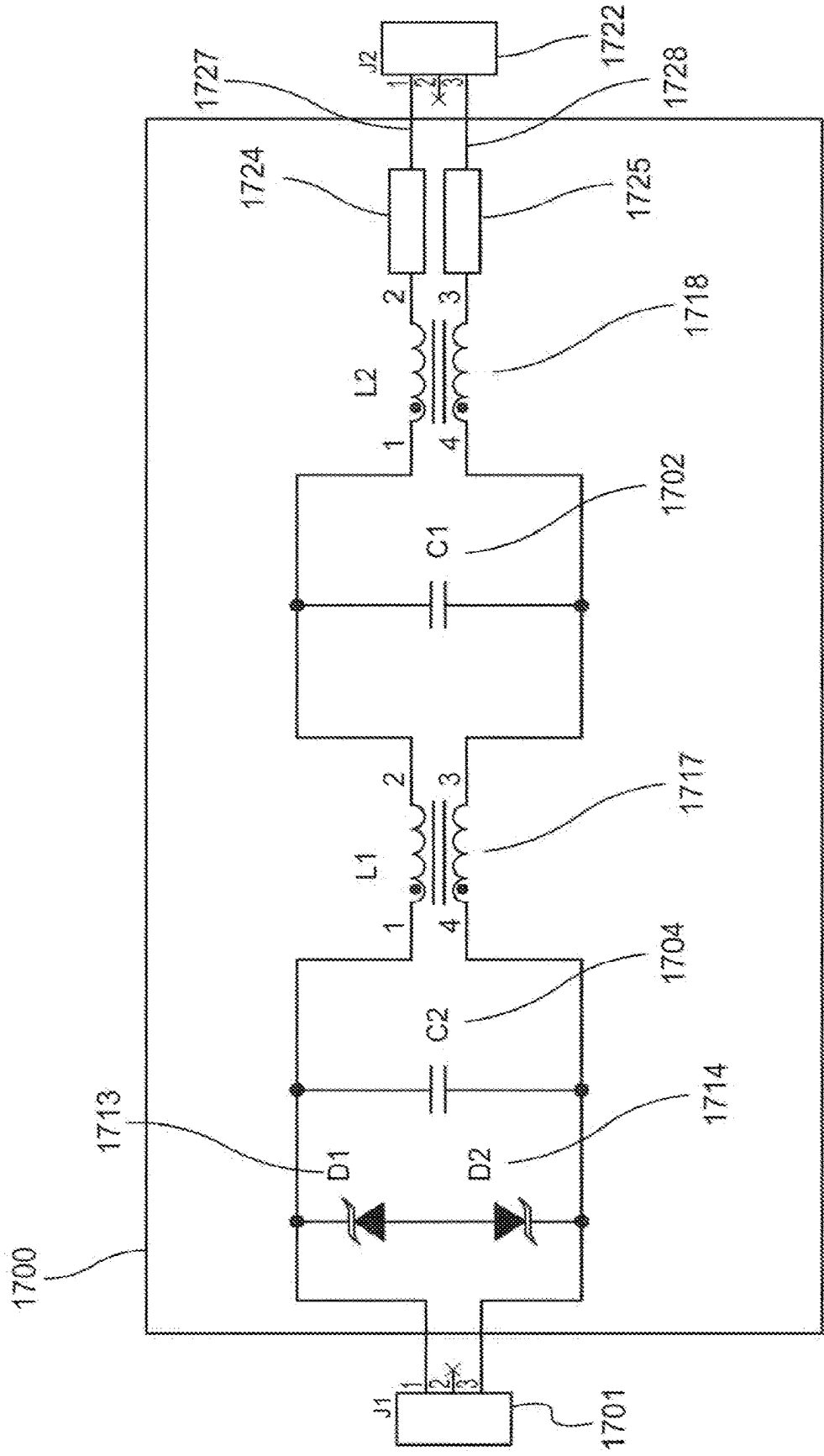


FIG. 17

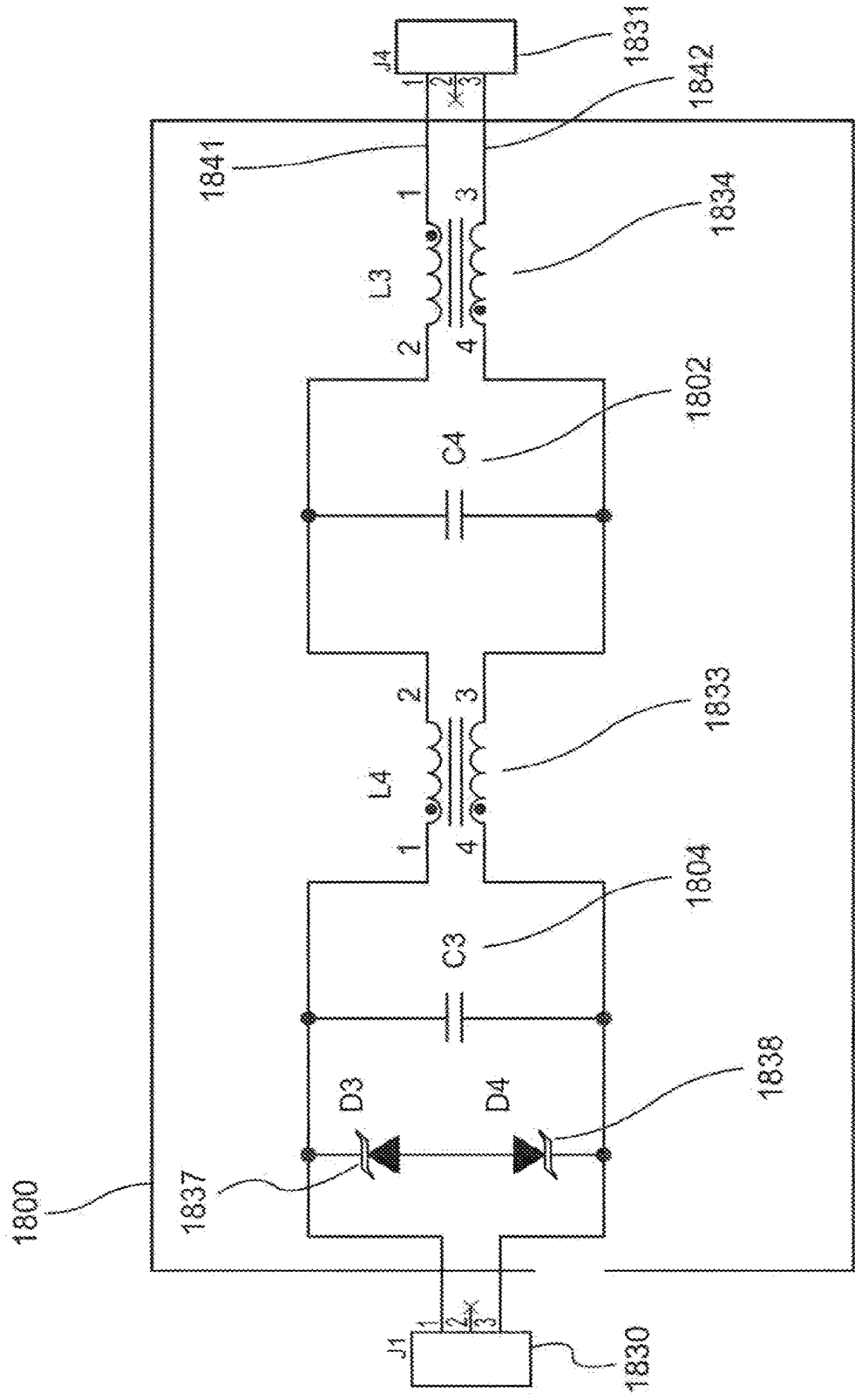


FIG. 18

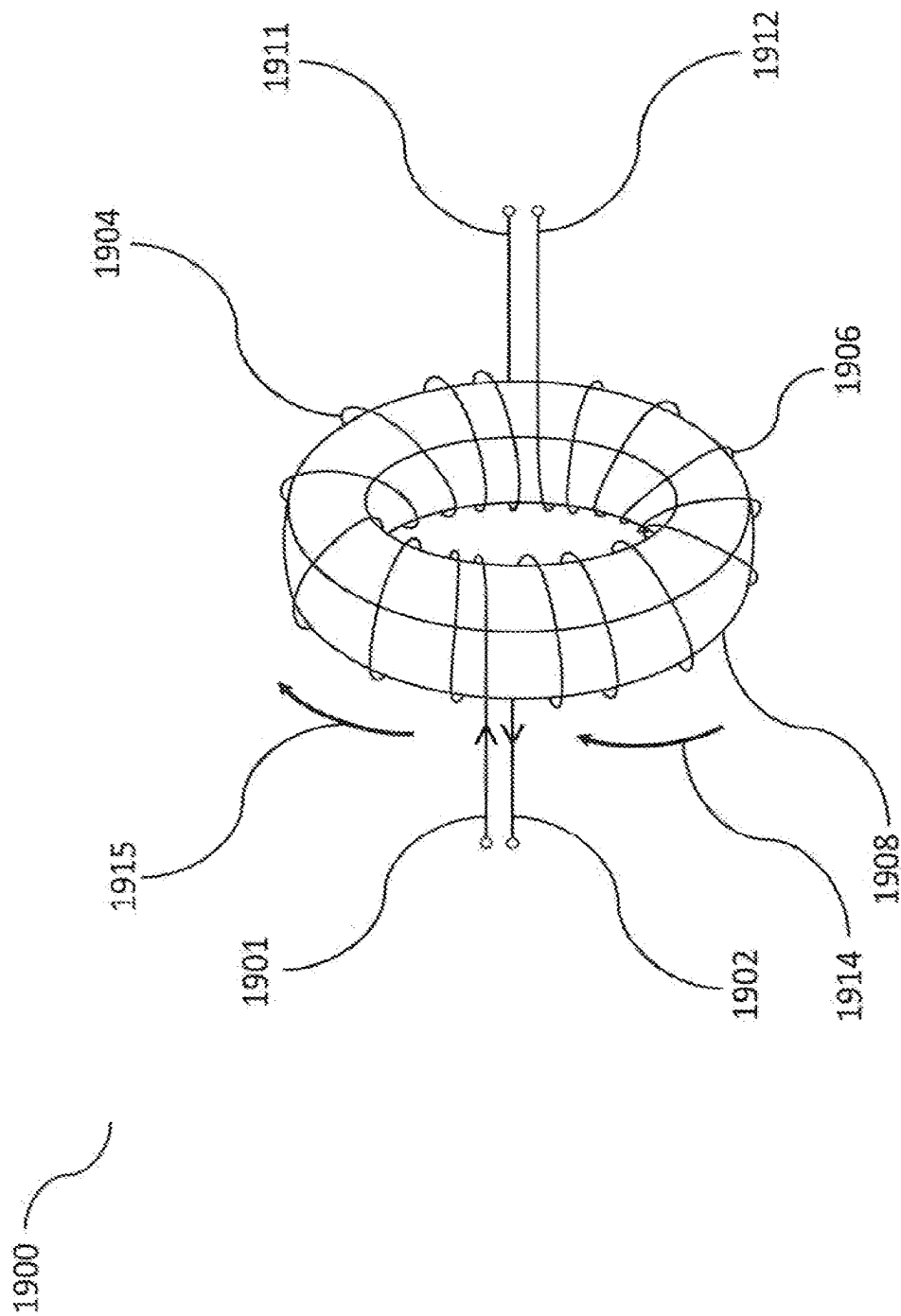


FIG. 19

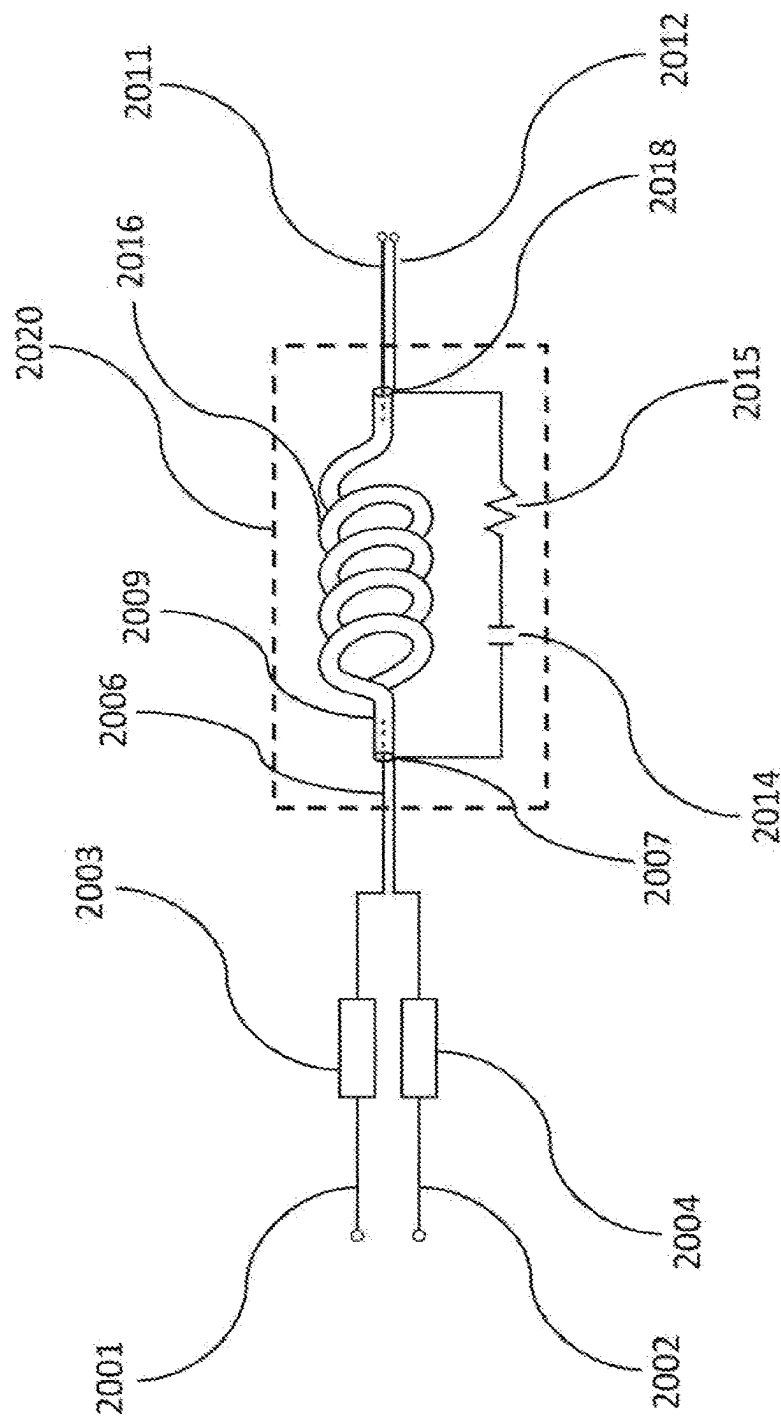


FIG. 20A

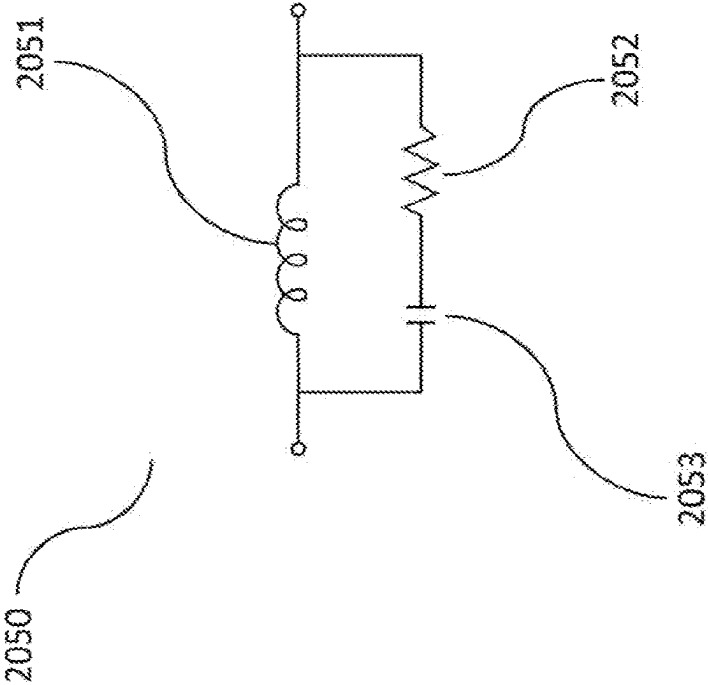


FIG. 20B

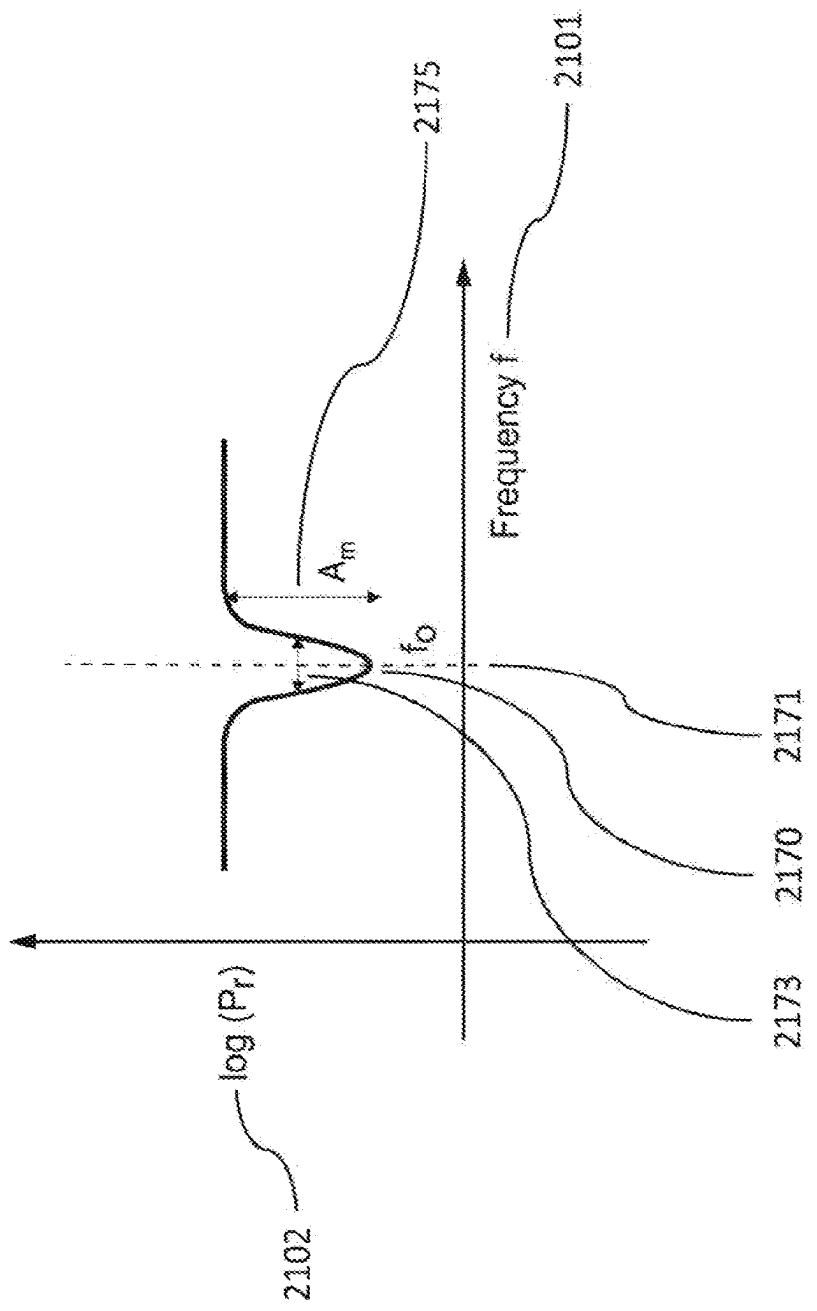


FIG. 21

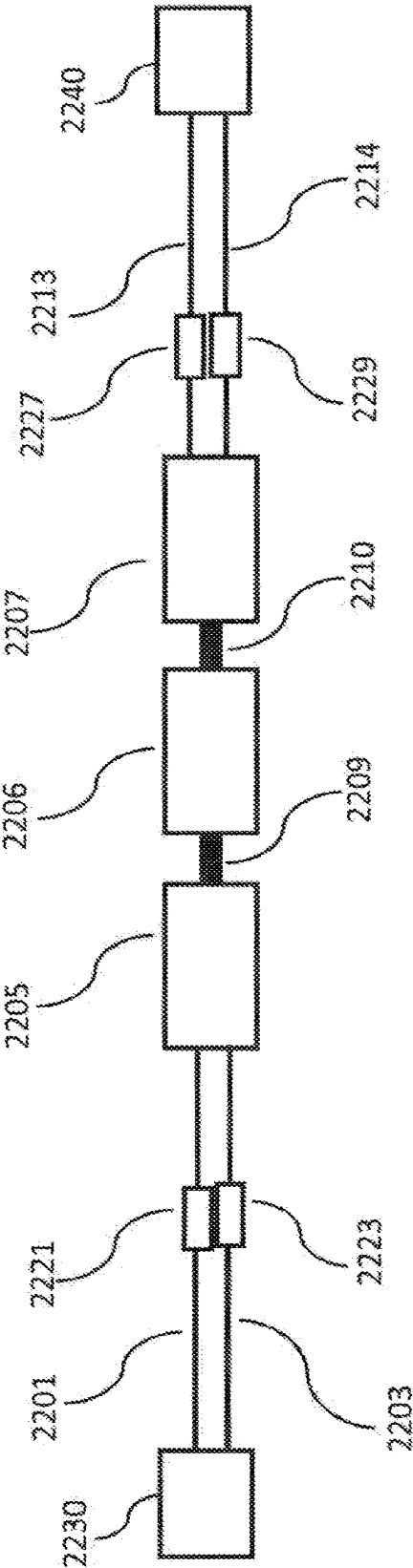


FIG. 22

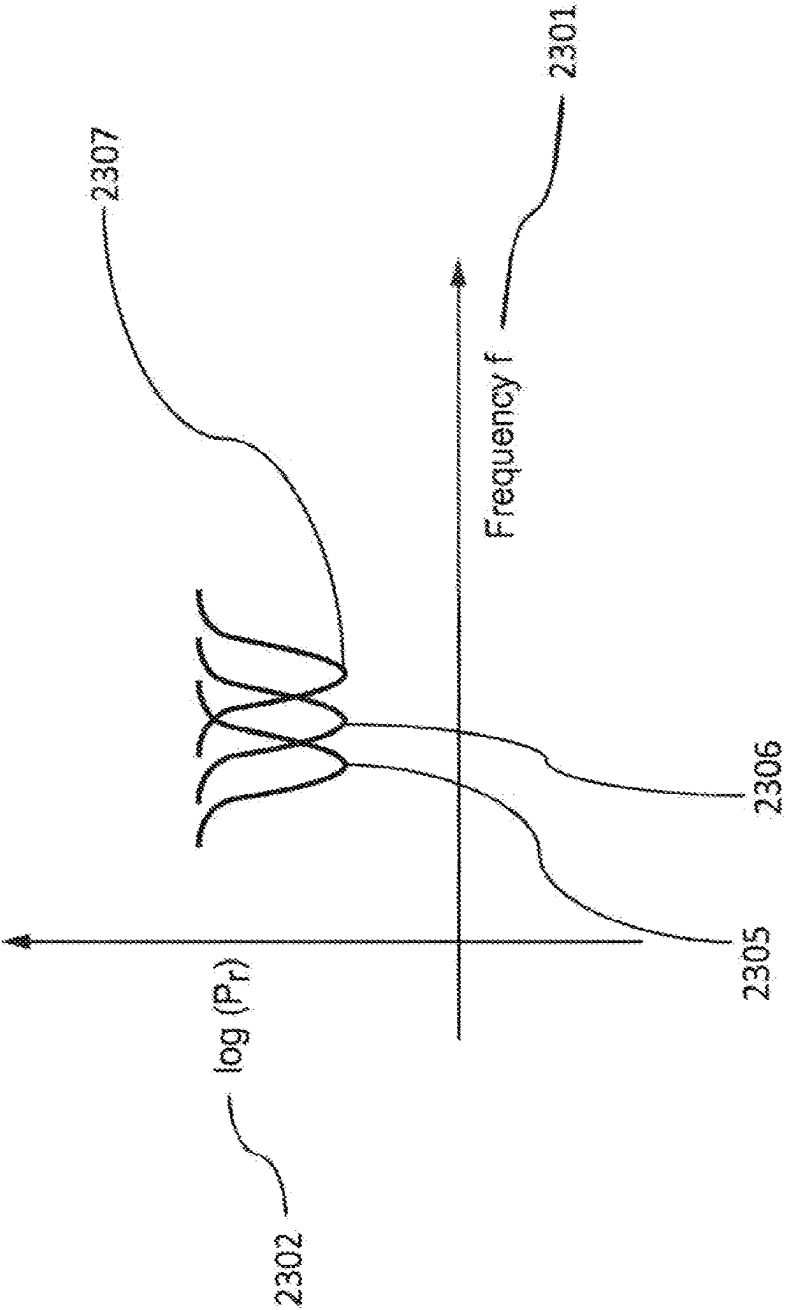


FIG. 23



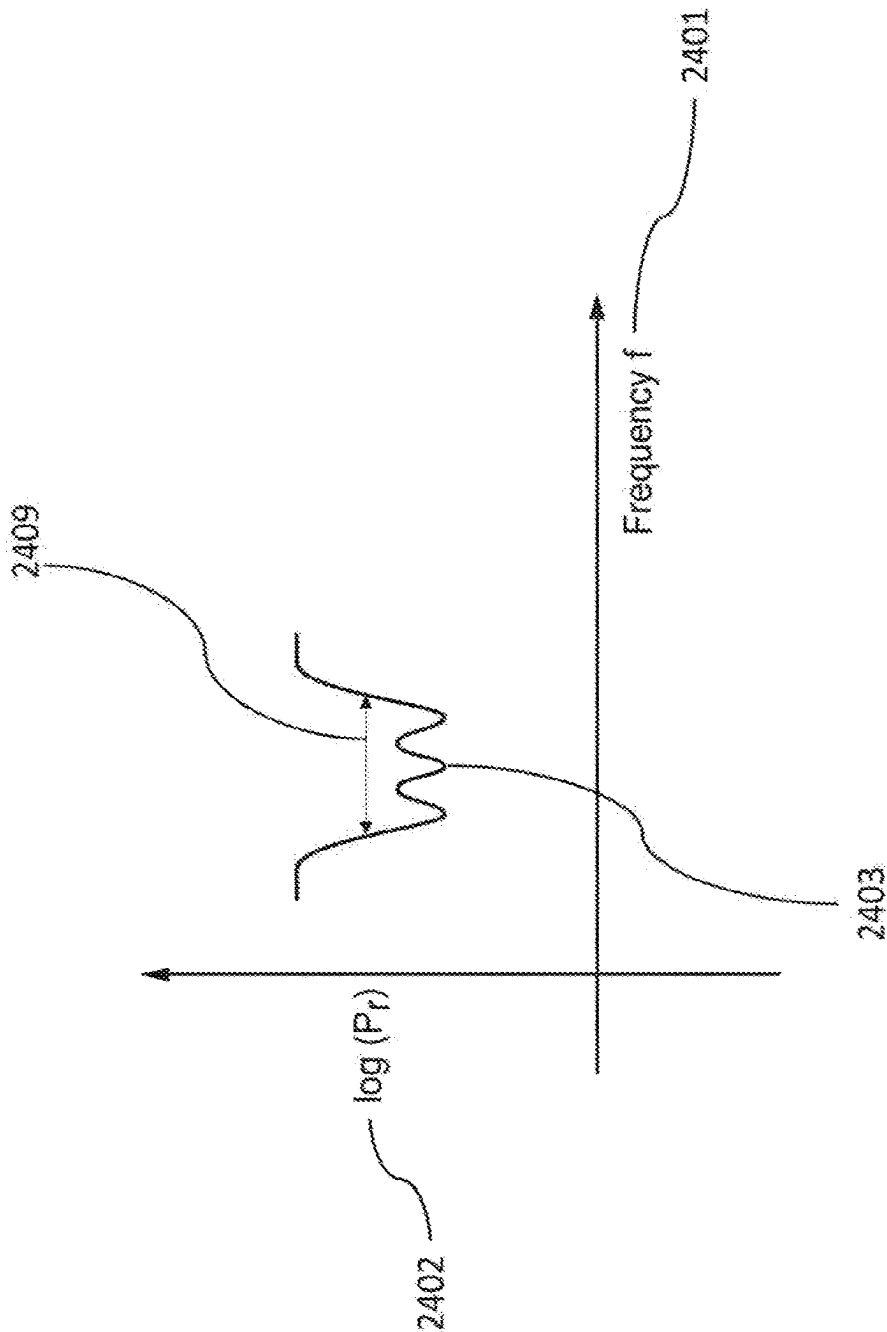


FIG. 24

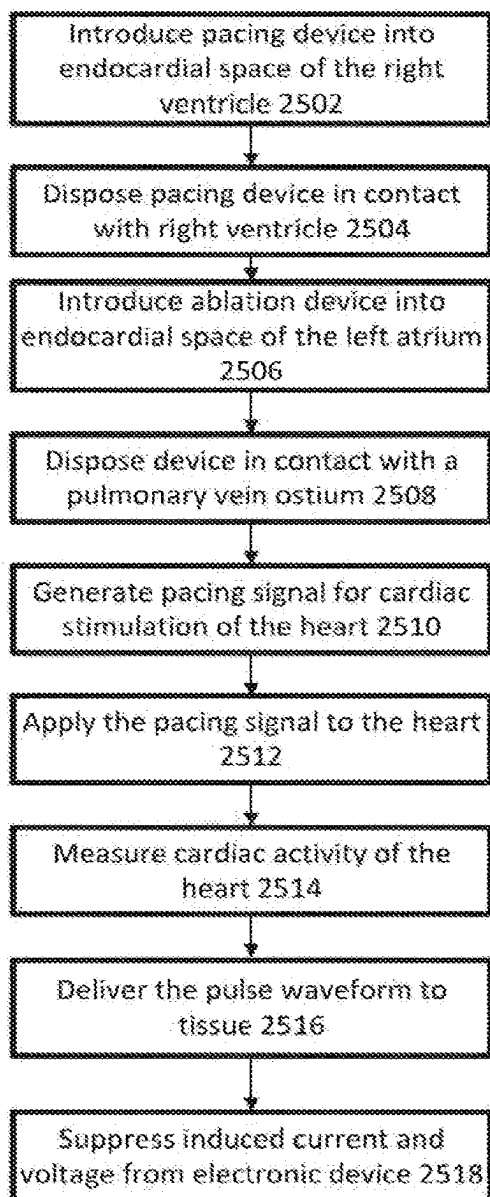
2500

FIG. 25

2600

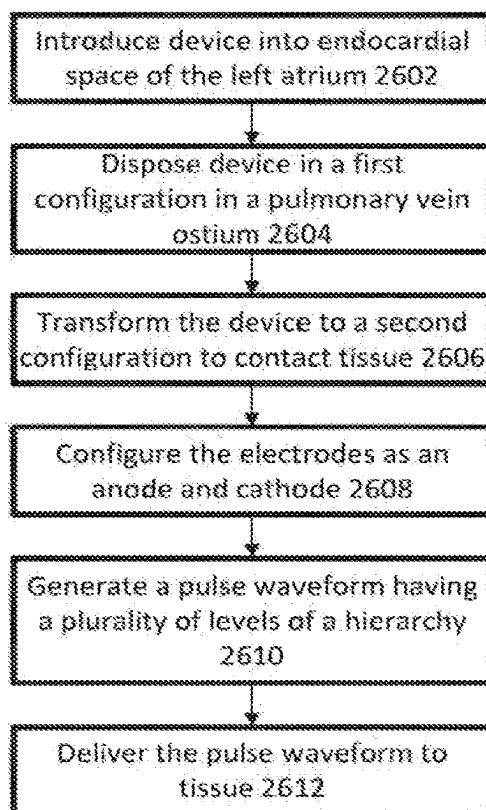


FIG. 26

# SYSTEMS, APPARATUSES, AND METHODS FOR FILTERING HIGH VOLTAGE NOISE INDUCED BY PULSED ELECTRIC FIELD ABLATION

## CROSS-REFERENCE TO RELATED APPLICATIONS

**[0001]** This application is a continuation of U.S. patent application Ser. No. 17/340,745, filed on Jun. 7, 2021, which is a continuation of U.S. patent application Ser. No. 17/091,289, filed on Nov. 6, 2020, now issued as U.S. Pat. No. 11,033,236, which is a continuation International Application No. PCT/US2019/030922, filed on May 6, 2019, which claims the benefit of U.S. Provisional Application No. 62/667,887, filed on May 7, 2018, the entire disclosure of each of which is incorporated herein by reference in its entirety.

## BACKGROUND

**[0002]** The generation of pulsed electric fields for tissue therapeutics has moved from the laboratory to clinical applications over the past two decades, while the effects of brief pulses of high voltages and large electric fields on tissue have been investigated for the past forty years or more. Application of brief high direct current (DC) voltages to tissue may generate locally high electric fields typically in the range of hundreds of volts per centimeter that disrupt cell membranes by generating pores in the cell membrane. While the precise mechanism of this electrically-driven pore generation or electroporation continues to be studied, it is thought that the application of relatively brief and large electric fields generates instabilities in the lipid bilayers in cell membranes, causing the occurrence of a distribution of local gaps or pores in the cell membrane. This electroporation may be irreversible if the applied electric field at the membrane is larger than a threshold value such that the pores do not close and remain open, thereby permitting exchange of biomolecular material across the membrane leading to necrosis and/or apoptosis (cell death). Subsequently, the surrounding tissue may heal naturally. High DC voltage pulses, however, may cause complications (e.g., ventricular fibrillation) if delivered during certain periods of cardiac activity.

**[0003]** For example, to avoid the risk of inducing arrhythmias, high voltage pulses may be applied in synchrony with a cardiac cycle of a subject. For example, high voltage pulses can be applied during specific periods of the cardiac cycle. In some applications, a cardiac stimulator can be used to stimulate the cardiac chamber(s) of a subject to establish periodicity of electrocardiogram (ECG) activity of the heart. Other devices, e.g., sensing and/or mapping systems, can also be used to monitor a subject's cardiac cycle. When these devices are used during a pulsed electric field ablation procedure, however, they can become exposed to high voltages. Such exposure can result in induced currents that can disrupt the operation of the devices. Accordingly, it can be desirable to have systems, apparatuses, and methods for addressing this issue.

## SUMMARY

**[0004]** Described herein are systems, devices, and methods for protecting electronic circuitry, devices, and/or other components from induced currents during pulsed electric

field ablation procedures. In some embodiments, the ablation devices used in these systems may be deployed epicardially or endocardially in cardiac applications. The pulsed waveforms may include predetermined parameters or may be automatically generated by a signal generator.

**[0005]** In some embodiments, a system may comprise a set of electrodes disposable near tissue of a subject. A signal generator may be configured to generate a voltage pulse waveform, the signal generator coupleable to the set of electrodes and configured to deliver the voltage pulse waveform to the set of electrodes to ablate the tissue. The set of electrodes may be configured to induce currents in a first electronic device disposed near the cardiac tissue in response to receiving the voltage pulse waveform. A protection device may be coupleable between the first electronic device and a second electronic device. The protection device may be configured to reduce the currents induced in the first electronic device.

**[0006]** In some embodiments, the first electronic device is a pacing device including a set of leads, and the second electronic device is a cardiac stimulator configured to deliver pacing signals to a pacing device. In some embodiments, the currents induced in the first electronic device include one or more common mode currents or differential mode currents. The protection device may include one or more transformers configured to reduce the common mode currents or the differential mode currents.

**[0007]** In some embodiments, each transformer of the one or more transformers includes a toroid core, a first winding around a first portion of the toroid core, and a second winding around a second portion of the toroid core. In some embodiments, the protection device includes one or more capacitors configured to shunt currents above a predetermined frequency to reduce the currents induced in the first electronic device. In some embodiments, the protection device includes one or more diodes configured to shunt high voltages away from the second electronic device. In some embodiments, the protection device includes one or more inductions configured to reduce alternating currents induced in the first electronic device. In some of these embodiments, the one or more inductors includes a first inductor coupled to a first lead of the first electronic device and a second inductor coupled to a second lead of the first electronic device. The first and second inductors may be configured to reduce alternating currents induced in the first and second leads.

**[0008]** In some embodiments, the protection device includes first and second capacitors each configured to shunt voltages above a predetermined frequency, a first transformer coupled between the first electronic device and the first capacitor, a second transformer coupled between the first capacitor and the second capacitor. The first and second may be configured to reduce common mode currents or differential mode currents induced in the first electronic device. A set of diodes may be arranged in parallel with the second capacitor and coupled to the second electronic device. The set of diodes may be configured to shunt high voltages away from the second electronic device. In some of these embodiments, the first and second transformers each are common mode transformers configured to reduce common mode currents induced in the first electronic device. In some of these embodiments, one of the first and second transformers is a differential mode transformer configured to reduce differential mode currents induced in the first elec-

tronic device. The other of the first and second transformers is a common mode transformer configured to reduce common mode currents induced in the first electronic device.

**[0009]** In some embodiments, the protection device includes one or more balun circuits configured to reduce common mode currents induced in the first electronic device over a predetermined frequency range. In some of these embodiments, the one or more balun circuits includes a plurality of balun circuits each configured to reduce common mode currents induced in the first electronic device over a predetermined frequency range of a set of predetermined frequency ranges. Each predetermined frequency range of the set of predetermined frequency ranges at least partially overlapping at least one other predetermined frequency range of the set of predetermined frequency ranges. In some of these embodiments, the protection device includes one or more inductors configured to reduce alternating currents induced in the first electronic device. The one or more balun circuits may be couple between the one or more inductors an the second electronic device.

**[0010]** In some embodiments, at least one balun circuit of the one or more balun circuits may include an inductor in parallel with a capacitor and a resistor. In some embodiments, at least one balun circuit of the one or more balun circuits includes a coaxial cable winding including first and second conductors. The first conductor may be coupled to a first lead of the first electronic device and the second conductor may be coupled to a second lead of the first electronic device. In some embodiments, the one or more balun circuits includes a plurality of balun circuits connected in series. In some embodiments, the protection device is further coupled between the first electronic device and the signal generator. In some embodiments, the protection device is integrated into at least one of the signal generator and the second electronic device.

**[0011]** In some embodiments, an apparatus may comprise a protection device coupleable between a first electronic device and a second electronic device, the first electronic device disposable near cardiac tissue of a subject such that currents can be induced in the first electronic device by voltage pulse waveforms delivered to a set of electrodes near tissue. The protection device may include a set of capacitors each configured to shunt currents above a predetermined frequency to reduce the currents induced in the first electronic device. A set of transformers may each be configured to reduce common mode currents or differential mode currents of the currents induced in the first electronic device. A set of diodes may be configured to shunt high voltage away from the second electronic device.

**[0012]** In some embodiments, a signal generator may be configured to generate the voltage pulse waveform. The signal generator may be coupled to the set of electrodes and configured to deliver the voltage pulse waveform to the set of electrodes. A cardiac stimulator may be configured to deliver pacing signals to the first electronic device, the cardiac stimulator being the second electronic device. In some embodiments, the first electronic device may be a pacing device including a set of leads, and the second electronic device may be a cardiac stimulator configured to deliver pacing signals to the pacing device.

**[0013]** In some embodiments, each transformer of the one or more transformers includes a toroid core, a first winding around a first portion of the toroid core, and a second winding around a second portion of the toroid core. In some

embodiments, the toroid core of each transformer of the one or more transformers includes a set of laminations having an aspect ratio of less than or equal to five. In some embodiments, the toroid core of each transformer of the one or more transformers defines a central axis with the first winding of that transformer winding around the central axis of the toroid core in a first direction and the second winding of that transformer winding around the central axis in a second direction opposite the first direction. In some embodiments, an outer radius of the toroid core of each transformer of the one or more transformers is between about 4 cm and 10 cm, and an inner radius of the toroid core of each transformer of the one or more transformers is between about 2 cm and about 9 cm. In some embodiments, a thickness of the toroid core of each transformer of the one or more transformers may be between about 1 cm and about 6 cm.

**[0014]** In some embodiments, each transformer of the one or more transformers may include an inductance of at least about 1 millihenry and a resistance of at least about 500 Ohms. In some embodiments, each diode of the set of diodes may be a Zener diode. In some embodiments, the set of diodes includes first and second diodes arranged in series facing opposite directions. In some embodiments, the set of diodes may be arranged in parallel to at least one capacitor of the set of capacitors.

**[0015]** In some embodiments, the set of capacitors includes first and second capacitors. The set of transformers includes a first transformer coupled between the first electronic device and the first capacitor. A second transformer may be coupled between the first capacitor and the second capacitor. The set of diodes includes first and second diode coupled to the second electronic device. In some of these embodiments, the first and second transformers may each be common mode transformers configured to reduce common mode currents of the currents induced in the first electronic device. In some embodiments, one of the first and second transformers may be a differential mode transformer configured to reduce differential mode currents of the currents induced in the first electronic device, and the other of the first and second transformers is a common mode transformer configured to reduce common mode currents of the currents induced in the first electronic device. In some embodiments, the protection device may include one or more inductors configured to reduce alternating currents induced in the first electronic device.

**[0016]** In some embodiments, an apparatus may comprise a protection device coupleable between a first electronic device and a second electronic device, the first electronic device disposable near tissue of a subject such that currents can be induced in the first electronic device by voltage pulse waveforms delivered to a set of electrodes near the cardiac tissue. The protection device may include a set of balun circuits connected in series and collectively configured to reduce the currents induced in the first electronic device over a set of predetermined frequency ranges.

**[0017]** In some embodiments, a signal generator may be configured to generate the voltage pulse waveform, the signal generator coupled to the set of electrodes and configured to deliver the voltage pulse waveform to the set of electrodes. In some embodiments, a cardiac stimulator may be configured to deliver pacing signals to the first electronic device, the cardiac stimulator being the second electronic device. In some embodiments, the first electronic device may be a pacing device including a set of leads, and the

second electronic device is a cardiac stimulator configured to deliver pacing signals to the pacing device. In some embodiments, each predetermined frequency range of the set of predetermined frequency ranges may at least partially overlap at least one other predetermined frequency range of the set of predetermined frequency ranges. In some embodiments, each predetermined frequency range of the set of predetermined frequency ranges may have a resonance peak associated with a balun circuit of the set of balun circuits.

**[0018]** In some embodiments, the protection device may further include one or more inductors configured to reduce an alternating current of the currents induced in the first electronic device. In some of these embodiments, the set of balun circuits may be coupled between the one or more inductors and the first electronic device. In some of these embodiments, the one or more inductors may include first and second sets of inductors. The first set of inductors may be coupled between the set of balun circuits and the second electronic device.

**[0019]** In some embodiments, at least one balun circuit of the set of balun circuits may include an inductor in parallel with a capacitor and a resistor. In some embodiments, at least one balun circuit of the set of balun circuits includes a coaxial cable winding including first and second conductors. The first conductor may be coupled to a first lead of the first electronic device and the second conductor coupled to a second lead of the first electronic device.

**[0020]** In some embodiments, a method may comprise delivering, using a pacing device positioned near cardiac tissue of a heart, pacing signals to the heart; delivering, using a signal generator, voltage pulse waveforms to an ablation device positioned near the cardiac tissue to ablate the cardiac tissue; inducing, in response to delivering the voltage pulse waveform, currents in a set of first electronic devices positioned near the cardiac tissue, the set of first electronic devices including the pacing device; reducing, using a protection device coupled between the set of first electronic devices and a second electronic device, the currents induced in the set of first electronic devices.

**[0021]** In some embodiments, the second electronic device may be a cardiac stimulator configured to generate the pacing signals. In some embodiments, the second electronic device may be an electrocardiogram (ECG) recording system. In some embodiments, the method may further comprise reducing, using the protection device, the current induced in the set of first electronic devices includes reducing, using one or more transformers or capacitors of the protection device, common mode currents or differential mode currents of the currents induced in the set of first electronic devices. In some embodiments, reducing, using the protection device, the current induced in the set of first electronic devices includes shunting, using a set of diodes of the protection device, high voltages away from the second electronic device. In some embodiments, reducing, using the protection device, the current induced in the set of first electronic devices includes reducing, using one or more inductors of balun circuits of the protection device, alternating currents having frequencies within a predetermined range of frequencies of the currents induced in the set of first electronic devices. In some embodiments, reducing, using the protection device, the current induced in the set of first electronic devices includes reducing, using one or more inductors of balun circuits of the protection device, alter-

nating currents having frequencies within a predetermined range of frequencies of the currents induced in the set of first electronic devices.

**[0022]** The pulse waveforms of the present invention may be hierarchical in organization and have a nested structure. Further, they involve a sequence of groupings with a variety of associated timescales. Furthermore, the associated timescales and pulse widths, and the numbers of pulses and hierarchical groupings, may be selected so as to satisfy one or more of a set of Diophantine inequalities involving the frequency of cardiac pacing.

#### BRIEF DESCRIPTION OF DRAWINGS

**[0023]** FIG. 1 is a block diagram of an electroporation system, according to embodiments.

**[0024]** FIG. 2 is a circuit diagram of a signal generator, according to embodiments.

**[0025]** FIG. 3 is a circuit diagram of a signal generator, according to embodiments.

**[0026]** FIG. 4A is a side view of an ablation catheter, according to embodiments.

**[0027]** FIG. 4B is a side view of an ablation catheter, according to embodiments.

**[0028]** FIG. 5 is a partial close-up view of a central portion of an ablation catheter, according to embodiments.

**[0029]** FIG. 6 illustrates a method for tissue ablation, according to embodiments.

**[0030]** FIG. 7 is an example waveform showing a sequence of voltage pulses with a pulse width defined for each pulse, according to embodiments.

**[0031]** FIG. 8 schematically illustrates a hierarchy of pulses showing pulse widths, intervals between pulses, and groupings of pulses, according to embodiments.

**[0032]** FIG. 9 provides a schematic illustration of a nested hierarchy of monophasic pulses displaying different levels of nest hierarchy, according to embodiments.

**[0033]** FIG. 10 is a schematic illustration of a nested hierarchy of biphasic pulses displaying different levels of nested hierarchy, according to embodiments.

**[0034]** FIG. 11 illustrates schematically a time sequence of electrocardiograms and cardiac pacing signals together with atrial and ventricular refractory time periods and indicating a time window for irreversible electroporation ablation, according to embodiments.

**[0035]** FIG. 12 is a schematic illustration of an ablation system coupled to a patient's heart, according to embodiments.

**[0036]** FIG. 13 is a block diagram illustrating an ablation system including a protection device, according to some embodiments.

**[0037]** FIG. 14 is a block diagram illustrating an ablation system including a protection device, according to other embodiments.

**[0038]** FIG. 15 is a circuit diagram of a protection device in a non-tuned common mode, according to embodiments.

**[0039]** FIG. 16 is a perspective view of a common mode transformer, according to embodiments.

**[0040]** FIG. 17 is a circuit diagram of a protection device in a non-tuned common mode, according to embodiments.

**[0041]** FIG. 18 is a circuit diagram of a protection device in a non-tuned common-differential mode, according to embodiments.

**[0042]** FIG. 19 is a perspective view of a differential mode transformer, according to embodiments.

[0043] FIGS. 20A-20B are circuit diagrams of a tunable protection device, according to embodiments.

[0044] FIG. 21 is a plot of a resonance peak and frequency of the tunable protection device of FIGS. 20A-20B, according to embodiments.

[0045] FIG. 22 is a circuit diagram of a series of tunable protection devices, according to embodiments.

[0046] FIG. 23 is a plot of a set of resonance peaks and frequency of the tunable protection devices illustrated in FIG. 22, according to embodiments.

[0047] FIG. 24 is a plot of a set of resonance peaks and frequency of the tunable protection devices illustrated in FIG. 22, according to embodiments.

[0048] FIG. 25 illustrates a method for protecting electronic components from high voltage signals, according to embodiments.

[0049] FIG. 26 illustrates a method for tissue ablation, according to embodiments.

#### DETAILED DESCRIPTION

[0050] Described herein are systems, devices, and methods for protecting circuits from high power noise induced during pulsed electric field ablation, e.g., pulsed electric fields to ablate tissue by irreversible electroporation. Generally, pulsed electric field systems may be used to generate large electric field magnitudes (e.g., electric fields of about 200 V/cm and above) at desired regions of interest to ablate tissue. As an example for illustrative purposes, pulsed electric field systems for the treatment of atrial fibrillation via irreversible electroporation in synchronicity with a cardiac cycle are described herein. Synchronizing ablation energy delivery with the cardiac cycle may reduce the risk of induced arrhythmias such as atrial and/or ventricular fibrillation. For example, a cardiac stimulator may be used to deliver pacing pulses to one or more cardiac chambers such that the cardiac rhythm of a patient synchronizes with the pacing pulse. However, high voltage pulse waveforms applied to tissue, for example cardiac tissue, may couple to the pacing device and induce currents in one or more of the pacing device and devices coupled thereto. This noise may lead to stimulator device malfunction, and/or device damage.

[0051] An ablation system as described herein may include a signal generator and a processor configured to apply one or more voltage pulse waveforms to a selected set of electrodes of an ablation device to deliver energy to a region of interest (e.g., ablation energy to tissue in a pulmonary vein ostium). The pulse waveforms disclosed herein may aid in therapeutic treatment of a variety of cardiac arrhythmias (e.g., atrial fibrillation). The system may further include a cardiac stimulator and pacing device used to electrically pace the heart and/or measure cardiac activity to ensure pacing capture to establish periodicity and predictability of the cardiac cycle.

[0052] The cardiac stimulator may synchronize the generation of the pulse waveform to a paced heartbeat in order to reduce unintended tissue damage. For example, a time window within a refractory period of the periodic cardiac cycle may be selected for voltage pulse waveform delivery. Thus, voltage pulse waveforms may be delivered in the refractory period of the cardiac cycle so as to avoid disruption of the sinus rhythm of the heart. In some embodiments, the ablation device may transform into different configurations (e.g., compact and expanded) to position the device

within an endocardial space. In some embodiments, the system may optionally include one or more return electrodes.

[0053] Generally, to ablate tissue, one or more catheters may be advanced in a minimally invasive manner through vasculature to a target location. In a cardiac application, the electrodes through which the ablation pulse waveform is delivered may be disposed on an epicardial device or on an endocardial device. The methods described here may include introducing an ablation device into an endocardial space of an atrium (e.g., a left atrium) of the heart and disposing the device in contact with the pulmonary vein ostium. The ablation pulse waveform may be generated in synchronization with a pacing signal of the heart to avoid disruption of the sinus rhythm of the heart. The pulse waveform may be delivered to one or more electrodes of the device to ablate tissue. The pulse waveform may include hierarchical waveforms to aid in tissue ablation and reduce damage to healthy tissue.

[0054] Generally, the forward and return currents of electrical leads of the pacing device are balanced (e.g., equal in magnitude and opposite in direction) during delivery of pacing pulses. However, electrical coupling of the high voltage ablation energy to the pacing device may induce large and unbalanced currents in a frequency band in the electrical leads of the pacing device. These induced currents may disrupt the operation of the pacing device and/or cardiac stimulator coupled thereto. For example, the large voltage exposure of the pacing device may exceed the common-mode rejection of the cardiac stimulator and interrupt the pacing and/or energy delivery of the system. In some embodiments, the electrical connectors (e.g., wires, cables) coupled between components of the system may receive induced noise due to the high voltage pulse waveforms applied to tissue.

[0055] A protection device may be coupled to the pacing device to suppress induced voltage and currents in the pacing device from the other electronic components (e.g., cardiac stimulator, signal generator) of the ablation system. For example, common mode and differential mode currents induced in a pacing device may be reduced and/or suppressed by a protection device (e.g., filter device). Consequently, components of the system such as the cardiac stimulator may be protected from currents that may be induced in the pacing device by the high voltage pulse waveforms applied by the ablation device. Additionally or alternately, the protection device may further provide active circuit protection.

[0056] The term “electroporation” as used herein refers to the application of an electric field to a cell membrane to change the permeability of the cell membrane to the extracellular environment. The term “reversible electroporation” as used herein refers to the application of an electric field to a cell membrane to temporarily change the permeability of the cell membrane to the extracellular environment. For example, a cell undergoing reversible electroporation may observe the temporary and/or intermittent formation of one or more pores in its cell membrane that close up upon removal of the electric field. The term “irreversible electroporation” as used herein refers to the application of an electric field to a cell membrane to permanently change the permeability of the cell membrane to the extracellular environment. For example, a cell undergoing irreversible elec-

troporation may observe the formation of one or more pores in its cell membrane that persist upon removal of the electric field.

**[0057]** Pulse waveforms for electroporation energy delivery as disclosed herein may enhance the safety, efficiency and effectiveness of energy delivery to tissue by reducing the electric field threshold associated with irreversible electroporation, thus yielding more effective ablative lesions with a reduction in total energy delivered. In some embodiments, the voltage pulse waveforms disclosed herein may be hierarchical and have a nested structure. For example, a pulse waveform may include hierarchical groupings of pulses having associated timescales. In some embodiments, the methods, systems, and devices disclosed herein may comprise one or more of the methods, systems, and apparatuses described in International Application No. PCT/US2016/057664, filed Oct. 19, 2016, and titled “SYSTEMS, APPARATUSES AND METHODS FOR DELIVERY OF ABLATIVE ENERGY TO TISSUE,” the contents of which are hereby incorporated by reference in its entirety.

#### Systems

**[0058]** Disclosed herein are systems and devices configured for suppressing induced currents in connection with tissue ablation via the selective and rapid application of voltage pulse waveforms resulting in irreversible electroporation. Generally, a system for ablating tissue described here may include a cardiac stimulator for generating a cardiac pacing signal delivered by a pacing device to the heart. The cardiac pacing signal is used to synchronize delivery of a pulse waveform generated by a signal generator, and the pulse waveform is delivered using an ablation device having one or more electrodes. As described herein for cardiac applications, the systems and devices may be deployed epicardially and/or endocardially. Voltages may be applied to a selected subset of the electrodes, with independent subset selections for anode and cathode electrode selections.

**[0059]** FIG. 12 is a schematic illustration of an embodiment of an electroporation system including a pacing device (1207) and an ablation device (1212) disposed in a heart (1202). The pacing device (1207) may be configured to measure cardiac activity and/or deliver a pacing signal to the heart (1201) and the ablation device (1212) may be configured to receive and/or deliver a pulse waveform to cardiac tissue. For example, FIG. 12 schematically illustrates an anterior cross-section of a heart (1201) where lines (1202) schematically approximate the boundaries of the four heart chambers including the right ventricle RV (1215) and left atrium LA (1214). Pacing device (1207) may be introduced into the right ventricle (1215) and positioned such that it can stimulate the right ventricle (1215) and obtain pacing capture. The pacing device (1207) may comprise pacing and/or signal electrodes (1209). The pacing electrodes (1209) may be configured as a bipolar pair to pace the right ventricle (1215) and may be coupled to a cardiac stimulator (1260). The signal electrodes (1209) may be configured as sensors configured to measure intracardiac activity (e.g., ECG signal) of the heart (1201). The ablation device (1212) may be configured to couple to a signal generator (1250). The signal generator (1250) may be configured to generate pulse waveforms for irreversible electroporation of tissue, such as, for example, heart tissue (1201).

**[0060]** In some embodiments, a distal portion of an ablation device (1212) may be introduced into an endocardial

space of the left atrium (1214) through an atrial septum via a trans-septal puncture. In some embodiments, the methods, systems, and devices disclosed herein may comprise one or more of the methods, systems, and devices described in International Application Serial No. PCT/US2013/031252, filed on Mar. 14, 2013, and titled “CATHETERS, CATHETER SYSTEMS, AND METHODS FOR PUNCTURING THROUGH A TISSUE STRUCTURE AND ABLATING A TISSUE REGION,” the contents of which are hereby incorporated by reference in its entirety.

**[0061]** The distal portion of the ablation device (1212) may include a set of electrodes (1213) configured to deliver ablation energy (e.g., pulse electric field energy) to tissue. For example, the ablation device (1212) may be positioned to align one or more electrodes (1213) to contact an inner radial surface of a lumen (e.g., one or more pulmonary vein ostia) (not shown) for delivery of pulse waveforms to ablate tissue. In some embodiments, the electrodes (1210) of the ablation device (1212) may be a set of independently addressable electrodes. Each electrode may include an insulated electrical lead configured to sustain a voltage potential of at least about 700V without dielectric breakdown of its corresponding insulation. In some embodiments, the insulation on each of the electrical leads may sustain an electrical potential difference of between about 200V to about 3,000 V across its thickness without dielectric breakdown. In some embodiments, the set of electrodes may include a plurality of electrodes. The plurality of electrodes may be grouped into one or more anode-cathode subsets such as, for example, a subset including one anode and one cathode, a subset including two anodes and two cathodes, a subset including two anodes and one cathode, a subset including one anode and two cathodes, a subset including three anodes and one cathode, a subset including three anodes and two cathodes, and/or the like.

**[0062]** During the delivery of the pacing signal by the cardiac stimulator (1260), the forward current in the return current that flow through the electrical leads of the pacing device (1212) may be balanced; said another way, the magnitude of the forward current may be substantially equal to the magnitude of the return current, while the direction of the forward current is opposite to the direction of the return current. When the signal generator (1250) delivers the voltage pulse waveform to the heart via the ablation device (1212), due to, for example, the proximity of the ablation device (1212) and the pacing device (1207), currents may be induced in the pacing catheter leads. These may generally be unbalanced currents with high power in the kW range, and may span a range of frequencies and impact the operation of the cardiac stimulator (1260) (as well as other electronic components), in turn impacting the delivery of the voltage pulse waveform by the signal generator (1250). Thus, a need exists for methods and apparatuses for suppressing induced currents in the ablation system.

**[0063]** FIG. 1 illustrates an ablation system (100) configured to deliver voltage pulse waveforms for tissue ablation. The system (100) may include a signal generator (1190), ablation device (140), and optionally a cardiac stimulator (150), pacing device (160), and return electrode (170). The signal generator (1190) may be coupled to at least one ablation device (140), and optionally to the cardiac stimulator (150). The ablation device (140) may include a set of one or more electrodes (142). The signal or waveform generator (1250), the cardiac stimulator (1260), and the



ablation catheter (1212) may be structurally and functionally similar to the signal generator (110), the cardiac stimulator (150), and the ablation device (140) described, respectively, with respect to FIG. 1.

**[0064]** The signal generator (110) may be configured to generate pulse waveforms for irreversible electroporation of tissue, such as, for example, heart tissue. The signal generator (110) may be a voltage pulse waveform generator and deliver a pulse waveform to a set of electrodes (142a, 142b, . . . , 142n) of the ablation device (140). The signal generator (110) may generate and deliver several types of signals including, but not limited to, radiofrequency (RF), direct current (DC) impulses (such as high-voltage, ultra-short pulses used in electroporation), stimulus range impulses, and/or hybrid electrical impulses. For example, the signal generator (110) may generate monophasic (DC) pulses and biphasic (DC and AC) pulses. The signal generator (110) may include a processor (120), memory (122), a set of electrode channels (124a, 124b, . . . , 124n), energy source (126), sensing circuit (128), routing console (130), and user interface (132). One or more signal generator components may be coupled using a communication bus. The processor (190) may incorporate data received from one or more of memory (122), electrode channels (124), energy source (126), sensing circuit (128), routing console (130), user interface (132), ablation device (140), and cardiac stimulator (150) to determine the parameters (e.g., amplitude, width, duty cycle, timing, etc.) of the voltage pulse waveform to be generated by the signal generator (110). The memory (122) may further store instructions to cause the processor (120) to execute modules, processes and/or functions associated with the system (100), such as pulse waveform generation and delivery, electrode channel configuration, fault testing, energy discharge, and/or cardiac pacing synchronization. For example, the memory (122) may be configured to store anode/cathode configuration data, electrode channel configuration data, pulse waveform data, fault data, energy discharge data, heart pacing data, patient data, clinical data, procedure data, and/or the like.

**[0065]** In some embodiments, the ablation device (140) may include a catheter configured to receive and/or deliver the pulse waveforms described herein. For example, the ablation device (140) may be introduced into an endocardial space of the left atrium and positioned to align one or more electrodes (142a, 142b, . . . , 142n) to heart tissue (e.g., one or more pulmonary vein ostia of the left atrium), and then deliver the pulse waveforms to ablate tissue. In another example, the ablation devices (140) may ablate tissue using an epicardial approach. The ablation device (140) may include one or more electrodes (142a, 142b, . . . , 142n) which may, in some embodiments, be a set of independently addressable electrodes. For example, the electrodes (142a, 142b, . . . , 142n) may be grouped into one or more anode-cathode subsets such as, for example, a subset including one anode and one cathode, a subset including two anodes and two cathodes, a subset including two anodes and one cathode, a subset including one anode and two cathodes, a subset including three anodes and one cathode, a subset including three anodes and two cathodes, and/or the like. The set of electrodes (142) may include any number of electrodes, for example, 2, 3, 4, 5, 6, 7, 8, 9, 10, 12, 14, 16, 18, 20, or more electrodes. In some embodiments, the methods, systems, and devices disclosed herein may comprise one or more of the methods, systems, and devices

described in International Patent Application Serial No. PCT/US2017/012099, filed Jan. 4, 2017, and titled “SYSTEMS, DEVICES, AND METHODS FOR DELIVERY OF PULSED ELECTRIC FIELD ABLATIVE ENERGY TO ENDOCARDIAL TISSUE”; International Patent Application Serial No. PCT/US2018/029552, filed on Apr. 26, 2018, and titled “SYSTEMS, DEVICES, AND METHODS FOR SIGNAL GENERATION”; International Application Serial No. PCT/US2019/014226, filed on Jan. 18, 2019, and titled “SYSTEMS, DEVICES, AND METHODS FOR FOCAL ABLATION”; and International Application Serial No. PCT/US2013/031252, filed on Mar. 14, 2013, and titled “CATHETERS, CATHETER SYSTEMS, AND METHODS FOR PUNCTURING THROUGH A TISSUE STRUCTURE AND ABLATING A TISSUE REGION,” the contents of each of which are hereby incorporated by reference in their entirety.

**[0066]** In some embodiments, the processor (120) may be any suitable processing device configured to run and/or execute a set of instructions or code and may include one or more data processors, image processors, graphics processing units, physics processing units, digital signal processors, and/or central processing units. The processor (120) may be, for example, a general purpose processor, Field Programmable Gate Array (FPGA), and Application Specific Integrated Circuit (ASIC), and/or the like. The processor (120) may be configured to run and/or execute application processes and/or other modules, processes and/or functions associated with the system and/or network associated therewith (not shown). In some embodiments, the processor may comprise both a microcontroller unit and an FPGA unit, with the microcontroller sending electrode sequence instructions to the FPGA. The underlying device technologies may be provided in a variety of component types, e.g., metal-oxide semiconductor field-effect transistor (MOSFET) technologies like complementary metal-oxide semiconductor (CMOS) bipolar technologies like emitter-coupled logic (ECL), polymer technologies (e.g., silicon-conjugated polymer and metal-conjugated polymer-metal structures), mixed analog and digital, and/or the like.

**[0067]** In some embodiments, the memory (122) may include a database (not shown) and may be, for example, a random access memory (RAM), a memory buffer, a hard drive, an erasable programmable read-only memory (EPROM), an electrically erasable read-only memory (EEPROM), a read-only memory (ROM), Flash memory, etc. The memory (122) may store instructions to cause the processor (120) to execute modules, processes and/or functions associated with the system (100), such as pulse waveform generation, electrode channel configuration, fault detection, energy discharge, and/or cardiac pacing.

**[0068]** In some embodiments, a set of electrode channels (124) may include a set of active solid-state switches. The set of electrode channels (124) may be configured in a number of ways, including independent anode/cathode configuration for each electrode channel. For example, the electrode channels (124a, 124b, . . . , 124n) may be grouped into one or more anode-cathode subsets such as, for example, a subset including one anode and one cathode, a subset including two anodes and two cathodes, a subset including two anodes and one cathode, a subset including one anode and two cathodes, a subset including three anodes and one cathode, a subset including three anodes and two cathodes, and/or the like. The set of electrode channels (124)

may include any number of channels, for example, 2, 3, 4, 5, 6, 7, 8, 9, 10, 12, 14, 16, 18, 20, or more electrode channels. Energy delivery may use any combination of electrode channels (124) and any order for any energy delivery sequence. The energy delivered may be an RF and/or any tissue ablation energy.

**[0069]** The set of electrode channels (124) may be coupled to a routing console (130) to deliver energy to a set of electrodes (142) coupled to the routing console (130). The set of electrode channels (124) may be coupled to an energy source (126) to receive energy (e.g., a pulse waveform). Processor (120) may be coupled to each electrode channel (124) to configure an anode/cathode configuration for each electrode channel (124), which may be configured on a per pulse basis, per operator input, and/or the like. The processor (120) and energy source (126) may be collectively configured to deliver a pulse waveform to the set of electrodes (142) through the set of electrode channels (124). In some embodiments, each electrode channel (124) may have a bootstrap configuration for low and high frequency operation. For example, the pulse duration of voltage pulses delivered through an electrode channel may be in the range of between about 1 microsecond and about 1000 microseconds. In biphasic mode, this corresponds to an approximate frequency range of between about 500 Hz and about 500 KHz for the frequency associated with the voltage pulses.

**[0070]** In some embodiments, an energy source (126) may be configured to convert and supply energy to a set of electrodes (142) coupled to the signal generator (110). The energy source (126) of the signal generator (110) may include a DC power supply and be configured as an AC/DC switcher. In some embodiments, an energy source (126) of the signal generator (110) may deliver rectangular-wave pulses with a peak maximum voltage of about 7 kV into a device with an impedance in the range of about 30Ω and 3000Ω for a maximum duration of about 1000 μs. In some of these embodiments, the energy source (126) may include one or more capacitors to store energy from a power supply. While these examples are included for purely non-limiting illustrative purposes, it is noted that a variety of pulse waveforms with a range of pulse durations, intervals between pulses, pulse groupings, etc. may be generated depending on the clinical application.

**[0071]** In some embodiments, a sensing circuit (128) may be configured to determine an amount of current being delivered to a device coupled to the signal generator (110) (e.g., electrode (142) coupled to the electrode channel (124)). As described in more detail herein, the sensing circuit (128) may also be used to classify an electrode channel fault, monitor capacitor discharge, and/or sense arcing. In some embodiments, the sensing circuit (128) may be direct current sensing circuit and/or low-side sensing circuit. The sensing circuit may include one or more operational amplifiers, difference amplifiers (DA), instrumentation amplifiers (IA), and/or current shunt monitors (CSM).

**[0072]** In some embodiments, the routing console (130) may be configured to electrically couple a set of electrodes (142) of an ablation device (140) to a set of electrode channels (124). The routing console (130) may be configured to selectively deliver energy to the set of electrodes (142) using the set of electrode channels (124). One or more ablation devices (140) each having a set of electrodes (142) may be coupled to the routing console (130). The set of

electrodes (142) may include any number of electrodes, for example, 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 12, 14, 16, 18, 20, or more electrodes.

**[0073]** In some embodiments, the electrode channels (124) configured for energy delivery (e.g., configured as an anode/cathode pair of electrode channels) may not be adjacent to each other. For example, the set of electrode channels (124) may include a set of N electrode channels (124n) in a linear array. In one embodiment, a first electrode channel may correspond to a first electrode channel (124a) in the linear array of N electrode channels (124n). One or more of a second and third electrode channel (124b, 124c) may not be adjacent to the first electrode channel (124a) in a linear array of N electrode channels (124n).

**[0074]** A multi-electrode ablation device may allow targeted and precise energy delivery tissue. In some embodiments, the electrodes (142) or an ablation device (140) may be configured for energy delivery (e.g., as an anode/cathode pair of electrodes (142)) and may be adjacent to each other within a linear array of the electrodes (142) in the ablation device (140). For example, an ablation device (140) may include a set of electrodes (142) as a linear array of N electrodes (142n). As discussed in more detail herein, FIG. 5 illustrates another embodiment of an ablation device (500) including a linear array of electrodes (530). The signal generator (110) coupled to the ablation device (140) may include a set of electrode channels (124) having N electrode channels (124n) corresponding to N electrodes (142n) of the ablation device (140). In one embodiment, the first electrode channel (124a) of the N electrode channels (124n) may correspond to a first electrode (124a) in the linear array of N electrodes (142n). One or more of second and third electrode channel (124b, 124c) of the N electrode channels (124n) may not correspond to any of the electrodes adjacent to the first electrode (142a) in the linear array of N electrodes (142n).

**[0075]** Configurable electrode channel and electrode selection may provide flexibility in positioning the electrodes for ablating a desired region of interest. In one embodiment, the routing console (130) may couple to a set of 16 electrodes (142) of an ablation device (140). The routing console (1030) may receive input from the processor (120) and/or user interface (132) for electrode channel selection and energy delivery to one or more electrodes (142). Additionally or alternatively, the routing console (130) may couple to a cardiac stimulator (150) and be configured to receive data from devices (e.g., heart pacing data from a pacing device) used for synchronization of a pulse waveform with a patient cardiac cycle. In embodiments, the waveform or signal generator may integrate and/or include pacing and/or cardiac stimulator functionality.

**[0076]** In some embodiments, a user interface (132) may be configured as a communication interface between an operator and the system (100). The user interface (132) may include an input device and output device (e.g., touch surface and display). For example, patient data from memory (122) may be received by user interface (132) and output visually and/or audibly. Electric current data from sensing circuit (128) may be received and output on a display of user interface (132). As another example, operator control of an input device having one or more buttons, knobs, dials, switches, trackball, touch surface, and/or the

like, may generate a control signal to the signal generator (110) and/or ablation device (140).

[0077] In some embodiments, an input device of the user interface (132) may include a touch surface for operator input and may be configured to detect contact and movement on the touch surface using any of a plurality of touch sensitivity technologies including capacitive, resistive, infrared, optical imaging, dispersive signal, acoustic pulse recognitions, and surface acoustic wave technologies. Additionally or alternatively, the user interface (132) may include a step switch or foot pedal.

[0078] In some embodiments, an output device of the user interface (132) may include one or more of a display device and audio device. The display device may include at least one of a light emitting diode (LED), liquid crystal display (LCD), electroluminescent display (ELD), plasma display panel (PDP), thin film transistor (TFT), and organic light emitting diodes (OLED). An audio device may audibly output patient data, sensor data, system data, other data, alarms, warnings, and/or the like. The audio device may include at least one of a speaker, piezoelectric audio device, magnetostrictive speaker, and/or digital speaker. In one embodiment, the audio device may output an audible warning upon detection of a fault in the signal generator (110).

[0079] In some embodiments, the signal generator (110) may be mounted on a trolley or cart. In some embodiments the user interface (132) may be formed in the same or different housing as the signal generator (110). The user interface (132) may be mounted to any suitable object, such as furniture (e.g., a bed rail), a wall, a ceiling, or may be self-standing. In some embodiments, the input device may include a wired and/or wireless transmitter configured to transmit a control signal to a wired and/or wireless receiver of the signal generator (110).

[0080] In some embodiments, a cardiac stimulator (150) including a pacing device (160) may be configured to generate a heart pacing signal to be delivered to a patient via the pacing device (160) to pace one or more chambers of the heart for cardiac stimulation. The pacing device (160) may be configured to pace the heart and measure cardiac activity. The pacing device (160) may include pacing electrodes and signal electrodes. In some embodiments, the pacing device (160) may deliver a pacing pulse generated by a cardiac stimulator (150) using the pacing electrodes. The pacing device (160) may further measure cardiac activity corresponding to intracardiac activity (e.g., ECG signal) using the signal electrodes. An indication of the pacing signal may be transmitted by the cardiac stimulator (150) to the signal generator (110). Based on the pacing signal, an indication of a voltage pulse waveform may be selected, computed, and/or otherwise identified by the processor (120) and generated by the signal generator (110). In some embodiments, the signal generator (110) may further include circuitry for the generation of cardiac stimulation and/or pacing signals and thereby provide stimulator functionality. In some embodiments, the signal generator (110) may be configured to generate the voltage pulse waveform in synchronization with the indication of the pacing signal (e.g., within a common refractory window). For example, in some embodiments, the common refractory window may start substantially immediately following a ventricular pacing signal (or after a very small delay) and last for a duration of approximately 250 milliseconds (ms) or less (e.g., between about 150 ms and about 250 ms) thereafter. In such embodiments,

an entire pulse waveform may be delivered within this duration. Heart pacing is described further herein with respect to FIG. 11.

[0081] The return electrode (170) may be coupled to a patient (e.g., disposed on a patient's back) to allow current to pass from the pacing device (160) through the patient and then to the return electrode (170) to provide a safe current return path from the patient (not shown). In some embodiments, the systems described herein may include one or more sterile coverings configured to create a sterile barrier around portions of the system (100). In some embodiments, the system (100) may include one or more sterile coverings to form a sterile field. For example, a sterile covering may be placed between the ablation device(s) and the patient, forming a barrier between an interior, non-sterile side including the patient, signal generator, and ablation devices and an exterior, sterile side including the operator. Additionally or alternatively, components of the system (100) may be sterilizable. The sterile covering may include, for example, a sterile drape configured to cover at least a portion of a system component. In one embodiment, a sterile covering (e.g., sterile drape) may be configured to create a sterile barrier with respect to a user interface (132) of the system (100). The sterile drape may be clear and allow an operator to visualize and manually manipulate the user interface (132). The sterile covering may conform tightly around one or more system components or may drape loosely so as to allow components to be adjusted within the sterile field.

[0082] FIG. 2 illustrates a circuit diagram of an embodiment of a signal generator (200) that may be structurally and/or functionally similar to a signal generator (110). The signal generator (200) may include one or more electrode channels (201, 202, 203). FIG. 2 illustrates each of the electrode channels having a similar circuit configuration that may be structurally and/or functionally similar to the electrode channels (124a, 124b, . . . , 124n). In some embodiments, each of the electrodes channels (201, 202, 203) may be configured individually as a half bridge amplifier while a pair of the electrode channels may be collectively configured as a full bridge amplifier. The signal generators as described herein may include a flexibly programmable electrode configuration; various subsets of electrodes may be configured as anodes and cathodes dynamically and rapidly. Thus, in an ablation energy delivery process, energy may be delivered rapidly over a sequence of paired electrode subsets. In some case, a given electrode may be configured as an anode, and shortly thereafter as a cathode, during the course of sequencing over a succession of paired electrode subsets. Likewise, a biphasic waveform may also be delivered with the help of this topology, where an initially given anode-cathode pair may be made to reverse polarity after a very brief switching time interval; repeatedly alternating the sequencing of anode/cathode selection may yield a biphasic voltage pulse train. The signal generator (200) may include N number of electrode channels, for example, 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 12, 14, 16, 18, 20, or more electrode channels. Described with reference to the first electrode channel (201) for the sake of simplicity, each electrode channel may include a first electronic switch (220) configured to switch between an ON state and an OFF state. A first drive circuit (222) may be coupled to the gate terminal of the first electronic switch (220) to control the state of the first electronic switch (220). The first electrode channel (201) further includes a second

electronic switch (230) configured to switch between an ON and an OFF state. A second drive circuit (232) may be coupled to the gate terminal of the second electronic switch (230) to control the state of the second electronic switch (230). Each of the drive circuits (222, 232) may be coupled to and controlled by a processor (e.g., processor (120)). An output channel (211) may be coupled to the emitter terminal of the first electronic switch (220) and to the collector terminal of the second electronic switch (230), and may form part of a current path for electrical currents to pass via electrodes on a medical device (not shown) through an electrical load (such as patient anatomy) to one or more output channels coupled to a second electrode channel as described below. The output channel (211) may be coupled to a first electrode such as a first electrode (142a) of ablation device (140).

[0083] Likewise, second and third electrode channels (202, 203) may include respective first electronic switches (220', 220''), each configured to switch between an ON state and an OFF state. First drive circuits (222', 222'') may be coupled to respective first electronic switches (220', 220'') to control the state of the first electronic switches (220', 220''). Output channels (212, 213) may be coupled between the emitter terminals of the first electronic switches (220', 220'') and the collector terminals of the second electronic switches (230', 230''). The output channels (212, 213) may be coupled to respective second and third electrodes, such as the second electrode (142b) and the third electrode (142c) of ablation device (140). The second and third electrode channels (202, 203) further include respective second electronic (230', 230'') configured to switch between an ON and an OFF state. Second drive circuits (232', 232'') may be coupled to the gate terminals of the second electronic switches (230', 230''). Each of the drive circuits (222', 222'', 232', 232'') may be coupled to and controlled by a processor (e.g., processor (120)). The drive circuits controlled by the processor effectively comprise the routing console 130. As described above, the routing console may be configured to couple to a set of device electrodes connected to the output channels. Each electrode channel (201, 202, ...) corresponds to a respective electrode (142a, 142b, ...) of the set of device electrodes. As an exemplary illustration of waveform delivery, if switches (220, 230) are respectively in ON and OFF states, switches (220', 230') are respectively in OFF and ON states, and switches (220'', 230'') are respectively in OFF and ON states, and all other switches of all other electrode channels are in an OFF state, a positive voltage pulse is delivered with output channel N (211) as anode or positive terminal and with output channels N+3 (212 in FIG. 2) and N+4 (213 in FIG. 2) as cathodes or negative/ground terminals. The duration of the ON state of the switches determines the time width of the pulse. In this manner a sequence of pulses may be delivered over any sequence of anode-cathode pairings, including repeated pulsing of a given or particular anode-cathode combination. Waveform delivery may be interspersed over a sequence of electrodes with the architecture of the generator disclosed herein. While the example of electrode channel selection disclosed in the foregoing described the selection of one anode channel and two cathode channels, it should be clear that a variety of such anode-cathode combinations may be selected without limitation.

[0084] The electronic switches (220-220'', 230-230'', 320-320'', 330-330'') as described herein may include one or

more bipolar transistors, such as bipolar junction transistors or Bipolar Field Effect Transistors. In some embodiments, one or more of the electronic switches include insulated-gate bipolar transistors (IGBT's). Such IGBT switches may be capable of handling the high instantaneous power associated with high voltages, in the approximate range of about 50,000 W to about 300,00 W. An energy source (not shown) may be coupled to the collector terminals of the first electronic switches (220, 220', 220'') of the electrode channels (201, 202, 203) through respective resistive elements (240, 240', 240''). As described herein in more detail, the resistive elements (240, 240', 240'') may each be configured to discharge a capacitive element of the energy source when the energy source is not in use. In some embodiments, the resistive element may have a resistance in the range of between about 5 Ohms and about 25 Ohms. Each of the electrode channels (201, 202, 203) may be coupled to a sensing circuit (250) and current sense resistor (252). In some embodiments, the sensing circuit (250) may be configured to detect arcing during use. In FIG. 2, the sensing circuit (250) may be coupled between the emitter terminal of the second electronic switches (230, 230', 230'') and ground (254). Additionally or alternatively, each electrode channel (201, 202, 203) may be coupled to a respective sensing circuit (250) and current sense resistor (252).

[0085] In some embodiments, as described with respect to FIGS. 1 and 2, a processor such as processor (120) coupled to the set of drive circuits (222, 232) may configure the first electrode channel (201) as an anode. One or more of the second and third electrode channels (202, 203) may similarly be configured by the processor (120) as a cathode. In one embodiment, the first electrode channel (201) may be configured as an anode by setting the first electronic switch (220) of the first electrode channel (201) to the ON state and by setting the second electronic switch (230) of the first electrode channel (201) to the OFF state. Each of the second and third electrode channels (202, 203) may be configured as a cathode by setting their respective first electronic switches (220', 220'') to the OFF state and setting their respective second switches (230', 230'') to the ON state. In this manner, the electrode channels (201, 202) may, for example, form a current path to a tissue site (e.g., coupled to each of the output channels (211, 212)) using the first electronic switch (220) of the first electrode channel (201) and second electronic switch (230') of the second electrode channel (202).

[0086] The processor (120) and energy source (126) may be collectively configured to deliver a pulse waveform to the set of electrodes during use via one or more of the electrode channels (201, 202, 203). The signal generator (200) may deliver biphasic (AC) pulses where in some embodiments, after delivering a voltage pulse to the set of output channels (211, 212, 213) with output channels (211) as an anode and output channels (212, 213) as cathodes, the polarities are immediately reversed and a voltage pulse of opposite polarity is then delivered with the output channel (211) as a cathode and output channels (212, 213) as anodes, and so on until a desired number of biphasic pulses has been delivered to the output channel set (211, 212, 213) in the form of a suitable waveform. Subsequently (and possibly with a programmable time interval), a different set of device electrodes (or output channels) may be configured as anodes and cathodes, and the waveform may be delivered again over this new set of device electrodes. In this manner, the voltage waveform

may be sequenced over any desired collection of electrodes. Generally, the processor (120) and energy source (126) may be collectively configured to deliver the pulse waveform over a sequenced set of electrodes (142a, 142b, . . . , 142n).

[0087] In some embodiments, as described in more detail herein, the pulse waveform delivered using the signal generator (200) may include a set of levels of a hierarchy and/or may be in synchronization with the indication of a pacing signal generated from a cardiac stimulator (150).

[0088] FIG. 3 illustrates a circuit diagram of an embodiment of a signal generator (300) that may be structurally and/or functionally similar to the signal generator (1190). For example, the signal generator (300) may include on or more electrode channels (301, 302, 316) that may be structurally and/or functionally similar to the electrode channels (124a, 124b, . . . , 124n). For ease of explanation, unless explicitly noted otherwise, elements in FIG. 3 may have the same components, functionality, and/or values as discussed with respect to similar elements in FIG. 2. For example, the electrode channels (201, 202, 203) used to deliver pulse waveforms to a set of electrodes in FIG. 2 may be the same set of electrode channels (301, 302, 316) used for capacitive energy discharge in FIG. 3. The signal generator (300) may include one or more electrode channels (301, 302, . . . , 316) where FIG. 3 illustrates each of the electrode channels having a same circuit configuration. FIG. 3 illustrates 16 electrode channels, although it should be appreciated that the signal generator (300) may include N number of electrode channels, for example, 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 12, 14, 16, 18, 20, or more electrode channels. A first electrode channel (301) may include a first electronic switch (320) configured to switch between an ON state and an OFF state. A first drive circuit (322) may be coupled to the gate terminal of the first electronic switch (320) to control the state of the first electronic switch (320). The first electrode channel (301) may further include a second electronic (330) configured to switch between an ON and an OFF state. A second drive circuit (322) may be coupled to the gate terminal of the second electronic switch (330) to control the state of the second electronic switch (330). An output channel (361) may be coupled between the emitter terminal of the first electronic switch (320) and the collector terminal of the second electronic switch (330).

[0089] Likewise, a second and sixteenth electrode channel (302, 316) may include respective first electronic switches (320', 320'') configured to switch between an ON state and an OFF state. The first drive circuits (322', 322'') may be coupled to respective first electronic switches (320', 320'') to control the state of the first electronic switches (320', 320''). Output channels (362, 376) may be coupled between the emitter terminal of the first electronic switches (320', 320'') and the collector terminal of the second electronic switches (330', 330''). The second and sixteenth electrode channels (302, 316) further include respective second electronic switches (330', 330'') configured to switch between an ON and an OFF state. A second drive circuit (332', 332'') may be coupled to the gate terminal of the second electronic switch (330', 330'') to control the state of the second electronic switch (330', 330''). Each of the output channels (361, 362, 376) may be coupled to respective electrodes on one or more medical devices (not shown). Each electrode channel (301, 302, 316) may thus correspond to a respective electrode of the set of electrodes on one or more medical devices.

[0090] The electronic switches as described herein may include one or more bipolar transistors. In some embodiments, one or more of the electronic switches include insulated-gate bipolar transistors. An energy source (not shown) may be coupled to the collector terminals of the first electronic switches (320, 320', 320'') of the electrode channel (301, 302, 316) through respective resistive elements (340, 340', 340''). The resistive elements (340, 340', 340'') may each be configured to discharge a capacitive element of the energy source when the energy source is not in use. Each of the electrode channels (301, 302, 316) may be coupled to a sensing circuit (350) and current sense resistor (352). In some embodiments, the sensing circuit (350) may be configured to detect arcing during use. In FIG. 3, the sensing circuit (350) may be coupled between the emitter terminal of the second electronic switches (330, 330', 330'') and ground (354). Additionally or alternatively, each electrode channel (301, 302, 316) may be coupled to a respective sensing circuit (350) and current sense resistor (352).

[0091] In some embodiments, as described with respect to FIGS. 1 and 3, the signal generator (110) may provide active monitoring of the electrode channels. For example, the processor (120) of the signal generator (110) may be configured to perform one or more fault tests to verify operation of one or more electrode channels (124a, 124b, . . . , 124n) (e.g., electronic switches and drive circuits), the energy source (126) (e.g., DC power supply), and sensing circuit (128) (e.g., arc detection). The fault tests may be performed on one or more electrode channels (124a, 124b, . . . , 124n) at predetermined intervals (e.g., upon startup before delivery of a pulse waveform, between delivery of pulse waveforms, when the energy source (126) is not in use). In some embodiments, the signal generator (300) may perform a series of fault tests on one or more electrode channels to classify a working state of one or more electrode channels. In one embodiment, after delivery of a pulse waveform to set of electrodes (142a, 142b, . . . , 142n) at a first time, a first fault test may be conducted individually for one or more of the set of electrode channels (301, 302, . . . , 316). In some embodiments, the first fault test may include, for the first electrode channel (301), setting the first electronic switch (320) to the ON state and the second electronic switch (330) to the OFF state. A verification DC voltage may be applied to the first electrode channel (301) for fault testing. In one embodiment, the verification DC voltage may be about 50V. The first electrode channel (301) may be classified as passing the first fault test when substantially no current is detected by the sensing circuit (350) during the first fault test. The first electrode channel (301) may be classified as failing the first fault test (e.g., in fault) when a threshold current, for example a current of 10 mA or higher, is detected by the sensing circuit (350). In some embodiments, the second fault test may include, for the first electrode channel (301), setting the first electronic switch (320) to the OFF state and the second electronic switch (330) to the ON state. The first electrode channel (301) may be classified as passing the second fault test when substantially no current is detected by the sensing circuit (350) during the second fault test. The first electrode channel (301) may be classified as failing the second fault test when a threshold current, for example a current of 10 mA or higher, is detected by the sensing circuit (350). In some embodiments, the third fault test may include, for the first electrode channel (301), setting the first electronic switch (320) to the ON state and the

second electronic switch (330) to the ON state. The first electrode channel (301) may be classified as passing the third fault test when a predetermined amount of current is detected by the sensing circuit (350) during the third fault test and classified as failing the third fault test when the sensing circuit (350) detects a non-predetermined amount of current. For example, the predetermined amount of current (e.g., about 5A) may be equal to a DC voltage output by the energy source (e.g., about 50 V) divided by a resistance of the resistive element (340) (e.g., about 10 (2)).

[0092] A failure in the first fault test may indicate a malfunction in the second electronic switch (330) and/or second drive circuit drive (332) (e.g., lower IGBT circuitry in FIG. 3) while a failure in the second fault test may indicate a malfunction in the first electronic switch (320) and/or first drive circuit (322) (e.g., upper IGBT circuitry in FIG. 3). A failure in the third fault test may indicate a malfunction in one or more of the energy source, sensing circuit, electronic switches, and drive logic. Accordingly, the fault tests may verify the individual and collective operation of upper and lower IGBT circuitry for a fault tested electrode channel. Each of the fault tests described herein may be performed for each electrode channel (301, 302, . . . , 316) at a predetermined interval.

[0093] In some embodiments, a fault test may be performed for an electrode channel (124) based on predetermined criteria (e.g., a predetermined number of pulses delivered, a predetermined amount of energy delivered, and/or the like). Each electrode channel or a subset of electrode channels may be verified. For example, fault tests may be performed on each electrode channel (124) configured as an anode, or for each electrode channel (124) after delivery of 5 pulses. In some embodiments, the fault tests may be conducted in conjunction with voltage pulse waveform delivery and capacitor discharge, as described in more detail herein.

[0094] The generation and delivery of high voltage pulse waveforms using a signal generator as described herein may lead to an energy source (e.g., one or more capacitors) of the signal generator storing excess energy. This energy may be discharged to ground through a set of discharge pulses using the electrode channels. Discharge may be performed prior to delivering subsequent pulse waveforms. In other words, the electrode channels may be used to deliver tissue ablation energy to one or more electrodes as well as separately and internally discharge excess energy to ground. This configuration may be used in place of a dump circuit and/or bleeder resistor circuit for discharging excess stored energy in the signal generator.

[0095] In some embodiments, as described with respect to FIGS. 1 and 3, each electrode channel (124) may sequentially partially discharge the energy source (126) to ground over a set of cycles. Each electrode channel (124) may be configured as a half bridge amplifier to partially discharge the energy source to ground. The energy source (126) may complete discharge of a predetermined amount of energy within seconds. As used herein, a discharge cycle refers to energy discharge of the energy source to ground using each of the electrode channels of the set of electrode channels. For example, energy may be partially discharged to ground one at a time through each electrode channel (124) of a signal generator (110). In some embodiments, fault detection may be performed on the electrode channels (124) at predetermined intervals (e.g., before each discharge cycle, after

a predetermined number of discharge cycles, etc.) to ensure that energy discharge is performed as intended. As stored energy is reduced through discharging, a pulse width of a discharge pulse may be increased without causing damage to the electrode channels (124). For example, an initial, first amount of stored energy (e.g., about 3 kJ) of the energy source (126) may correspond to discharge pulses having a first predetermined pulse width (e.g., about 0.5  $\mu$ s). After discharge of the energy source to a second amount of stored energy, the pulse width of the discharge pulses may be configured to a second predetermined pulse width (e.g., about 2  $\mu$ s).

[0096] In some embodiments, the set of electrode channels illustrated in FIG. 3 may correspond to a set of discharge paths to ground to reduce an amount of stored energy of an energy source (126). In some embodiments, the first electrode channel (301) of the set of electrode channels (301, 302, . . . , 316) may be configured to partially discharge energy to ground after delivering a pulse waveform to a set of electrodes (142). For example, the first electronic switch (320) may be set to the ON state and the second electronic switch (330) may be set to the ON state for a predetermined duration of time to at least partially discharge the energy source (126). This current through the first electrode channel (301) may be about equivalent to the DC voltage of the energy source (126) divided by a resistance of the resistive element (340). The first electrode channel (301) may discharge energy to ground using a predetermined pulse width (e.g., about 0.5  $\mu$ s).

[0097] Once the first electrode channels (302, . . . 316) may be configured to partially discharge the energy source (126) one at a time in a manner analogous to the first electrode channel (301). In some embodiments, a channel inactive time period (e.g., dead time) may follow the partial energy discharge of an electrode channel. For example, a channel inactive time period following each electrode channel energy discharge may be about 100  $\mu$ s. In some embodiments, a discharge cycle inactive time period may follow each discharge cycle. For example, a discharge cycle inactive time period may be about 5 ms and may correspond to a bootstrap charge time. By staggering the discharge of each electrode channel, the signal generator (300) may discharge capacitor energy at a faster rate than conventional circuit topologies.

[0098] The set of electrode channels (124) may discharge the energy source to ground sequentially over a set of discharge cycles until reaching a predetermined energy threshold. In some embodiments, energy discharge may be performed such that a pulse width increases over time or over each discharge cycle. The number of pulses may decrease as the pulse width increases. In some embodiments, energy discharge may be configured as follows: a first pulse width may be between about 0.1  $\mu$ s and about 1  $\mu$ s and may be set between about 90 discharge cycles and about 130 discharge cycles; a second pulse width may be between about 1  $\mu$ s and about 5  $\mu$ s and may be set between about 80 discharge cycles and about 90 discharge cycles; a third pulse width may be between about 5  $\mu$ s and 10  $\mu$ s and may be set between about 70 discharge cycles and about 80 discharge cycles; a fourth pulse width may be between about 10  $\mu$ s and about 15  $\mu$ s and may be set for about 70 discharge cycles or less; and a fifth pulse width may be between about 15  $\mu$ s and 25  $\mu$ s and may be set for about 70 discharge cycles or less.

[0099] In one merely illustrative and non-limiting example, a set of 16 electrode channels may be used to discharge to ground an energy source of about 3 kJ at an average rate of about 1 kJ/sec such that the signal generator may complete discharge in about 3 seconds. In one embodiment, energy discharge may be configured as follows: a first pulse width of about 0.5  $\mu$ s may be set for about 110 discharge cycles over about 730 ms; a second pulse width of about 2  $\mu$ s may be set for about 80 discharge cycles over about 530 ms; a third pulse width of about 6  $\mu$ s may be set for about 73 discharge cycles over about 490 ms; a fourth pulse width of about 12.5  $\mu$ s may be set for about 70 discharge cycles over about 480 ms; and a fifth pulse width of about 25  $\mu$ s may be set over about 780 ms for any remaining discharge cycles left to complete the energy source discharge.

[0100] In some embodiments, fault detection as described herein may be performed on an electrode channel prior to a partial energy discharge using that electrode channel. If the electrode channel is determined to be in a fault state, the electrode channel may be excluded from the set of electrode channels used to discharge the energy source to ground and/or the fault status may be output to the operator. Verification of the electrode channels at predetermined intervals such as for: each energy discharge pulse; one or more discharge cycles (e.g., fault test the electrode channels after each cycle or every other cycle); pulse width transitions (e.g., fault detect the electrode channels between every increase in pulse width); and a predetermined time interval (e.g., fault test the electrode channels every 0.1 seconds, 0.25 seconds, 0.5 seconds, 1 second, etc.).

#### Ablation Device

[0101] The systems described here may include one or more multi-electrode ablation devices configured to ablate heart tissue for treating atrial fibrillation such as in a left atrial chamber of a heart. FIG. 4A illustrates an embodiment of an ablation device (e.g., structurally and/or functionally similar to the ablation device (140)) that may be configured to deliver voltage pulse waveforms using a set of electrodes to ablate tissue and electrically isolate a pulmonary vein. In some of these embodiments, the ablation device may be transformed from a first configuration to a second configuration such that the electrodes of ablation device expand outward to contact a lumen or an ostium or an antrum of an orifice in tissue (e.g., pulmonary vein ostium or pulmonary vein antrum). The ablation devices as described herein are for exemplary or illustrative purposes only and a variety of other ablation devices may be implemented without departing from the scope of the present invention.

[0102] The ablation device (400) includes a catheter shaft (410) at a proximal end of the device (400), a distal cap (412) of the device (400), and a set of splines (414) coupled thereto. The distal cap (412) may include an atraumatic shape. A proximal end of the set of splines (414) may be coupled to a distal end of the catheter shaft (410), and a distal end of the set of splines (414) may be tethered to the distal cap (412) of the device (400). Each spline (414) of the ablation device (400) may include one or more electrodes (416) formed on a surface of the spline (414). Each electrode (416) may include an insulated electrical lead configured to sustain a voltage potential of at least about 700 V without dielectric breakdown of its corresponding insulation. In other embodiments, the insulation on each of the electrical

leads may sustain an electrical potential difference of between about 200V to about 1500V across its thickness without dielectric breakdown. Each spline (414) may include the insulated electrical leads of each electrode (416) formed in a body of the spline (414) (e.g., within a lumen of the spline (414)). A set of spline wires (418, 419) may be electrically conductive and electrically couple adjacent electrodes (416) disposed on different splines (414). For example, the spline wire (418) (connecting electrodes (416)) and the spline wire (419) (connecting electrodes (416')) may extend in a transverse direction relative to a longitudinal axis of the ablation device (400).

[0103] FIG. 4A illustrates a set of splines (414) where each spline (414) includes a pair of electrodes (416 and 416') having about the same size, shape, and spacing as the electrodes (416 and 416') of an adjacent spline (414). In other embodiments, the size, shape, and spacing of the electrodes (416, 416') may differ. For example, the electrodes (416) electrically coupled to a first spline wire (418) may differ in size and/or shape from electrodes (416') electrically coupled to a second spline wire (419).

[0104] In some embodiments, the first spline wire (418) may include a first set of spline wires (420, 421, 422, 423), where each spline wire of the set of spline wires (420, 421, 422, 423) may couple electrodes (416) between a different pair of splines of the set of splines (414). In some of these embodiments, the set of spline wires (420, 421, 422, 423) may form a continuous loop between the electrodes (416) coupled thereto. Likewise, the second spline wire (419) may include a second set of spline wires (424, 425, 426), where each spline wire of the set of spline wires (424, 425, 426) may couple electrodes (416') across the set of splines (414). The second set of spline wires (424, 425, 426) may couple different electrodes (416') across the set of splines (414) than the first set of spline wires (420, 421, 422, 423). In some of these embodiments, the first set of spline wires (420, 421, 422, 423) may form a first continuous loop between the electrodes (416) coupled thereto and the second set of spline wires (424, 425, 426) may form a second continuous loop between the electrodes (416') coupled thereto. The first continuous loop may be electrically isolated from the second continuous loop. In some embodiments, the electrodes coupled to the first continuous loop may be configured as anodes and the electrodes (416') coupled to the second continuous loop may be configured as cathodes. A pulse waveform generated by a signal generator may be delivered to the electrodes (416 and 416') of the first and second continuous loop. In some embodiments, the spline wires such as 421, 422, 423, etc. may be replaced by similar electrical connections in the proximal part of the device (for example, in the device handle). For example, the electrodes (416) may all be electrically wired together in the handle of the device.

[0105] In another embodiment illustrated in FIG. 4B, the first spline wire (461) of the set of spline wires (461, 462) may couple electrodes (459) between a first spline (451) and a second spline (452) of the set of splines, and a second spline wire (462) of the set of spline wires (461, 462) may couple electrodes (460) between the third spline (453) and a fourth spline (454) of the set of splines. The electrodes (459) coupled by the first spline wire (461) and the electrodes (460) coupled by the second spline wire (462) may be configured as an anode and a cathode respectively (or vice-versa). A pulse waveform may be delivered to the



electrodes (459) coupled by the first spline wire (461) and the electrodes (460) coupled by the second spline wire (462). In some embodiments, instead of spline wires the electrical leads of at least two electrodes of the set of electrodes may be electrically coupled at or near a proximal portion of the ablation device, such as, for example, within a handle.

[0106] In other embodiments, referring to FIG. 4A, one or more of the spline wires (418, 419) may form a continuous loop between the electrically coupled electrodes (416). For example, a first set of spline wires (418) may form a first continuous loop between the electrodes (416) coupled thereto and a second set of spline wires (419) may form a second continuous loop between the electrodes (416') coupled thereto. In this case, the first continuous loop may be electrically isolated from the second continuous loop. In one embodiment, each of the electrodes (416) coupled to a second set of spline wires (419) may be configured as a cathode. Each group of electrically coupled electrodes (416) may be independently addressable. In some embodiments, instead of spline wires the electrical leads of at least two electrodes of the set of electrodes may be electrically coupled at or near a proximal portion of the ablation device, such as, for example, within a handle.

[0107] In other embodiments, the size, shape, and spacing of the electrodes (416) may differ. The ablation device (400) may include any number of splines, for example, 3, 4, 5, 6, 7, 8, 9, 10, 12, 14, 16, 18, 20, or more splines. In some embodiments, the ablation device (400) may include 3 to 20 splines. For example, in one embodiment, the ablation device (400) may include between 4 and 9 splines.

[0108] For each of the ablation devices described herein, each of the splines may include a polymer and define a lumen so as to form a hollow tube. The one or more electrodes of the ablation device described herein may include a diameter from about 0.2 mm to about 2.5 mm and a length from about 0.2 mm to about 5.0 mm. In some embodiments, the electrode may include a diameter of about 1 mm and a length of about 1 mm. As the electrodes may be independently addressable, the electrodes may be energized in any sequence using any pulse waveform sufficient to ablate tissue by irreversible electroporation. For example, different sets of electrodes may deliver different sets of pulses (e.g., hierarchical pulse waveforms). It should be appreciated that the size, shape, and spacing of the electrodes on and between the splines may be configured to deliver energy sufficient to generate contiguous/transmural lesions in order to electrically isolate one or more pulmonary veins. In some embodiments, alternate electrodes (e.g., all the distal electrodes) may be at the same electric potential, and likewise for all the other electrodes (e.g., all the proximal electrodes). Thus, ablation may be delivered rapidly with all electrodes activated at the same time. A variety of such electrode pairing options exist and may be implemented based on the convenience thereof.

[0109] For each of the ablation devices discussed herein, the electrodes (e.g., ablation electrode, return electrode) may include biocompatible metals such as titanium, palladium, silver, platinum or platinum alloy. For example, the electrode may preferably include platinum or platinum alloy. Each electrode may include an electrical lead having sufficient electrical insulation to sustain an electrical potential difference of at least 700V across its thickness without dielectric breakdown. In other embodiments, the insulation on each of the electrical leads may sustain an electrical

potential difference of between about 200 V to about 3,000 V across its thickness without dielectric breakdown, including all values and sub-ranges in between. The insulated electrical leads may run to the proximal handle portion of the catheter from where they may be connected to a suitable electrical connector. The catheter shaft may be made of a flexible polymeric material such as Teflon, Nylon, Pebax, etc.

[0110] FIG. 5 illustrates an embodiment of an ablation device (500) (e.g., structurally and/or functionally similar to the ablation device (140)) that may be configured to deliver voltage pulse waveforms generated by a signal generator (110) as described herein using a set of electrodes to ablate tissue which in some embodiments may generate a linear circumferential ablation lesion. The ablation device (500) may include a catheter (510) having a flexible elongate shaft (520). The elongate shaft (520) may be advanced and withdrawn from a lumen of the catheter (510). The flexibility of the catheter (510) may facilitate positioning of the electrodes (530) around asymmetric and/or complex contours. The elongate shaft (520) may include a set of electrodes (530) spaced apart along the elongate shaft (520). In some embodiments, the electrodes (530) may be integrally formed with the elongate shaft (520). Each of the electrodes (530) may be connected to a respective output channel of a signal generator. The electrodes (530) may be independently configured as an anode or cathode and configured to deliver a pulse waveform to target tissue to perform ablation. In some embodiments, the set of electrodes (530) may be independently configured as an anode or cathode and configured to deliver a pulse waveform to target tissue to perform ablation. In some embodiments, the set of electrodes (530) may have a spacing (532) between electrodes configured to create a contiguous ablation lesion such as a circumscribing lesion around a left atrial target and pulmonary vein. In some embodiments, the ratio of the spacing (532) between consecutive electrodes (530) to the longitudinal length of each electrode may be less than about 3:1, and may be less than about 2:1.

#### Protection Device

[0111] Generally, the systems and devices configured for suppressing induced currents in connection with tissue ablation may include a protection device coupled between a first electronic device (e.g., pacing device) and a second electronic device (e.g., an apparatus to be protected). As described in more detail herein, the protection device configured to suppress a voltage and current induced in the first electronic device. The induced current may include one or more of a common mode current and a differential mode current. In some embodiments, the protection device may include one or more transformers and capacitors configured to suppress the induced current, and one or more diodes configured to shunt the induced voltage. In some embodiments, the protection device may include one or more inductors configured to suppress an alternating current induced in the first electronic device in a predetermined frequency range. In some embodiments, the protection device may include one or more balun circuits configured to suppress a common mode current induced in the first electronic device over a set of predetermined frequency ranges. In some embodiments, the protection device may be formed separate from the apparatus and the pacing device. In some



embodiments, one or more protection device may be formed integral with one or more of the apparatus and pacing device.

[0112] FIG. 13 is a block diagram illustrating an ablation system (1300) including a protection device (1314) (e.g., filter box), according to some embodiments. In some embodiments, the ablation system (1300) may include a signal generator (1309), a cardiac stimulator (1303), an interface device (1305), a protection device (1314), electrical leads (1317, 1318) of pacing electrodes in a pacing device (1320), and the electrical ablation lead (1311) coupled to an ablation device (e.g., ablation catheter) (1330). The signal generator (1309) and the cardiac stimulator (1303) may be structurally and functionally similar to the signal generator (1250) and the cardiac stimulator (1260) described, respectively, with respect to FIG. 12. The cardiac stimulator (1303) may be configured to generate a pacing signal and deliver it to the pacing device (1320) via the interface device (1305), the protection device (1314), and the electrical leads (1317, 1318). The pacing signal may be further delivered to the signal generator (1309) via the interface device (1305). The interface device (1305) may be configured to allow a connection between two or more of the cardiac stimulator (1303), one or more pacing devices (1320), a signal generator (1309), and ablation device(s) (1330). For example, connectors such as a wired connector (e.g., cable of several meters in length) may be used and allow convenient and adaptable placement of system components within a space such as clinical procedure room. In some embodiments, the signal generator (1309) may be configured to generate the voltage pulse waveform in synchronization with the indication of the pacing signal (e.g., within a common refractory window). The signal generator (1309) may deliver the voltage pulse waveform to the ablation device (1330) via the ablation lead (1311) (e.g., cable) to ablate tissue.

[0113] As the signal generator (1309) delivers the voltage pulse waveform to an ablation device (1330), the cardiac stimulator (1303) may deliver the pacing signal to the pacing device (1320). However, a current may be induced in the pacing device leads (1317, 1318) by the proximity of the pacing device (1320) to the voltage pulse waveform applied to tissue. These unbalanced currents may span a range of frequencies and impact the operation of one or more components of the system such as the cardiac stimulator (1303) and the signal generator (1309). Induced voltages and currents from the pacing device (1320) may interfere with operation of cardiac stimulator (1303) such that the signal generator (1309) does not receive proper pacing signals from the cardiac stimulator (1303). Accordingly, operation of the signal generator (1309) may be interrupted, resulting in error and interruption of ablation energy delivery. The protection devices, as described in more detail herein, may be configured to suppress the propagation of induced currents and voltages in the system.

[0114] FIG. 14 is a block diagram illustrating an ablation system (1400) including an interface device integrated with the protection device (1407) in the same enclosure (e.g., housing) and a cardiac stimulator (1408) integrated with the signal generator (1405) in the same enclosure. A pacing signal may be generated by the integrated signal generator (1405) and cardiac stimulator (1408) and delivered to a pacing device (1430) via the pacing device leads (1417, 1418). The combined interface device and the protection

device (1407) may be configured to suppress (or reduce) the induced currents in the pacing device leads (1417, 1418).

[0115] In some embodiments, one or more electrode channels (1421, 1422) on an intracardiac device (1432) may be used as sensing channels for ECG analysis. The incoming ECG signals received from the patient via electrode channels (1421, 1422) may be filtered through the combined interface and protection device (1407) before they are delivered via leads (1411, 1412) to the signal generator (1405) for ECG analysis. In some embodiments, the ECG signals may also be delivered through leads (1401, 1402) to an ECG recording system (1440) and/or other external device (e.g., sensing and/or mapping systems). The signal generator (1405) may deliver the voltage pulse waveform to an ablation device (1450) via the cable (1409) to ablate tissue.

[0116] In some embodiments, one or more of an interface device (e.g., interface (1305)), a protection device (e.g., protection device (1314)), or an interface and protection device (e.g., interface and protection device (1407)) can be integrated into a cardiac stimulator and/or signal generator (e.g., cardiac stimulator (1303), signal generator (1309), or cardiac stimulator (1408) and signal generator (1405)). For example, in an alternative arrangement, a protection device can be integrated into a signal generator, which in turn is connected to and/or integrated with a cardiac stimulator, such that induced currents from the pacing device leads can be suppressed (e.g., filtered) by the protection device before it reaches the cardiac stimulator.

[0117] FIG. 15 is a circuit diagram of a protection circuit (1500) configured to suppress common mode currents with transformers as being described in the following, according to embodiments. The protection circuit (1500) may be configured to suppress large voltages and common mode currents that are induced in the pacing device leads (1509, 1510) by the pulse waveform generated by the signal generator (not shown in FIG. 15). In addition to suppressing common mode currents, the protection circuit may be configured in a common-differential mode (as described with respect to FIG. 18) and a tunable mode (as described with respect to FIGS. 20A-24). The protection circuit (1500) may include a first common mode transformer L2 (1507), a second common mode transformer L1 (1505), a first capacitor C1 (1502), a second capacitor C2 (1504), and first and second Zener diodes (1513, 1514). In some embodiments, the first diode D1 (1513) and second diode D2 (1514) may be configured to shunt an induced voltage away from the second electronic device (e.g., cardiac stimulator (1501)). The second capacitor C2 (1504) may short relatively high frequencies across the pacing device leads (1509, 1510) and suppress induced currents from flowing in the pacing device leads (1509, 1510). The first common mode transformer (1507) and the second common mode transformer (1505) may be configured to suppress common mode currents in the pacing device leads (1509, 1510), as a high-impedance element for common mode currents. The first capacitor C1 (1502) may short above a predetermined frequency (higher than the lower end of the band-stop range) across the pacing device leads (1509, 1510) and suppress induced currents from flowing in the pacing device leads (1509, 1510).

[0118] The signal generator may be structurally and/or functionally similar to the signal generator (1309) described with respect to FIG. 13 or the signal generator (1405) described with respect to FIG. 14. The protection circuit (1500) may be configured to be coupled to the cardiac

simulator (1501) and the pacing leads (1509, 1510). The pacing device leads (1509, 1510) may be included within a pacing device that may be introduced into a cardiac chamber of a patient (1503). The cardiac stimulator (1501) may be configured to generate a heart pacing signal to be delivered to the patient (1503).

[0119] FIG. 16 illustrates an embodiment of the first common mode transformer L2 (1507) and the second common mode transformer L1 (1505). As shown in FIG. 16, a common mode transformer (1600) may include a toroid core (1608), a first winding 91601, 1611), and a second winding (1602, 1612). The toroid core (1608) may include or be a soft magnetic material that has a high magnetic permeability (e.g., iron) in a ring shape. The toroid core (1608) may be chose to have a construction with relatively broad laminations (for example, a set of laminations where each lamination's cross section has an aspect ratio of 5 or less) to allow losses in the core (1608). The first winding (1601 to 1611) and the second winding (1602 to 1612) wind around a body to the core (1608) in the same direction, while the first winding (1601 to 1611) winds along circumferential axis of the core (1608) in a first direction (e.g., a counterclockwise direction as shown in FIG. 16)) and the second winding (1602 to 1612) winds along the circumferential axis of the core (1608) in a second direction opposite to the first direction (e.g., a clockwise direction as shown in FIG. 16).

[0120] In some embodiments, the outer radius of the core (1608) may be between about 4 cm and about 10 cm, and the inner radius of the core (1608) may be between about 2 cm and about 9 cm, including all values and sub-ranges in-between. In some embodiments, the thickness of the core (1608) may be between about 1 cm and about 6 cm. In some instances, the number of turns (e.g., windings) on each winding (1601, 1602) may be between about 3 and about 50, including all values and sub-ranges in-between.

[0121] In some embodiments, common mode currents with a non-zero frequency may now through the first winding (from 1601 to 1611) and the second winding (from 1602 to 1612) in parallel directions in the leads 1601 and 1602 at the ends of the toroid such that a first magnetic field generated by the current through the first winding (1601 to 1611) and a second magnetic field generated by the current through the first winding (1602 to 1612) may have equal magnitude in the same direction (1615). In other words, the common mode transformer (1600) may generate a magnetic field in a single circumferential direction (i.e., either parallel or anti-parallel to the arrow 1615) when a common mode current flows through the first and second windings. The equal magnitude and in-phase magnetic fields may add together, resulting in a high impedance to the common mode current, which passes through the common mode transformer (1600) heavily attenuated. For example, the common mode power suppression may be at least about 15 dB over a frequency band between about 100 kHz and about 10 MHz, including all values and sub-ranges in-between. In some embodiments, the suppression can be 20 dB, 25 dB or larger. The impedance to the common mode current may further include a real component (e.g., due to resistive core losses) in addition to an imaginary component (e.g., due to a high inductance of the core winding). The actual attenuation (e.g., common mode rejection) depends on the relative magnitudes of the transformer impedance and the load impedance. Thus, the common mode transformer (1600) may be configured to have an inductance of at least about 1

milliHenry and an effective resistance of at least about 500 Ohms. In other words, this impedance may act to suppress, reduce, or minimize the magnitude of the common mode current.

[0122] Returning to FIG. 15, the first common mode transformer (1507) and the second common mode transformer (1505) may be configured to suppress common mode currents in the leads, as a high-impedance element for common mode currents. As shown in FIG. 15, the first common mode transformer (1507) may be positioned between the pacing device leads (1509, 1510) of the first electronic device (1503) (e.g., pacing device) and the first capacitor C1 (1502). The first capacitor C1 (1502) may be used as a low-impedance capacitor for frequencies greater than the lower end (e.g., 0.5 MHz or higher) of the desired band-stop range. Thus, the first capacitor C1 (1502) may be short relatively high frequencies (higher than the lower end of the band-stop range) across the pacing device leads (1509, 1510) and suppress induced currents from flowing in the pacing device leads (1509, 1510).

[0123] As shown in FIG. 15, the second common mode transformer (1505) may be positioned between the first capacitor C1 (1502) and the second capacitor C2 (1502). Similar to the second common mode transformer (1507), the first common mode transformer (1505) may be configured to generate a high impedance, and thus, suppressing, reducing or minimizing the magnitude of the common mode current. Similar to the first capacitor C1 (1502), the second capacitor C2 (1504) may short relatively high frequencies (e.g., voltages having frequencies above a predetermined threshold) across the pacing device leads (1509, 1510) and suppress included currents from flowing in the pacing device leads (1509, 1510).

[0124] The first and second diodes D1 (1513) and D2 (1514) are diodes (e.g., Zener diodes) configured to allow current to flow not only from its anode to its cathode, but also in the reverse direction, when a predetermined voltage (e.g., Zener voltage) is reached. The first and second diodes D1 (1513) and D2 (1514) may be configured to shunt high voltages away from the second electronic device (e.g., cardiac stimulator (1501)).

[0125] FIG. 17 illustrates a circuit diagram of a protection circuit (1700) in a non-tuned common mode format. Similar to the protection circuit (1500) in FIG. 15, the protection circuit (1700) may be configured to suppress an induced voltage and induced common mode currents in pacing device leads (1727, 1728) by the pulse waveform generated by the signal generator (not shown in FIG. 17). Inductors (1724, 1725) (e.g., chokes) may add a further measure of large-current suppression, whether common-mode or differential mode. Further, in addition to the non-tuned common mode, a protection circuit may also include circuitry to operate in a non-tuned common-differential mode (as described with respect to FIG. 18) and a tunable mode (as described with respect to FIGS. 20A-24).

[0126] The signal generator may be structurally and/or functionally similar to the signal generator (1309) described with respect to FIG. 13 or the signal generator (1405) described with respect to FIG. 14. The protection circuit (1700) may be configured to be coupled to the second electronic device (e.g., cardiac stimulator (1701)) and the pacing device leads (1727, 1728). The pacing device leads (1727, 1728) may be included within a first electronic device (e.g., pacing device) (1722) that may be introduced into the

cardiac chamber of a patient. The second electronic device (e.g., cardiac stimulator (1701)) may be configured to generate a heart pacing signal to be delivered to the patient (1722). The protection circuit (1700) may include a first common mode transformer L2 (1718), a second common mode transformer L1 (1717), a first capacitor C1 (1702), a second capacitor C2 (1704), a first and a second diode (1713, 1714), and first and second inductors (e.g., chokes) (1724, 1725).

[0127] The first common mode transformer L2 (1718), the second common mode transformer L1 (1717), the first capacitor C1 (1702), the second capacitor C2 (1704), and the first and second diodes (1713, 1714) may be structurally and/or functionally similar to the first and second diodes (1513, 1514) respectively described with respect to FIG. 15. The first common mode transformer (1719) and the second common mode transformer (1717) may be configured to suppress induced common mode currents in the leads (1727, 1728), as a high-impedance element for common mode currents. The first capacitor C1 (1702) and the second capacitor C2 (1704) may short relatively high frequencies (higher than the lower end of the band-stop range) across the pacing device leads (1727, 1728). The first and second diodes D1 (1713) and D2 (1714) may be configured to shunt high voltages away from the second electronic device (e.g., cardiac stimulator (1701)).

[0128] The first and second inductors (1724, 1725) may be configured to block or suppress induced higher-frequency alternating current (AC), while passing lower-frequency current and/or direct current (DC). In some instances, the first and second inductors (1724, 1725) may be configured to suppress induced currents for frequencies corresponding to the lower or the higher end of a predetermined band-stop range. In some instances, a first inductor (1724) may be coupled to a first pacing device lead (1727) and a second inductor (1725) may be coupled to a second pacing device lead (1728).

[0129] FIG. 18 illustrates a circuit diagram of a protection circuit (1800) in a non-tuned common-differential mode. Similar to the protection circuit (1500) in FIG. 15, the protection circuit (1800) may be configured to suppress induced voltages and induced common mode currents in pacing device leads (1841, 1842) by the pulse waveform generated by a signal generator (now shown in FIG. 18), as well as induced differential mode currents. Generally, the protection circuit may be operated in a non-tuned common mode (as described with respect to FIGS. 15 and 17), a non-tuned common-differential mode (as described herein with respect to FIG. 18), and/or a tunable mode (as described with respect to FIGS. 20A-24).

[0130] The signal generator may be structurally and/or functionally similar to the signal generator (1309) described with respect to FIG. 14 or the signal generator (1405) described with respect to FIG. 14. The protection circuit (1800) may be configured to be coupled to a second electronic device (e.g., cardiac stimulator (1830)) and the first and second pacing device leads (1841, 1842). The first and second pacing device leads (1841, 1842) may be included within a first electronic device (1831) (e.g., pacing device) that may be introduced into a cardiac chamber of a patient. The second electronic device (1830) (e.g., cardiac stimulator) may be configured to generate a heart pacing signal to be delivered to a patient. The protection circuit (1800) may include a first differential mode transformer L3 (1834), a

second common mode transformer L4 (1833), a first capacitor C4 (1802), a second capacitor C3 (1804), and first and second Zener diodes (1837, 1838).

[0131] The first differential mode transformer L3 (1834) is described with respect to FIG. 19. As shown in FIG. 19, a differential mode transformer (1900) includes a toroid core (1908), a first winding (1901, 1911), and a second winding (1902, 1912). The toroid core (1908) may include and/or be composed of a soft magnetic material that has a high magnetic permeability (e.g., iron) in a ring shape. The toroid core (1908) may be chosen to have a construction with relatively broad laminations to allow losses in the core (1908). The first winding (1901, 1911) may wind around a body of the core (1908) in a first direction, and the second winding (1902, 1912) may wind around the body of the core (1908) in a second direction opposite the first direction. The first winding (1901, 1911) may wind along the circumferential axis of the core (1908) in a first direction (e.g., a counterclockwise). Similar to the toroid core (1608) in the common mode transformer (1600) in FIG. 16, in some instances, the outer radius of the core (1908) may be between about 4 cm and about 10 cm, and the inner radius of the core (1908) may be between about 2 cm and about 9 cm. In some instances, the thickness of the core (1908) may be between about 1 cm and about 6 cm. In some instances, the number of turns/windings on each winding (1901, 1902) may be between about 3 and about 50.

[0132] A differential mode current with a non-zero frequency flowing through the first winding (1901, 1911) in a first direction (e.g., left-to-right in FIG. 19) may generate a first magnetic field in the core (1908) in the first direction that is indicated by arrow (1915). In this differential mode, the current flowing through the second winding (1902, 1912) is in a second direction opposite to the first direction (e.g., right-to-left in FIG. 19) and generates a second magnetic field in the same direction (1914). The magnitudes of the first magnetic field and the second magnetic field may be similar or equal. In other words, the winding configuration of the differential mode transformer (1900) allows currents flowing in opposite directions through the first winding (1901, 1911) and the second winding (1902, 1912) (e.g., a differential mode current) to induce a magnetic field in the toroid core (1908) that points in the same direction (1914, 1915). These in-phase magnetic fields may add together, resulting in a high impedance to the differential mode current flowing through the coupled pacing device leads (e.g., 1841, 1842 in FIG. 18). The impedance to the differential mode current may further include a real component (e.g., due to resistive core losses) in addition to an imaginary component (e.g., due to high inductance of the core winding). This high impedance of the differential mode transformer (1900) may act to suppress, reduce, or minimize the magnitude of the differential mode transformer (1900) may act to suppress, reduce or minimize the magnitude of the differential mode transformer L4 (1822) may be configured to suppress induced common mode current and induced differential mode current in a predetermined band-stop frequency range.

[0133] Returning to FIG. 18, the common mode transformer L4 (1833), the first capacitor C4 (1802), the second capacitor C3 (1804), and the first and second diodes (1837, 1838) are structurally and/or functionally similar to the second common mode transformer L1 (1505), the first capacitor C1 (1502), the second capacitor C2 (1504), and the

first and second diodes (1513, 1514) respectively described with respect to FIG. 15. The common mode transformer L4 (1833) may be configured to suppress common mode currents in the leads, as a high-impedance element for common mode currents. The first capacitor C4 (1802) and the second capacitor C3 (1804) may short relatively high frequencies (higher than the lower end of the band-stop range) across the pacing device leads (1841, 1842) and suppress induced currents from flowing in the pacing device leads (1841, 1842). The first and second diodes D3 (1837) and D4 (1838) may be configured to shunt high voltages away from the second electronic device (e.g., cardiac stimulator (1830)).

[0134] FIGS. 20A-20B illustrates a circuit diagram of a protection device including a tunable circuit (2020) (e.g., balun circuit). The balun circuit (2020) may be configured to join a balanced line (e.g., two conductors with equal currents in opposite directions) to an unbalanced line (e.g., on conductor and a ground, coaxial cable). The balun circuit (2020) may be configured to convert an unbalanced signal to a balanced signal by suppressing common mode currents. In some embodiments, the balun circuit (2020) may be configured to provide impedance transformation in addition to conversion between balanced and unbalanced signal modes. The balun circuit (2020) may be implemented in any of the protection devices described herein (e.g., described with respect to FIGS. 15, 17, and 18).

[0135] As shown in FIG. 20A, in some embodiments, the balun circuit (2020) may be coupled to a first and second inductor (e.g., choke) (2003, 2004). The first and second inductors (2003, 2004) may be configured to suppress large currents for frequencies corresponding to the lower or the high end of a predetermined band-stop range. In some embodiments, a first inductor (2003) may be coupled to a first pacing device lead (2001) and a second inductor (2004) may be coupled to a second pacing device lead (2002). The pacing device leads (2001, 2002) may be coupled to a first electronic device (not shown) that is introduced into a cardiac chamber of a patient. The balun circuit (2020) may be coupled to a second electronic device (e.g., cardiac stimulator) (not shown) via third and fourth leads (2011, 2012).

[0136] In some embodiments, the balun circuit (2020) may include a coaxial cable (2009) formed into a coaxial winding (2016) having a center conductor (configured to carry a signal) connected to first lead (2001) at a first junction (2006) and having a shield conductor (configured to connect to electrical ground) connected in series with a resistor (2015) may be coupled in parallel and across the length of the coaxial cable (2009) at a second junction (2007) and third junction (2018) (i.e., the capacitor (2014) and the resistor (2015) can be connected to a shield conductor of the coaxial cable (2009) at second junction (2007) and third junction (2018). The coaxial cable (2009) may be coupled to third and fourth leads (2011, 2012).

[0137] FIG. 20B illustrates an equivalent circuit diagram (2050) of the tunable balun circuit (2020) in FIG. 20A. A coaxial shield conductor of the coaxial winding (2016) is represented, as shown in FIG. 20B, by an equivalent inductor (2051). The inductor (2051) may be coupled parallel with a serially-connected capacitor (2053) and resistor (2052). The capacitor (2053) may be configured to resonate with the inductor (2051). For example, a vector network analyzer may be configured to select a capacitance value of the capacitor (2053). A resistance value of the resistor (2052)

may be selected to configure a width and height of a resonance peak as described in more detail with respect to FIGS. 21, 23, and 24.

[0138] FIG. 21 is a schematic illustration of a resonance peak obtained using a tunable circuit (e.g., balun circuit). The balun circuit can be structurally and/or functionally similar to the balun circuit (2020) described with respect to FIG. 20A. When power is transmitted through the shield of the coaxial winding the ratio ( $P_r$ ) of the power transmitted through the shield of a winding may be measured with, for example, a network analyzer. The plot in FIG. 21 illustrates the logarithmic function of transmitted power ( $P_r$ ) (2102) as a function of the frequency  $f$  (2101). The resonance has a peak (2170) at a frequency  $f_0$  (2171) where the height  $A_m$  of the peak (2175) corresponds to the power attenuation of  $f_0$ . the resonance frequency of  $f_0$  (2171) may be adjusted by selecting the capacitance value of a capacitor (e.g., capacitor (2014) in FIG. 20A) of a tunable circuit. In some instances, the circuit may be configured for high power attenuation. The breadth of the resonance peak (2173) may be measured, for example, by the full-width (2173) of the resonance peak at half-maximum height and may be adjusted by the resistance value of a resistor (e.g., resistor (2015) in FIG. 20A). In some instances, increasing a resistance value increases the breadth and reduces the height (2175) of the resonance peak (2173).

[0139] FIG. 22 is a schematic illustration of a set of tunable circuits (e.g., sets of balun circuits). Each tunable balun circuit in the set of tunable circuits (2205, 2206, 2207) may be structurally and/or functionally similar to the tunable circuit (2020) as described with respect to FIG. 20A. The set of tunable circuits (2205, 2206, 2207) may be collectively configured as a broadband common mode suppression circuit in a protection device.

[0140] In some embodiments, the set of tunable circuits (2205, 2206, 2207) may be coupled to first and second inductors (e.g., chokes) (2221, 2223). The first and second inductors (2221, 2223,) may be configured to suppress induced currents for frequencies corresponding to the lower or higher end of a predetermined band-stop range. For example, a first inductor (2221) may be coupled to a first pacing device lead (2201) and a second inductor (2223) may be coupled to a second pacing lead (2203). The first and second pacing device leads (2201, 2203) may be coupled to a first electronic device (e.g., pacing device) that is introduced into a cardiac chamber of a patient. The set of tunable circuits (2205, 2206, 2207) may be interconnected through respective coaxial lines (2209, 2210) and configured to attenuate common mode currents over a set of predetermined frequency ranges. Each tunable circuit in the set of tunable circuits (2205, 2206, 2207) may be configured for different frequency ranges such that their frequency response partially overlap (described with respect to FIG. 23). Thus, the set of tunable circuits (2205, 2206, 2207) may provide attenuation over overlapping frequency ranges to suppress common mode currents over a predetermined range of frequencies. The other end of the set of tunable circuits (2205, 2206, 2207) may be coupled to a second electronic device (e.g., cardiac stimulator) via third and fourth inductors (2227, 2229). The third and fourth inductors (2227, 2229) may be configured to suppress currents corresponding to frequencies on a lower or higher end of a predetermined band-stop range.

[0141] FIG. 23 is a schematic illustration of a set of resonance peaks of each tunable circuit of a set of tunable circuits (e.g. the set of tunable circuits (2205, 2206, 2207) in FIG. 22), according to embodiments. The power (X) is transmitted through a coaxial winding and may be measured with, for example, a network analyzer. The plot in FIG. 23 illustrates the logarithmic function of the transmitted power (X) (2302) as a function of the frequency f (2301). FIG. 23 illustrates the respective resonance peaks (2305, 2306, 2307) of each tunable circuit of a set of tunable circuits (e.g., the set of tunable circuits (2205, 2206, 2207) in FIG. 22). For example, each tunable circuit in the set of the tunable circuits (2205, 2206, 2207) may be configured such that the frequency response curves partially overlap.

[0142] FIG. 24 is a schematic illustration of a suppression of common mode currents by a set of tunable circuits (e.g., the set of tunable circuits (2205, 2206, 2207) in FIG. 22). The set of tunable circuits (e.g. the set of tunable balun circuits (2205, 2206, 2207) in FIG. 22) may have overlapping resonance frequencies, as described with respect to FIG. 23. FIG. 24 illustrates a net resonance peak (2403) from the set of tunable circuits. Similar to FIG. 23, the plot in FIG. 24 illustrates the logarithmic function of transmitted power (P<sub>r</sub>) (2402) as a function of the frequency f (2401). The net effect of the set of tunable circuits may be broadband suppression over a range (2409) of frequencies from the partially overlapping frequency ranges.

#### Methods

[0143] Also described here are methods for protecting electronic circuitry from induced currents during a tissue ablation process performed in a heart chamber using the systems and devices described herein. The heart chamber may be the left atrial chamber and include its associated pulmonary veins. Generally, the methods described here include introducing and disposing a pacing device (e.g., pacing device (160), pacing device (1207)) in contact with one or more heart chambers. The pacing device may deliver a pacing signal to the heart using a cardiac stimulator (e.g., stimulator (150)) and/or measure cardiac activity. An ablation device (140, 1212) may be introduced and disposed in contact with one or more pulmonary vein ostial or antral regions. A pulse waveform may be delivered by one or more electrodes (e.g., electrodes (1213)) of the ablation device to ablate tissue. The pacing device may be electrically coupled to the system and configured to deliver pacing signals to the heart and/or measure cardiac activity. In some embodiments, a protection device may suppress the induced current and voltage from a first electronic device.

[0144] Additionally or alternatively, the pulse waveforms may include a plurality of levels of a hierarchy to reduce total energy delivery, e.g., as described in International Application Serial No. PCT/US2016/057664, filed Oct. 19, 2016, titled "Systems, apparatuses and methods for deliver of ablative energy to tissue," and incorporated herein by reference. The tissue ablation thus performed may be delivered in synchrony with paced heartbeats to reduce the risk of atrial and/or ventricular fibrillation and damage to healthy tissue. It should be appreciated that any of the ablation devices described herein (e.g., ablation device (140), ablation device (1212)) may be used to ablate tissue using the methods discussed below as appropriate.

[0145] In some embodiments, the ablation devices (e.g., ablation device (140), ablation device (1212)) described

herein may be used for focal ablation of cardiac features/structures identified to cause arrhythmia (e.g., fibrillation). For example, a cardiac electrophysiology diagnostic catheter (e.g., mapping catheter) may be used to map areas with sources of arrhythmias that may be subsequently ablated through focal ablation using any of the ablation devices described herein. Focal ablation may, for example, create spot lesion that neutralizes a rotor while sparing surrounding tissue. In some embodiments, one or more focal ablation lesions may be formed in combination with one or more box or line lesions to treat cardiac arrhythmia. As a non-limiting example, in some embodiments, a system may include one or more mapping catheters and one or more ablation devices (e.g., ablation device 9140), ablation device (1212)) useful for creating lesions via focal ablation. Examples of suitable focal ablation catheters are described in International Application Serial No. PCT/US2019/014226, as incorporated herein by reference.

[0146] FIG. 25 is an example method (2500) of tissue ablation. In some embodiments, the voltage pulse waveforms described herein may be applied during a refractory period of the cardiac cycle so as to avoid disruption of the sinus rhythm of the heart. The method (2500) includes introduction of a pacing device (e.g., pacing device (160, 1207)) into an endocardial space of a right ventricle, at (2502). The pacing device may be advanced to be disposed in contact with the right ventricle, at (2504). For example, sensor electrodes may be configured for cardiac activity measurement (e.g., ECG signals) and pacing electrodes may be configured for delivering pacing signals may be disposed in contact with an inner surface of the right ventricle. An ablation device (e.g., ablation device (140, 1212)) may be introduced into an endocardial space of a left atrium, at (2506). The ablation device may be advanced to be disposed in contact with a pulmonary vein ostium, at (2508). In one non-limiting example, electrodes of an ablation device may form an approximately circular arrangement of electrodes disposed in contact with an inner radial surface at a pulmonary vein ostium. In some embodiments, a pacing signal may be generated by a cardiac stimulator (e.g., cardiac stimulator (150, 1260)) for cardiac stimulation of the heart, at (2510). The pacing signal may then be applied to the heart, at (2512), using the pacing electrodes of the pacing device. For example, the heart may be electrically paced with the pacing signal to ensure pacing capture to establish periodicity and predictability of the cardiac cycle. One or more of atrial and ventricular pacing may be applied. Examples of applied pacing signals relative to patient cardiac activity are described in more detail herein.

[0147] In some embodiments, pacing capture may be automatically confirmed by one or more of the signal generator, processor (e.g., signal generator (110, 1250)), and confirmed by a user. For example, the user may confirm pacing capture using a user interface (e.g., an input/output device such as a touch screen monitor or other type of monitor) based on measured cardiac activity signals. If the signal generator, processor, and/or the user viewing the displayed cardiac output, determines that there is an absence of pacing captured, pulse waveform generation may be prohibited and the user may be prompted to adjust system parameters by, for example, repositioning the pacing device to improve tissue engagement and/or modify pacing signal parameters (e.g., pulse width, pulse amplitude, pulse frequency, etc.).

[0148] In some embodiments, the pacing device may further measure cardiac activity (e.g., ECG signals) corresponding to electrical cardiac activity of the heart, at (2514). For example, the measured cardiac activity may include a measured cardiac pacing pulse, R-wave, and high voltage signals. One or more of the measured cardiac activity parameters may be used to generate protection signal.

[0149] The generated pulse waveform may be delivered to tissue for ablation, at (2516). In some embodiments, a refractory time period may follow a pacing signal. For example, a common refractory time period may be between both atrial and ventricular refractory time windows. A voltage pulse waveform may be applied in the common refractory time period. In some embodiments, the pulse waveform may be generated with a time offset with respect to the indication of the pacing signal. For example, the start of a refractory time period may be offset from the pacing signal by a time offset. The voltage pulse waveform(s) may be applied over a series of heartbeats over corresponding common refractory time periods.

[0150] Current and voltage induced in the pacing device are suppressed from an electronic device by a protection device, at (2518). For example, the protection device suppresses an induced current and voltage from coupling to a cardiac stimulator and/or other components coupled to the protection device.

[0151] In some embodiments, hierarchical voltage pulse waveforms having a nested structure and a hierarchy of time intervals as described herein may be useful for irreversible electroporation, providing control and selectivity in different tissue types. FIG. 26 is a flowchart (2600) of another example method of a tissue ablation process. The method (2600) includes the introduction of a device (e.g., ablation device (140, 1212)) into an endocardial space of a left atrium, at (2602). The device may be advanced to be disposed in a pulmonary vein ostium, at (264). In embodiments where the device may include a first and second configuration (e.g., compact and expanded), the device may be introduced in the first configuration and transformed to a second configuration to contact tissue at or near the pulmonary vein antrum or ostium, at (2606). The device may include electrodes and may be configured in anode-cathode subsets, at (2608), as discussed in detail above. For example, subset of electrodes of the device may be selected as anodes, while another subset of electrodes of the devices may be selected as anodes, while another subset of electrodes of the device may be selected as cathodes, with the voltage pulse waveform applied between the anodes and cathodes.

[0152] A pulse waveform may be generated by a signal generator (e.g., the signal generator (110)) and may include a plurality of levels in a hierarchy, at (2660). A variety of hierarchical waveforms may be generated with a signal generator as disclosed herein. For example, the pulse waveform may include a first level of a hierarchy of the pulse waveform including a first set of pulses. Each pulse has a pulse time duration and a first time interval separating successive pulses. A second level of the hierarchy of the pulse waveform may include a plurality of first sets of pulses. The second time interval may be at least three times the duration of the first time interval. A third level of hierarchy of the pulse waveform may include a plurality of second sets of pulses as a third set of pulses. A third time interval may separate successive second sets of pulses. The third time interval may be at least thirty times the duration

of eh second level time interval. The pulse waveform generated by the signal generator (e.g., signal generator (110)) may be delivered to tissue using the ablation device, at (2612).

[0153] It is understood that while the examples herein identify separate monophasic and biphasic waveforms, it should be appreciated that combination waveforms, where some portions of the waveform hierarchy are monophasic while other portions are biphasic, may also be generated. A voltage pulse waveform having a hierarchical structure may be applied across different anode-cathode subsets (optionally with a time delay). As discussed above, one or more of the waveforms applied across the anode-cathode subsets may be applied during the refractory period of a cardiac cycle. The pulse waveform may be delivered to tissue. It should be appreciated that the steps described in FIGS. 25 and 26 may be combined and modified as appropriate.

[0154] In some embodiments, the pulse waveform may be delivered to pulmonary vein ostium of a patient via a set of splines of an ablation device. In some embodiments, voltage pulse waveforms as described herein may be selectively delivered to electrode subsets such as anode-cathode subsets for ablation and isolation of the pulmonary vein. For example, a first electrode of a group of electrodes may be configured as an anode and a second electrode of the group of electrodes may be configured as a cathode. These steps may be repeated for a desired number of pulmonary vein ostial or antral regions to have been ablated (e.g., 1, 2, 3, or 4 ostia). Suitable examples of ablation devices and methods are described in International Application No. PCT/US2019/014226.

[0155] FIG. 6 is a method (600) for one embodiment of a signal generation process using the systems and devices described herein. The method (600) includes controlling a state of a first and second electronic switch of a first electrode channel, at (602). For example, (602) may control a state of first electronic switch (220) and second electronic switch (230) of the first electrode channel (201) illustrated in FIG. 2. In some embodiments, a drive circuit (e.g., drive circuits (222, 232)) coupled to an electronic switch may be configured to control the state of the electronic switch. In some embodiments, the electronic switch may be configured to switch between an ON state and an OFF state using the drive circuit. The first electrode channel may be configured as an anode, at (604). A state of a first and second electronic switch of a second electrode channel may be controlled, at (606) by, for example, drive circuits controlling the ON/OFF states of respective electronic switches. The second electrode channel may be configured as a cathode, at (608).

[0156] In some embodiments, hierarchical voltage pulse waveforms having a nested structure and a hierarchy of time intervals, as described herein, may be useful for irreversible electroporation, as well as providing control and selectivity in different tissue types. In some embodiments, a first pulse waveform may be generated having a set of levels of a hierarchy, at (610). In some embodiments, a first level of a hierarchy of the first pulse waveform may include a first set of pulses, with each pulse having a pulse time duration. A first time interval may separate successive pulses. A second level of the hierarchy of the first pulse waveform may include a set of first sets of pulses as a second set of pulses with a second time interval separating successive first sets of pulses. The second time interval may be at least three times the duration of the first time interval. A third level of the

hierarchy of the pulse waveform may include a set of second sets of pulses as a third set of pulses with a third time interval separating successive second sets of pulses. The third time interval may be at least thirty times the duration of the second level time interval. An energy source may deliver the first pulse waveform to a set of electrodes during use via the first electrode channel and the second electrode channel, at (612). The first pulse waveform may be delivered at a first time.

[0157] At a second time subsequent to the first time, the state of the first and second electronic switch of the first electrode channel may be controlled, at (614). The first electrode channel may be configured as a cathode, at (616). The state of the first and second electronic switch of the second electrode channel may be controlled, at (618). The second electrode channel may be configured as an anode, at (620). In some embodiments, a second pulse waveform may be generated having a set of levels of a hierarchy, at (622), such as including the first, second, and third hierarchy levels described herein. The energy source may deliver the second pulse waveform to the set of electrodes during use via the first electrode channel and the second electrode channel at the second time, at (624).

#### Pulse Waveform

[0158] Disclosed herein are methods, systems and devices for the selective and rapid application of pulsed electric fields/waveforms to effect tissue ablation with irreversible electroporation. The pulse waveform(s) as disclosed herein are useable with any of the systems (100, 200, 300), ablation devices (e.g., 140, 400, 500), and methods (e.g., 600) described herein. Some embodiments are directed to pulsed high voltage waveforms together with a sequenced delivery scheme for delivering energy to tissue via sets of electrodes. In some embodiments, peak electric field values may be reduced and/or minimized while at the same time sufficiently large electric field magnitudes may be maintained in regions where tissue ablation is desired. This also reduces the likelihood of excessive tissue damage or the generation of electrical arcing, and locally may include a signal generator capable of being configured to deliver pulsed voltage waveforms to a set of electrodes of an ablation device. In some embodiments, a processor of the signal generator is configured to control a set of electrode channels whereby selected pairs of anode-cathode subsets of electrodes may be sequentially triggered based on a predetermined sequence, and in one embodiment the sequenced delivery may be triggered from a cardiac stimulator and/or pacing device. In some embodiments, the ablation pulse waveforms may be applied in a refractory period of the cardiac cycle so as to avoid disruption of the sinus rhythm of the heart. One example method of enforcing this is to electrically pace the heart with a cardiac stimulator (e.g., cardiac stimulator (150)) and ensure pacing capture to establish periodicity and predictability of the cardiac cycle, and then to define a time window well within the refractory period of this periodic cycle within which the ablation waveform is delivered.

[0159] In some embodiments, the pulsed voltage waveforms disclosed herein are hierarchical in organization and have a nested structure. In some embodiments, the pulsed waveform includes hierarchical groupings of pulses with a variety of associated timescales. Pulsed waveforms for electroporation energy delivery as disclosed herein may enhance the safety, efficiency and effectiveness of the energy delivery

by reducing the electric field threshold associated with irreversible electroporation, yielding more effective ablative lesions with reduced total energy delivered. This in turn may broaden the areas of clinical application of electroporation including therapeutic treatment of a variety of cardiac arrhythmias.

[0160] FIG. 7 illustrates a pulsed voltage waveform in the form of a sequence of rectangular double pulses, with each pulse, such as the pulse (700) being associated with a pulse width or duration. The pulse width/duration may be about 0.5 microseconds, about 1 microsecond, about 5 microseconds, about 10 microseconds, about 25 microseconds, about 50 microseconds, about 100 microseconds, about 125 microseconds, about 140 microseconds, about 150 microseconds, including all values and sub-ranges in between. The pulsed waveform of FIG. 7 illustrates a set of monophasic pulses where the polarities of all the pulses are the same (all positive in FIG. 7, as measured from a zero baseline). In some embodiments, such as for irreversible electroporation applications, the height of each pulse (700) or the voltage amplitude of the pulse (700) may be in the range from about 400 V, about 1,000 V, about 5,000 V, about 10,000 V, about 15,000 V, including all values and sub ranges in between. As illustrated in FIG. 7, the pulse (700) is separated from a neighboring pulse by a time interval (702), also sometimes referred to as a first time interval. The first time interval may be about 10 microseconds, about 50 microseconds, about 100 microseconds, about 200 microseconds, about 500 microseconds, about 800 microseconds, about 1 millisecond including all values and sub ranges in between, in order to generate irreversible electroporation.

[0161] FIG. 8 introduces a pulse waveform with the structure of a hierarchy of nested pulses. FIG. 8 shows a series of monophasic pulses such as pulse (800) with pulse width/pulse time duration  $w$ , separated by a time interval (also sometimes referred to as a first time interval) such as (802) of duration  $t_1$  between successive pulses, a number  $m_1$  of which are arranged to form a group of pulses (810) also sometimes referred to as a first time interval) such as (802) of duration  $t_1$  between successive pulses, a number  $m_1$  of which are arranged to form a group of pulses (810) (also sometimes referred to as a first set of pulses). Furthermore, the waveform has a number  $m_2$  of such groups of pulses (also sometimes referred to as a second set of pulses) separated by a time interval (812) (also sometimes referred to as a second time interval) of duration  $t_2$  between successive groups. The collection of  $m_2$  such pulse groups, marked by (820) in FIG. 8, constitutes the next level of the hierarchy, which may be referred to as a packet and/or as a third set of pulses. The pulse width and the time interval  $t_1$  between pulses may both be in the range of microseconds to hundreds of microseconds, including all values and sub ranges in between. In some embodiments, the time interval  $t_2$  may be at least three times larger than the time interval  $t_1$ . In some embodiments, the ratio  $t_2/t_1$  may be in the range between about 3 and about 300, including all values and sub-ranges in between.

[0162] FIG. 9 further elaborates the structure of a nested pulse hierarchy waveform. In this figure, a series of  $m_1$  pulses (individual pulses not shown) form a group of pulses (902) (e.g., a first set of pulses). A series of  $m_2$  such groups separated by an inter-group time interval (910) of duration  $t_2$  (e.g., a second time interval) between one group and the next form a packet (910) (e.g., a second set of pulses). A



series of X such packets separated by time intervals (912) of duration  $t_3$  (e.g., a third time interval) between one packet and the next form the next level in the hierarchy, a super-packet labeled (920) (e.g., a third set of pulses) in the figure. In some embodiments, the time interval  $t_3$  may be at least about thirty times larger than the time interval  $t_2$ . In some embodiments, the time interval  $t_3$  may be at least fifty times larger than the time interval  $t_2$ . In some embodiments, the ratio  $t_{23}/t_2$  may be in the range between about 30 and about 800, including all values and sub ranges in between. The amplitude of the individual voltage pulses in the pulse hierarchy may be anywhere in the range from 500 V to 7,000 V or higher, including all values and sub ranges in between.

[0163] FIG. 10 provides an example of a biphasic waveform sequence with a hierarchical structure. In the example shown in the figure, biphasic pulses (1000) have a positive voltage portion as well as a negative voltage portion to complete one cycle of the pulse. There is a time delay (1002) (e.g., a first time interval) between adjacent cycles of duration  $t_1$ , and  $n_1$  such cycles from a group of pulses (1010) (e.g., the first set of pulses). A series of  $n_2$  such groups separated by an inter-group time interval (1012) (e.g., a second time interval) of duration  $t_3$  between one group and the next from a packet (1020) (e.g., a second set of pulses). The figure also shows a second packet (1032), with a time delay (1030) (e.g., the third time interval) of duration  $t_3$  between the packets. Just as for monophasic pulses, higher levels of the hierarchical structure may be formed as well. The amplitude of each pulse or the voltage amplitude of the biphasic pulse may be as well. The amplitude of each pulse or the voltage amplitude of the biphasic pulse may be anywhere in the range from 500 V to 7,000 V or higher, including all values and sub ranges in between. The pulse width/pulse time duration may be in the range from nanoseconds or even sub-nanoseconds to tens of microseconds, while the delays  $t_1$  may be in the range from zero to several microseconds. The inter-group time interval  $t_2$  may be at least ten times larger than the pulse width. In some embodiments, the time interval  $t_3$  may be at least about twenty times larger than the time interval  $t_2$ . In some embodiments, the time interval  $t_3$  may be at least fifty times larger than the time interval  $t_2$ .

[0164] Embodiments disclosed herein may include waveforms structured as hierarchical waveforms that include waveform elements/pulses at various levels of the hierarchy, and have an associated pulse time duration and a first time interval between successive pulses. A set of pulses, or elements of the first level structure, form a second level of the hierarchy such as the group of pulses/second set of pulses (810) in FIG. 8. Among other parameters associated with the waveform are parameters such as a total time duration of the second set of pulses (not shown), a total number of first level elements/first set of pulses, and second time intervals between successive first level elements that describe the second level structure/second set of pulses. In some embodiments, the total time duration of the second set of pulses may be between about 20 microseconds and about 10 milliseconds, including all values and sub ranges in between. A set of groups, second set of pulses, or elements of the second level structure, form a third level of the hierarchy such as the packet of groups/third set of pulses (820) in FIG. 8. Among other parameters, there is a total time duration of the third set of pulses (not shown), a total number of second level elements/second set of pulses, and

third time intervals between successive second level elements that describe the third level structure/third set of pulses. In some embodiments, the total time duration of the third set of pulses may be between about 60 microseconds, and about 200 milliseconds, including all values and sub ranges in between. The generally iterative or nested structure of the waveforms may continue to a higher plurality of levels, such as ten levels of structure, or more.

[0165] In some embodiments, hierarchical waveforms with a nested structured and hierarchy of time intervals as described herein may be useful for irreversible electroporation ablation energy delivery, providing a good degree of control and selectivity for applications in different tissue types. A variety of hierarchical waveforms may be generated with a suitable pulse generator of the type described in this disclosure. It is understood that while the examples herein identify separate monophasic and biphasic waveforms for clarity, it should be noted that combination waveforms, where some portions of the waveform hierarchy are monophasic while other portions are biphasic, may also be generated/implemented.

[0166] In some embodiments, the ablation pulse waveforms described herein may be applied during the refractory period of the cardiac cycle so as to avoid disruption of the sinus rhythm of the heart. In some embodiments, a method of treatment may include electrically pacing the heart with a cardiac stimulator (e.g., cardiac stimulator (150)) to ensure pacing capture to establish periodicity and predictability of the cardiac cycle, and then defining a time window within the refractory period of the cardiac cycle within which one or more pulsed ablation waveforms may be delivered. FIG. 11 illustrates an example where both atrial and ventricular pacing is applied (for instance, with pacing leads or catheters situated in the right atrium and right ventricle respectively). With time represented on the horizontal axis, FIG. 11 illustrates a series of ventricular pacing signals (1100, 1110), and a series of atrial pacing signals (1120, 1130), along with a series of ECG waveforms (1140, 1142) that are driven by the pacing signals. As indicated in FIG. 11 by the thick arrows, there is an atrial refractory time window (1122) and a ventricular refractory time window (1102) that respectively follow the atrial pacing signal (1120) and the ventricular pacing signal (1100). As shown in FIG. 11, a common refractory time window (1150) on duration  $T_r$  may be defined that lies within both atrial and ventricular refractory time windows (1122, 1102). In some embodiments, the electroporation ablation waveform(s) may be applied in this common refractory time window (1150). The start of this refractory time window (1150) is offset from the pacing signal (1100) by a time offset (1104) as indicated in FIG. 11. The time offset (1104) may be smaller than about 25 milliseconds, in some embodiments. At the next heartbeat, a similarly defined common refractory time window (1152) is the next time window available for application of the ablation waveform(s). In this manner, the ablation waveform (s) may be applied over a series of heartbeats, at each heartbeat remaining within the common refractory time window. In one embodiment, each packet of pulses as defined above in the pulse waveform hierarchy may be applied over a heartbeat, so that a series of packets is applied over a series of heartbeats, for a given electrode, followed by a second waveform packet delivered over a second sequence of electrodes, and so on; in some cases, it may even be convenient for the second sequence of electrodes to be



different from the first sequence of electrodes. The architecture of the signal generator and routing console as disclosed herein permits the delivery of a variety of such hierarchical waveforms wherein waveform packet delivery to a given set of electrodes, in the sense disclosed herein, may be interspersed with waveform packet deliveries to a different set of electrodes. The modality of interspersed waveform delivery described herein may include monophasic, biphasic, and mixed pulses that include both monophasic and biphasic components.

**[0167]** It is understood that while the examples herein identify separate monophasic and biphasic waveforms, it should be appreciated that combination waveforms, where some portions of the waveform hierarchy are monophasic while other portions are biphasic, may also be generated. A voltage pulse waveform having a hierarchical structure may be applied across different anode-cathode subsets (optionally with a time delay). As discussed above, one or more of the waveforms applied across the anode-cathode subsets may be applied during the refractory period of a cardiac cycle. It should be appreciated that the method steps described herein may be combined and modified as appropriate. Likewise, while the examples of electrode channel selection disclosed herein describe the selection of one anode and two cathode channels, it should be clear that a wide variety of channels may be selected to act as anodes or cathodes, without limitation.

**[0168]** It should be understood that while cardiac ablation devices are described here for ablation, the protection devices and embodiments disclosed herein are applicable to other clinical procedures and other ablation devices can be used without departing from the scope of the present invention.

**[0169]** As used herein, the terms “about” and/or “approximately” when used in conjunction with numerical values and/or ranges generally refer to those numerical values and/or ranges near to a recited numerical value and/or range. In some instances, the terms “about” and “approximately” may mean within  $\pm 10\%$  of the recited value. For example, in some instances, “about 100 [units]” may mean within  $\pm 10\%$  of 100 (e.g., from 90 to 100). The terms “about” and “approximately” may be used interchangeably.

**[0170]** Some embodiments described herein relate to a computer storage product with a non-transitory computer-readable medium (also may be referred to as a non-transitory processor readable medium) having instructions or computer code thereon for performing various computer-implemented operations. The computer-readable medium (or processor-readable medium) is non-transitory in the sense that it does not include transitory propagating signals per se (e.g., a propagating electromagnetic wave carrying information on a transmission medium such as space or a cable). The media and computer code (also may be referred to as code or algorithm) may be those designed and constructed for the specific purpose or purposes. Examples of non-transitory computer-readable media include, but are not limited to, magnetic storage media such as hard disks, floppy disks, and magnetic tape, optical storage media such as Compact Disc/Digital Video Discs (CD/DVDs); Compact Disc-Read Only Memories (CD-ROMs), and holographic devices; magneto-optical storage media such as optical disks; solid state storage devices such as a solid state drive (SSD) and a solid state hybrid drive (SSHD); carrier wave signal processing modules; and hardware devices that are specially

configured to store and executed program code, such as Application-Specific Integrated Circuits (ASICs), Programmable Logic Devices (PLDs), Read-Only Memory (ROM), and Random-Access Memory (RAM) devices. Other embodiments described herein relate to a computer program product, which may include, for example, the instructions and/or computer code disclosed herein.

**[0171]** The systems, devices, and/or methods described herein may be performed by software (executed on hardware), hardware, or a combination thereof. Hardware modules may include, for example, a general-purpose processor (or microprocessor or microcontroller), a filed programmable gate array (FPGA), and/or an application specific integrated circuit (ASIC). Software modules (executed on hardware) may be expressed in a variety of software languages (e.g., computer code), including C, C++, Java®, Python, Ruby, Visual Basic®, and/or other object-oriented, procedural, or other programming language and development tools. Examples of computer code include, but are not limited to, micro-code or micro-instructions, machine instructions, such as produced by a compiler, code used to produce a web service, and files containing higher-level instructions that are executed by a computer using an interpreter. Additional examples of computer code include, but are not limited to, control signals, encrypted code, and compressed code.

**[0172]** In some embodiments, the systems, devices, and methods may be in communication with other computing devices (not shown) via, for example, one or more networks, each of which may be any type of network (e.g., wired network, wireless network). A wireless network may refer to any type of digital network that is not connected by cables of any kind. Examples of wireless communication in a wireless network include, but are not limited to cellular, radio, satellite, and microwave communication. However, a wireless network may connect to a wired network in order to interface with the Internet, other carrier voice and data networks, business networks, and personal networks. A wired network is typically carried over copper twisted pair, coaxial cable and/or fiber optic cables. There are many different types of wired networks including wide area networks (WAN), metropolitan area networks (MAN), local area networks (LAN), Internet area networks (IAN), campus area networks (CAN), global area networks (GAN), like the Internet, and virtual private networks (VPN). Hereinafter, network refers to any combination of wireless, wired, public and private data networks that are typically interconnected through the Internet, to provide a unified networking and information access system.

**[0173]** Cellular communication may encompass technologies such as GSM, PCS, CDMA or GPRS, W-CDMA, EDGE or CDMA2000, LTE, WiMAX, and 5G networking standards. Some wireless network deployments combine networks from multiple cellular networks or use a mix of cellular, Wi-Fi, and satellite communication. In some embodiments, the systems, devices, and methods described herein may include a radiofrequency receiver, transmitter, and/or optical (e.g., infrared) receiver and transmitter to communicate with one or more devices and/or networks.

**[0174]** The specific examples and descriptions herein are exemplary in nature and embodiments may be developed by those skilled in the art based on the material taught herein without departing from the scope of the present invention, which is limited only by the attached claims.

We claim:

1. An apparatus, comprising:
  - a protection device coupleable between a first electronic device and a second electronic device, the first electronic device disposable near tissue of a subject such that currents can be induced in the first electronic device by voltage pulse waveforms delivered to a set of electrodes near tissue, the protection device including:
    - a set of balun circuits connected in series and collectively configured to reduce the currents induced in the first electronic device over a set of predetermined frequency ranges.
2. The apparatus of claim 1, further comprising: a signal generator configured to generate the voltage pulse waveform, the signal generator coupled to the set of electrodes and configured to deliver the voltage pulse waveform to the set of electrodes.
3. The apparatus of claim 2, further comprising: a cardiac stimulator configured to deliver pacing signals to the first electronic device, the cardiac stimulator being the second electronic device.
4. The apparatus of claim 2, wherein the first electronic device is a pacing device including a set of leads, and the second electronic device is a cardiac stimulator configured to deliver pacing signals to the pacing device.
5. The apparatus of claim 2, wherein each predetermined frequency range of the set of predetermined frequency ranges at least partially overlaps at least one other predetermined frequency range of the set of predetermined frequency ranges.
6. The apparatus of claim 5, wherein each predetermined frequency range of the set of predetermined frequency ranges has a resonance peak associated with a balun circuit of the set of balun circuits.
7. The apparatus of claim 2, wherein the protection device further includes one or more inductors configured to reduce an alternating current of the currents induced in the first electronic device.
8. The apparatus of claim 7, wherein the set of balun circuits are coupled between the one or more inductors and the first electronic device.
9. The apparatus of claim 7, wherein the one or more inductors includes first and second sets of inductors, the first set of inductors coupled between the set of balun circuits and the first electronic device, the second set of inductors coupled between the set of balun circuits and the second electronic device.
10. The system of claim 2, wherein at least one balun circuit of the set of balun circuits includes an inductor in parallel with a capacitor and a resistor.
11. The system of claim 2, wherein at least one balun circuit of the set of balun circuits includes a coaxial cable winding including first and second conductors, the first conductor coupled to a first lead of the first electronic device and the second conductor coupled to a second lead of the first electronic device.

12. A method, comprising:

- delivering, using a pacing device positioned near cardiac tissue of a heart, pacing signals to the heart;
  - delivering, using a signal generator, voltage pulse waveforms to an ablation device positioned near tissue to ablate the tissue;
  - inducing, in response to delivering the voltage pulse waveform, currents in a set of first electronic devices positioned near the cardiac tissue, the set of first electronic devices including the pacing device; and
  - reducing, using a protection device coupled between the set of first electronic devices and a second electronic device, the currents induced in the set of first electronic devices.
13. The method of claim 12, wherein the second electronic device is a cardiac stimulator configured to generate the pacing signals.
  14. The method of claim 12, wherein the second electronic device is an electrocardiogram (ECG) recording system.
  15. The method of claim 12, wherein reducing, using the protection device, the current induced in the set of first electronic devices includes reducing, using one or more transformers or capacitors of the protection device, common mode currents or differential mode currents of the currents induced in the set of first electronic devices.
  16. The method of claim 12, wherein reducing, using the protection device, the current induced in the set of first electronic devices includes shunting, using a set of diodes of the protection device, high voltages away from the second electronic device.
  17. The method of claim 12, wherein reducing, using the protection device, the current induced in the set of first electronic devices includes reducing, using one or more inductors of balun circuits of the protection device, alternating currents having frequencies within a predetermined range of frequencies of the currents induced in the set of first electronic devices.
  18. The method of claim 14, wherein reducing, using the protection device, the current induced in the set of first electronic devices includes reducing, using one or more transformers or capacitors of the protection device, common mode currents or differential mode currents of the currents induced in the set of first electronic devices.
  19. The method of claim 14, wherein reducing, using the protection device, the current induced in the set of first electronic devices includes shunting, using a set of diodes of the protection device, high voltages away from the second electronic device.
  20. The method of claim 14, wherein reducing, using the protection device, the current induced in the set of first electronic devices includes reducing, using one or more inductors of balun circuits of the protection device, alternating currents having frequencies within a predetermined range of frequencies of the currents induced in the set of first electronic devices.

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