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# SYSTEMS AND METHODS FOR SIGNALING PICTURE TIMING AND DECODING UNIT INFORMATION IN VIDEO CODING

#### **Abstract**

Inventor(s)

This disclosure relates to video coding and more particularly to techniques for signaling picture timing and decoding unit information for coded video. According to an aspect of an invention, a flag syntax element, specifying whether decoding unit level decoded picture buffer output delay parameters are present in a picture timing message, in a buffering period message is parsed and a first syntax element, used to compute a decoded picture buffer output time, in the picture timing message is parsed, in a case that a value of the flag syntax element is equal to one.

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## **Background/Summary**

#### **TECHNICAL FIELD**

[0001] This disclosure relates to video coding and more particularly to techniques for signaling picture timing and decoding unit information for coded video.

#### **BACKGROUND ART**

[0002] Digital video capabilities can be incorporated into a wide range of devices, including digital televisions, laptop or desktop computers, tablet computers, digital recording devices, digital media players, video gaming devices, cellular telephones, including so-called smartphones, medical imaging devices, and the like. Digital video may be coded according to a video coding standard. Video coding standards define the format of a compliant bitstream encapsulating coded video data. A compliant bitstream is a data structure that may be received and decoded by a video decoding device to generate reconstructed video data. Video coding standards may incorporate video compression techniques. Examples of video coding standards include ISO/IEC MPEG-4 Visual and ITU-T H.264 (also known as ISO/IEC MPEG-4 AVC) and High-Efficiency Video Coding (HEVC). HEVC is described in High Efficiency Video Coding (HEVC), Rec. ITU-T H.265, December 2016, which is incorporated by reference, and referred to herein as ITU-T H.265. Extensions and improvements for ITU-T H.265 are currently being considered for the development of next generation video coding standards. For example, the ITU-T Video Coding Experts Group (VCEG) and ISO/IEC (Moving Picture Experts Group (MPEG) (collectively referred to as the Joint Video Exploration Team (JVET)) are working to standardized video coding technology with a compression capability that significantly exceeds that of the current HEVC standard. The Joint Exploration Model 7 (JEM 7), Algorithm Description of Joint Exploration Test Model 7 (JEM 7), ISO/IEC JTC1/SC29/WG11 Document: JVET-G1001, July 2017, Torino, IT, which is incorporated by reference herein, describes the coding features that were under coordinated test model study by the JVET as potentially enhancing video coding technology beyond the capabilities of ITU-T H.265. It should be noted that the coding features of JEM 7 are implemented in JEM reference software. As used herein, the term JEM may collectively refer to algorithms included in JEM 7 and implementations of JEM reference software. Further, in response to a "Joint Call for Proposals on Video Compression with Capabilities beyond HEVC," jointly issued by VCEG and MPEG, multiple descriptions of video coding tools were proposed by various groups at the 10.sup.th Meeting of ISO/IEC JTC1/SC29/WG11 16-20 Apr. 2018, San Diego, CA. From the multiple descriptions of video coding tools, a resulting initial draft text of a video coding specification is described in "Versatile Video Coding (Draft 1)," 10.sup.th Meeting of ISO/IEC JTC1/SC29/WG11 16-20 Apr. 2018, San Diego, CA, document JVET-J1001-v2, which is incorporated by reference herein, and referred to as JVET-J1001. The current development of a next generation video coding standard by the VCEG and MPEG is referred to as the Versatile Video Coding (VVC) project. "Versatile Video Coding (Draft 7)," 16th Meeting of ISO/IEC JTC1/SC29/WG11 1-11 Oct. 2019, Geneva, CH, document JVET-P2001-vE, which is incorporated by reference herein, and referred to as JVET-P2001, represents the current iteration of the draft text of a video coding specification corresponding to the VVC project.

[0003] Video compression techniques enable data requirements for storing and transmitting video data to be reduced. Video compression techniques may reduce data requirements by exploiting the inherent redundancies in a video sequence. Video compression techniques may sub-divide a video sequence into successively smaller portions (i.e., groups of pictures within a video sequence, a picture within a group of pictures, regions within a picture, sub-regions within regions, etc.). Intra prediction coding techniques (e.g., spatial prediction techniques within a picture) and inter prediction techniques (i.e., inter-picture techniques (temporal)) may be used to generate difference values between a unit of video data to be coded and a reference unit of video data. The difference values may be referred to as residual data. Residual data

may be coded as quantized transform coefficients. Syntax elements may relate residual data and a reference coding unit (e.g., intra-prediction mode indices, and motion information). Residual data and syntax elements may be entropy coded. Entropy encoded residual data and syntax elements may be included in data structures forming a compliant bitstream.

#### SUMMARY OF INVENTION

[0004] In one example, a method of receiving a picture timing message, the method comprising: receiving a buffering period message; parsing a flag syntax element, specifying whether decoding unit level decoded picture buffer output delay parameters are present in the picture timing message, in the buffering period message; receiving the picture timing message; and parsing a first syntax element, used to compute a decoded picture buffer output time, in the picture timing message, in a case that a value of the flag syntax element is equal to one.

[0005] In one example, a device of receiving a picture timing message, the device comprising: a processor, and a memory associated with the processor; wherein the processor is configured to perform the following steps: receiving a buffering period message; parsing a flag syntax element, specifying whether decoding unit level decoded picture buffer output delay parameters are present in the picture timing message, in the buffering period message; receiving the picture timing message; and parsing a first syntax element, used to compute a decoded picture buffer output time, in the picture timing message, in a case that a value of the flag syntax element is equal to one.

[0006] In one example, a method of signaling a picture timing message, the method comprising: signaling a buffering period message including a flag syntax element specifying whether decoding unit level decoded picture buffer output delay parameters are present in the picture timing message; and signaling the picture timing message including a first syntax element used to compute a decoded picture buffer output time, in a case that a value of the flag syntax element is equal to one.

[0007] In one example, a device of signaling a picture timing message, the device comprising: a processor, and a memory associated with the processor; wherein the processor is configured to perform the following steps: signaling a buffering period message including a flag syntax element specifying whether decoding unit level decoded picture buffer output delay parameters are present in the picture timing message; and signaling the picture timing message including a first syntax element used to compute a decoded picture buffer output time, in a case that a value of the flag syntax element is equal to one.

# Description

#### BRIEF DESCRIPTION OF DRAWINGS

[0008] FIG. **1** is a block diagram illustrating an example of a system that may be configured to encode and decode video data according to one or more techniques of this disclosure.

[0009] FIG. **2** is a conceptual diagram illustrating coded video data and corresponding data structures according to one or more techniques of this disclosure.

[0010] FIG. **3** is a conceptual diagram illustrating a data structure encapsulating coded video data and corresponding metadata according to one or more techniques of this disclosure.

[0011] FIG. **4** is a conceptual drawing illustrating an example of components that may be included in an implementation of a system that may be configured to encode and decode video data according to one or more techniques of this disclosure.

[0012] FIG. **5** is a block diagram illustrating an example of a video encoder that may be configured to encode video data according to one or more techniques of this disclosure.

[0013] FIG. **6** is a block diagram illustrating an example of a video decoder that may be configured to decode video data according to one or more techniques of this disclosure.

#### DESCRIPTION OF EMBODIMENTS

[0014] In general, this disclosure describes various techniques for coding video data. In particular, this disclosure describes techniques for signaling picture timing and decoding unit (DU) information for coded video data. It should be noted that although techniques of this disclosure are described with respect to ITU-T H.264, ITU-T H.265, JEM, and JVET-P2001, the techniques of this disclosure are generally applicable to video coding. For example, the coding techniques described herein may be incorporated into

video coding systems, (including video coding systems based on future video coding standards) including video block structures, intra prediction techniques, inter prediction techniques, transform techniques, filtering techniques, and/or entropy coding techniques other than those included in ITU-T H.265, JEM, and JVET-P2001. Thus, reference to ITU-T H.264, ITU-T H.265, JEM, and/or JVET-P2001 is for descriptive purposes and should not be construed to limit the scope of the techniques described herein. Further, it should be noted that incorporation by reference of documents herein is for descriptive purposes and should not be construed to limit or create ambiguity with respect to terms used herein. For example, in the case where an incorporated reference provides a different definition of a term than another incorporated reference and/or as the term is used herein, the term should be interpreted in a manner that broadly includes each respective definition and/or in a manner that includes each of the particular definitions in the alternative.

[0015] In one example, a method of signaling decoding unit parameters for video data, the method comprises signaling a syntax element indicating whether decoding unit parameters are included in a picture timing message and conditionally signaling decoding unit parameters in a picture timing message based on the value of the syntax element.

[0016] In one example, a device comprises one or more processors configured to signal a syntax element indicating whether decoding unit parameters are included in a picture timing message and conditionally signal decoding unit parameters in a picture timing message based on the value of the syntax element. [0017] In one example, a non-transitory computer-readable storage medium comprises instructions stored thereon that, when executed, cause one or more processors of a device to signal a syntax element indicating whether decoding unit parameters are included in a picture timing message and conditionally signal decoding unit parameters in a picture timing message based on the value of the syntax element. [0018] In one example, an apparatus comprises means for signaling a syntax element indicating whether decoding unit parameters are included in a picture timing message and means for conditionally signaling decoding unit parameters in a picture timing message based on the value of the syntax element. [0019] In one example, a method of decoding video data comprises parsing a syntax element indicating whether decoding unit parameters are included in a picture timing message and conditionally parsing decoding unit parameters in a picture timing message based on the value of the syntax element. [0020] In one example, a device comprises one or more processors configured to parse syntax element indicating whether decoding unit parameters are included in a picture timing message and conditionally parse decoding unit parameters in a picture timing message based on the value of the syntax element. [0021] In one example, a non-transitory computer-readable storage medium comprises instructions stored thereon that, when executed, cause one or more processors of a device to parse syntax element indicating whether decoding unit parameters are included in a picture timing message and conditionally parse decoding unit parameters in a picture timing message based on the value of the syntax element. [0022] In one example, an apparatus comprises means for parsing a syntax element indicating whether decoding unit parameters are included in a picture timing message and means for conditionally parsing decoding unit parameters in a picture timing message based on the value of the syntax element. [0023] The details of one or more examples are set forth in the accompanying drawings and the description below. Other features, objects, and advantages will be apparent from the description and drawings, and from the claims.

[0024] Video content includes video sequences comprised of a series of frames (or pictures). A series of frames may also be referred to as a group of pictures (GOP). Each video frame or picture may divided into one or more regions. Regions may be defined according to a base unit (e.g., a video block) and sets of rules defining a region. For example, a rule defining a region may be that a region must be an integer number of video blocks arranged in a rectangle. Further, video blocks in a region may be ordered according to a scan pattern (e.g., a raster scan). As used herein, the term video block may generally refer to an area of a picture or may more specifically refer to the largest array of sample values that may be predictively coded, sub-divisions thereof, and/or corresponding structures. Further, the term current video block may refer to an area of a picture being encoded or decoded. A video block may be defined as an array of sample values. It should be noted that in some cases pixel values may be described as including sample values for respective components of video data, which may also be referred to as color components, (e.g., luma (Y) and chroma (Cb and Cr) components or red, green, and blue components). It

should be noted that in some cases, the terms pixel value and sample value are used interchangeably. Further, in some cases, a pixel or sample may be referred to as a pel. A video sampling format, which may also be referred to as a chroma format, may define the number of chroma samples included in a video block with respect to the number of luma samples included in a video block. For example, for the 4:2:0 sampling format, the sampling rate for the luma component is twice that of the chroma components for both the horizontal and vertical directions.

[0025] A video encoder may perform predictive encoding on video blocks and sub-divisions thereof. Video blocks and sub-divisions thereof may be referred to as nodes. ITU-T H.264 specifies a macroblock including 16×16 luma samples. That is, in ITU-T H.264, a picture is segmented into macroblocks. ITU-T H.265 specifies an analogous Coding Tree Unit (CTU) structure (which may be referred to as a largest coding unit (LCU)). In ITU-T H.265, pictures are segmented into CTUs. In ITU-T H.265, for a picture, a CTU size may be set as including 16×16, 32×32, or 64×64 luma samples. In ITU-T H.265, a CTU is composed of respective Coding Tree Blocks (CTB) for each component of video data (e.g., luma (Y) and chroma (Cb and Cr). It should be noted that video having one luma component and the two corresponding chroma components may be described as having two channels, i.e., a luma channel and a chroma channel. Further, in ITU-T H.265, a CTU may be partitioned according to a quadtree (QT) partitioning structure, which results in the CTBs of the CTU being partitioned into Coding Blocks (CB). That is, in ITU-T H.265, a CTU may be partitioned into quadtree leaf nodes. According to ITU-T H.265, one luma CB together with two corresponding chroma CBs and associated syntax elements are referred to as a coding unit (CU). In ITU-T H.265, a minimum allowed size of a CB may be signaled. In ITU-T H.265, the smallest minimum allowed size of a luma CB is 8×8 luma samples. In ITU-T H.265, the decision to code a picture area using intra prediction or interprediction is made at the CU level.

[0026] In ITU-T H.265, a CU is associated with a prediction unit structure having its root at the CU. In ITU-T H.265, prediction unit structures allow luma and chroma CBs to be split for purposes of generating corresponding reference samples. That is, in ITU-T H.265, luma and chroma CBs may be split into respective luma and chroma prediction blocks (PBs), where a PB includes a block of sample values for which the same prediction is applied. In ITU-T H.265, a CB may be partitioned into 1, 2, or 4 PBs. ITU-T H.265 supports PB sizes from 64×64 samples down to 4×4 samples. In ITU-T H.265, square PBs are supported for intra prediction, where a CB may form the PB or the CB may be split into four square PBs. In ITU-T H.265, in addition to the square PBs, rectangular PBs are supported for inter prediction, where a CB may be halved vertically or horizontally to form PBs. Further, it should be noted that in ITU-T H.265, for inter prediction, four asymmetric PB partitions are supported, where the CB is partitioned into two PBs at one quarter of the height (at the top or the bottom) or width (at the left or the right) of the CB. Intra prediction data (e.g., intra prediction mode syntax elements) or inter prediction data (e.g., motion data syntax elements) corresponding to a PB is used to produce reference and/or predicted sample values for the PB.

[0027] JEM specifies a CTU having a maximum size of 256×256 luma samples. JEM specifies a quadtree plus binary tree (QTBT) block structure. In JEM, the QTBT structure enables quadtree leaf nodes to be further partitioned by a binary tree (BT) structure. That is, in JEM, the binary tree structure enables quadtree leaf nodes to be recursively divided vertically or horizontally. In JVET-P2001, CTUs are partitioned according a quadtree plus multi-type tree (QTMT or QT+MTT) structure. The QTMT in JVET-P2001 is similar to the QTBT in JEM. However, in JVET-P2001, in addition to indicating binary splits, the multi-type tree may indicate so-called ternary (or triple tree (TT)) splits. A ternary split divides a block vertically or horizontally into three blocks. In the case of a vertical TT split, a block is divided at one quarter of its width from the left edge and at one quarter its width from the right edge and in the case of a horizontal TT split a block is at one quarter of its height from the top edge and at one quarter of its height from the bottom edge.

[0028] As described above, each video frame or picture may be divided into one or more regions. For example, according to ITU-T H.265, each video frame or picture may be partitioned to include one or more slices and further partitioned to include one or more tiles, where each slice includes a sequence of CTUs (e.g., in raster scan order) and where a tile is a sequence of CTUs corresponding to a rectangular area of a picture. It should be noted that a slice, in ITU-T H.265, is a sequence of one or more slice segments starting with an independent slice segment and containing all subsequent dependent slice

segments (if any) that precede the next independent slice segment (if any). A slice segment, like a slice, is a sequence of CTUs. Thus, in some cases, the terms slice and slice segment may be used interchangeably to indicate a sequence of CTUs arranged in a raster scan order. Further, it should be noted that in ITU-T H.265, a tile may consist of CTUs contained in more than one slice and a slice may consist of CTUs contained in more than one tile. However, ITU-T H.265 provides that one or both of the following conditions shall be fulfilled: (1) All CTUs in a slice belong to the same tile; and (2) All CTUs in a tile belong to the same slice.

[0029] With respect to JVET-P2001, slices are required to consist of an integer number of complete tiles or an integer number of consecutive complete CTU rows within a tile, instead of only being required to consist of an integer number of CTUs. It should be noted that in JVET-P2001, the slice design does not include slice segments (i.e., no independent/dependent slice segments). Thus, in JVET-P2001, a picture may include a single tile, where the single tile is contained within a single slice or a picture may include multiple tiles where the multiple tiles (or CTU rows thereof) may be contained within one or more slices. In JVET-P2001, the partitioning of a picture into tiles is specified by specifying respective heights for tile rows and respective widths for tile columns. Thus, in JVET-P2001 a tile is a rectangular region of CTUs within a particular tile row and a particular tile column position. Further, it should be noted that JVET-P2001 provides where a picture may be partitioned into subpictures, where a subpicture is a rectangular region of a CTUs within a picture. The top-left CTU of a subpicture may be located at any CTU position within a picture with subpictures being constrained to include one or more slices Thus, unlike a tile, a subpicture is not necessarily limited to a particular row and column position. It should be noted that subpictures may be useful for encapsulating regions of interest within a picture and a sub-bitstream extraction process may be used to only decode and display a particular region of interest. That is, as described in further detail below, a bitstream of coded video data includes a sequence of network abstraction layer (NAL) units, where a NAL unit encapsulates coded video data, (i.e., video data corresponding to a slice of picture) or a NAL unit encapsulates metadata used for decoding video data (e.g., a parameter set) and a sub-bitstream extraction process forms a new bitstream by removing one or more NAL units from a bitstream.

[0030] FIG. **2** is a conceptual diagram illustrating an example of a picture within a group of pictures partitioned according to tiles, slices, and subpictures. It should be noted that the techniques described herein may be applicable to tiles, slices, subpictures, sub-divisions thereof and/or equivalent structures thereto. That is, the techniques described herein may be generally applicable regardless of how a picture is partitioned into regions. For example, in some cases, the techniques described herein may be applicable in cases where a tile may be partitioned into so-called bricks, where a brick is a rectangular region of CTU rows within a particular tile. Further, for example, in some cases, the techniques described herein may be applicable in cases where one or more tiles may be included in so-called tile groups, where a tile group includes an integer number of adjacent tiles. In the example illustrated in FIG. 2, Pic.sub.3 is illustrated as including 16 tiles (i.e., Tile.sub.0 to Tile.sub.15) and three slices (i.e., Slice.sub.0 to Slice.sub.2). In the example illustrated in FIG. 2, Slice.sub.0 includes four tiles (i.e., Tile.sub.0 to Tile.sub.3), Slice.sub.1 includes eight tiles (i.e., Tile.sub.4 to Tile.sub.11), and Slice.sub.2 includes four tiles (i.e., Tile.sub.12 to Tile.sub.15). Further, as illustrated in the example of FIG. 2, Pic.sub.3 is illustrated as including two subpictures (i.e., Subpicture.sub.0 and Subpicture.sub.1), where Subpicture.sub.0 includes Slice.sub.0 and Slice.sub.1 and where Subpicture.sub.1 includes Slice.sub.2. As described above, subpictures may be useful for encapsulating regions of interest within a picture and a sub-bitstream extraction process may be used in order to selectively decode (and display) a region interest. For example, referring to FIG. 2, Subpicture.sub.0 may corresponding to an action portion of a sporting event presentation (e.g., a view of the field) and Subpicture.sub.1 may corresponding to a scrolling banner displayed during the sporting event presentation. By using organizing a picture into subpictures in this manner, a viewer may be able to disable the display of the scrolling banner. That is, through a sub-bitstream extraction process Slice.sub.2 NAL unit may be removed from a bitstream (and thus not decoded and/or displayed) and Slice.sub.0 NAL unit and Slice.sub.1 NAL unit may be decoded and displayed. The encapsulation of slices of a picture into respective NAL unit data structures and sub-bitstream extraction are described in further detail below. [0031] For intra prediction coding, an intra prediction mode may specify the location of reference samples within a picture. In ITU-T H.265, defined possible intra prediction modes include a planar (i.e., surface

fitting) prediction mode, a DC (i.e., flat overall averaging) prediction mode, and 33 angular prediction modes (predMode: 2-34). In JEM, defined possible intra-prediction modes include a planar prediction mode, a DC prediction mode, and 65 angular prediction modes. It should be noted that planar and DC prediction modes may be referred to as non-directional prediction modes and that angular prediction modes may be referred to as directional prediction modes. It should be noted that the techniques described herein may be generally applicable regardless of the number of defined possible prediction modes. [0032] For inter prediction coding, a reference picture is determined and a motion vector (MV) identifies samples in the reference picture that are used to generate a prediction for a current video block. For example, a current video block may be predicted using reference sample values located in one or more previously coded picture (s) and a motion vector is used to indicate the location of the reference block relative to the current video block. A motion vector may describe, for example, a horizontal displacement component of the motion vector (i.e., MV.sub.x), a vertical displacement component of the motion vector (i.e., MV.sub.y), and a resolution for the motion vector (e.g., one-quarter pixel precision, one-half pixel precision, one-pixel precision, two-pixel precision, four-pixel precision). Previously decoded pictures, which may include pictures output before or after a current picture, may be organized into one or more to reference pictures lists and identified using a reference picture index value. Further, in inter prediction coding, uni-prediction refers to generating a prediction using sample values from a single reference picture and bi-prediction refers to generating a prediction using respective sample values from two reference pictures. That is, in uni-prediction, a single reference picture and corresponding motion vector are used to generate a prediction for a current video block and in bi-prediction, a first reference picture and corresponding first motion vector and a second reference picture and corresponding second motion vector are used to generate a prediction for a current video block. In bi-prediction, respective sample values are combined (e.g., added, rounded, and clipped, or averaged according to weights) to generate a prediction. Pictures and regions thereof may be classified based on which types of prediction modes may be utilized for encoding video blocks thereof. That is, for regions having a B type (e.g., a B slice), biprediction, uni-prediction, and intra prediction modes may be utilized, for regions having a P type (e.g., a P slice), uni-prediction, and intra prediction modes may be utilized, and for regions having an I type (e.g., an I slice), only intra prediction modes may be utilized. As described above, reference pictures are identified through reference indices. For example, for a P slice, there may be a single reference picture list, RefPicList0 and for a B slice, there may be a second independent reference picture list, RefPicList1, in addition to RefPicList0. It should be noted that for uni-prediction in a B slice, one of RefPicList0 or RefPicList1 may be used to generate a prediction. Further, it should be noted that during the decoding process, at the onset of decoding a picture, reference picture list(s) are generated from previously decoded pictures stored in a decoded picture buffer (DPB).

[0033] Further, a coding standard may support various modes of motion vector prediction. Motion vector prediction enables the value of a motion vector for a current video block to be derived based on another motion vector. For example, a set of candidate blocks having associated motion information may be derived from spatial neighboring blocks and temporal neighboring blocks to the current video block. Further, generated (or default) motion information may be used for motion vector prediction. Examples of motion vector prediction include advanced motion vector prediction (AMVP), temporal motion vector prediction (TMVP), so-called "merge" mode, and "skip" and "direct" motion inference. Further, other examples of motion vector prediction include advanced temporal motion vector prediction (ATMVP) and Spatial-temporal motion vector prediction (STMVP). For motion vector prediction, both a video encoder and video decoder perform the same process to derive a set of candidates. Thus, for a current video block, the same set of candidates is generated during encoding and decoding.

[0034] As described above, for inter prediction coding, reference samples in a previously coded picture are used for coding video blocks in a current picture. Previously coded pictures which are available for use as reference when coding a current picture are referred as reference pictures. It should be noted that the decoding order does not necessary correspond with the picture output order, i.e., the temporal order of pictures in a video sequence. In ITU-T H.265, when a picture is decoded it is stored to a decoded picture buffer (DPB) (which may be referred to as frame buffer, a reference buffer, a reference picture buffer, or the like). In ITU-T H.265, pictures stored to the DPB are removed from the DPB when they been output and are no longer needed for coding subsequent pictures. In ITU-T H.265, a determination of whether

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at the onset of decoding a picture. For example, referring to FIG. 2, Pic.sub.2 is illustrated as referencing
Pic.sub.1. Similarly, Pic.sub.3 is illustrated as referencing Pic.sub.0. With respect to FIG. 2, assuming the
picture number corresponds to the decoding order, the DPB would be populated as follows: after decoding
Pic.sup.0, the DPB would include {Pic.sub.0}; at the onset of decoding Pic.sub.1, the DPB would include
{Pic.sub.0}; after decoding Pic.sub.1, the DPB would include {Pic.sub.0, Pic.sub.1}; at the onset of
decoding Pic.sub.2, the DPB would include {Pic.sub.0, Pic.sub.1}. Pic.sub.2 would then be decoded with
reference to Pic.sub.1 and after decoding Pic.sub.2, the DPB would include {Pic.sub.0, Pic.sub.1,
Pic.sub.2}. At the onset of decoding Pic.sub.3, pictures Pic.sup.0 and Pic.sub.i would be marked for
removal from the DPB, as they are not needed for decoding Pic.sub.3 (or any subsequent pictures, not
shown) and assuming Pic.sub.i and Pic.sub.2 have been output, the DPB would be updated to include
{Pic.sub.0}. Pic.sub.3 would then be decoded by referencing Pic.sup.0. The process of marking pictures
for removal from a DPB may be referred to as reference picture set (RPS) management.
[0035] As described above, intra prediction data or interprediction data is used to produce reference
sample values for a block of sample values. The difference between sample values included in a current
PB, or another type of picture area structure, and associated reference samples (e.g., those generated using
a prediction) may be referred to as residual data. Residual data may include respective arrays of difference
values corresponding to each component of video data. Residual data may be in the pixel domain. A
transform, such as, a discrete cosine transform (DCT), a discrete sine transform (DST), an integer
transform, a wavelet transform, or a conceptually similar transform, may be applied to an array of
difference values to generate transform coefficients. It should be noted that in ITU-T H.265 and JVET-
P2001, a CU is associated with a transform tree structure having its root at the CU level. The transform
tree is partitioned into one or more transform units (TUs). That is, an array of difference values may be
partitioned for purposes of generating transform coefficients (e.g., four 8×8 transforms may be applied to
a 16×16 array of residual values). For each component of video data, such sub-divisions of difference
values may be referred to as Transform Blocks (TBs). It should be noted that in some cases, a core
transform and a subsequent secondary transforms may be applied (in the video encoder) to generate
transform coefficients. For a video decoder, the order of transforms is reversed.
[0036] A quantization process may be performed on transform coefficients or residual sample values
directly (e.g., in the case, of palette coding quantization). Quantization approximates transform
coefficients by amplitudes restricted to a set of specified values. Quantization essentially scales transform
coefficients in order to vary the amount of data required to represent a group of transform coefficients.
Quantization may include division of transform coefficients (or values resulting from the addition of an
offset value to transform coefficients) by a quantization scaling factor and any associated rounding
functions (e.g., rounding to the nearest integer). Quantized transform coefficients may be referred to as
coefficient level values. Inverse quantization (or "dequantization") may include multiplication of
coefficient level values by the quantization scaling factor, and any reciprocal rounding or offset addition
operations. It should be noted that as used herein the term quantization process in some instances may
refer to division by a scaling factor to generate level values and multiplication by a scaling factor to
recover transform coefficients in some instances. That is, a quantization process may refer to quantization
in some cases and inverse quantization in some cases. Further, it should be noted that although in some of
the examples below quantization processes are described with respect to arithmetic operations associated
with decimal notation, such descriptions are for illustrative purposes and should not be construed as
limiting. For example, the techniques described herein may be implemented in a device using binary
operations and the like. For example, multiplication and division operations described herein may be
implemented using bit shifting operations and the like.
[0037] Quantized transform coefficients and syntax elements (e.g., syntax elements indicating a coding
structure for a video block) may be entropy coded according to an entropy coding technique. An entropy
coding process includes coding values of syntax elements using lossless data compression algorithms.
Examples of entropy coding techniques include content adaptive variable length coding (CAVLC),
context adaptive binary arithmetic coding (CABAC), probability interval partitioning entropy coding
(PIPE), and the like. Entropy encoded quantized transform coefficients and corresponding entropy
```

encoded syntax elements may form a compliant bitstream that can be used to reproduce video data at a

pictures should be removed from the DPB is invoked once per picture, after decoding a slice header, i.e.,

video decoder. An entropy coding process, for example, CABAC, may include performing a binarization on syntax elements. Binarization refers to the process of converting a value of a syntax element into a series of one or more bits. These bits may be referred to as "bins." Binarization may include one or a combination of the following coding techniques: fixed length coding, unary coding, truncated unary coding, truncated Rice coding, Golomb coding, k-th order exponential Golomb coding, and Golomb-Rice coding. For example, binarization may include representing the integer value of 5 for a syntax element as 00000101 using an 8-bit fixed length binarization technique or representing the integer value of 5 as 11110 using a unary coding binarization technique. As used herein each of the terms fixed length coding, unary coding, truncated unary coding, truncated Rice coding, Golomb coding, k-th order exponential Golomb coding, and Golomb-Rice coding may refer to general implementations of these techniques and/or more specific implementations of these coding techniques. For example, a Golomb-Rice coding implementation may be specifically defined according to a video coding standard. In the example of CABAC, for a particular bin, a context provides a most probable state (MPS) value for the bin (i.e., an MPS for a bin is one of 0 or 1) and a probability value of the bin being the MPS or the least probably state (LPS). For example, a context may indicate, that the MPS of a bin is 0 and the probability of the bin being 1 is 0.3. It should be noted that a context may be determined based on values of previously coded bins including bins in the current syntax element and previously coded syntax elements. For example, values of syntax elements associated with neighboring video blocks may be used to determine a context for a current bin.

[0038] With respect to the equations used herein, the following arithmetic operators may be used: [0039] + Addition [0040] – Subtraction [0041] Multiplication, including matrix multiplication [0042] x.sup.y Exponentiation. Specifies x to the power of y. In other contexts, such notation is used for superscripting not intended for interpretation as exponentiation. [0043] / Integer division with truncation of the result toward zero. For example, 7/4 and -7/-4 are truncated to 1 and -7/4 and 7/-4 are truncated to -1. [0044]  $\div$  Used to denote division in mathematical equations where no truncation or rounding is intended. [0045] x/y Used to denote division in mathematical equations where no truncation or rounding is intended. [0046] Further, the following mathematical functions may be used: [0047] Log 2(x) the base-2 logarithm of x;

[00001]Min(
$$x$$
,  $y$ ) = {  $\begin{pmatrix} x & ; & x <= y \\ y & ; & x > y \end{pmatrix}$ ; Max( $x$ ,  $y$ ) = {  $\begin{pmatrix} x & ; & x >= y \\ y & ; & x < y \end{pmatrix}$  [0048] Ceil( $x$ ) the smallest integer greater than or equal to  $x$ .

[0049] With respect to the example syntax used herein, the following definitions of logical operators may be applied;  $[0050] \times \&\& y$  Boolean logical "and" of x and y  $[0051] \times \|y$  Boolean logical "or" of x and y [0052]! Boolean logical "not"  $[0053] \times ?y$ : z If x is TRUE or not equal to 0, evaluates to the value of y; otherwise, evaluates to the value of z.

[0054] Further, the following relational operators may be applied: [0055] > G reater than [0056] > G Greater than or equal to [0057] < L Ess than [0058] < G Less than or equal to [0059] = G Equal to [0059] = G Not equal to

[0061] Further, it should be noted that in the syntax descriptors used herein, the following descriptors may be applied: [0062] b(8): byte having any pattern of bit string (8 bits). The parsing process for this descriptor is specified by the return value of the function read\_bits(8). [0063] f(n): fixed-pattern bit string using n bits written (from loft to right) with the left bit first. The parsing process for this descriptor is specified by the return value of the function rend\_.bits(n). [0064] se(v): signed integer 0-th order Exp-Golomb-coded syntax element with the left bit first. [0065] tb(v): truncated binary using up to maxVal bits with maxVal defined in the semantics of the symtax element. [0066] tu(v): truncated unary using up to maxVal bits with maxVal defined in the semantics of the symtax element. [0067] u(n): unsigned integer using n bits. When n is "v" in the syntax table, the number of bits varies in a manner dependent on the value of other syntax elements. The parsing process for this descriptor is specified by the return value of the function read-bits(a) interpreted as a binary representation of an unsigned integer with most significant bit written first. [0068] ue(v): unsigned integer 0-th order Exp-Golomb-coded syntax element with the left bit first.

[0069] As described above, video content includes video sequences comprised of a series of pictures and each picture may be divided into one or more regions. In JVET-P2001, a coded representation of a picture

is referred to as a coded picture and all CTUs of the coded picture are encapsulated in one or more coded slice NAL units. That is, one or more corresponding coded slice NAL units encapsulate a coded representation of a picture. For example, referring again to FIG. 2, the coded representation of Pic.sub.3 is encapsulated in three coded slice NAL units (i.e., Slice.sub.0 NAL unit, Slice.sub.1 NAL unit, and Slice.sub.2 NAL unit). It should be noted that the term video coding layer (VCL) NAL unit is used as a collective term for coded slice NAL units, i.e., VCL NAL is a collective term which includes all types of slice NAL units. As described above, and in further detail below, a NAL unit may encapsulate metadata used for decoding video data. A NAL unit encapsulating metadata used for decoding a video sequence is generally referred to as a non-VCL NAL unit. Thus, in JVET-P2001, a NAL unit may be a VCL NAL unit or a non-VCL NAL unit. It should be noted that a VCL NAL unit includes slice header data, which provides information used for decoding the particular slice. Thus, in JVET-P2001, information used for decoding video data, which may be referred to as metadata in some cases, is not limited to being included in non-VCL NAL units. JVET-P2001 provides where a picture unit (PU) is a set of NAL units that contain all VCL NAL units of a coded picture and their associated non-VCL NAL units and where an access unit (AU) is a set of NAL units that are associated with each other according to a specified classification rule, are consecutive in decoding order, and contain exactly one coded picture for each present picture unit. A PU consists of one picture header NAL unit, one coded picture, which comprises of one or more VCL NAL units, and zero or more non-VCL NAL units. Thus, in JVET-P2001 an access unit includes one or more coded pictures. In some cases, an access unit may include pictures included in different layers of video. Layers of video are described in further detail below. Further, in JVET-P2001, a coded video sequence (CVS) is a sequence of AUs that consists, in decoding order, of a CVSS AU, followed by zero or more AUs that are not CVSS AUs, including all subsequent AUs up to but not including any subsequent AU that is a CVSS AU, where a coded video sequence start (CVSS) AU is an AU in which there is a picture unit for each layer in the CVS and the coded picture in each present picture unit is a coded layer video sequence start (CLVSS) picture. In JVET-P2001, a coded layer video sequence (CLVS) is a sequence of PUs within the same layer that consists, in decoding order, of a CLVSS PU, followed by zero or more PUs that are not CLVSS PUs, including all subsequent PUs up to but not including any subsequent PU that is a CLVSS PU. This is, in JVET-P2001, a bitstream may be described as including a sequence of NAL units forming a CVS, where a CVS includes AUs and each AU may include respective pictures for each of a plurality of layers for coded video.

[0070] Multi-layer video coding enables a video presentation to be decoded/displayed as a presentation corresponding to a base layer of video data and decoded/displayed one or more additional presentations corresponding to enhancement layers of video data. For example, a base layer may enable a video presentation having a basic level of quality (e.g., a High Definition rendering and/or a 30 Hz frame rate) to be presented and an enhancement layer may enable a video presentation having an enhanced level of quality (e.g., an Ultra High Definition rendering and/or a 60 Hz frame rate) to be presented. An enhancement layer may be coded by referencing a base layer. That is, for example, a picture in an enhancement layer may be coded (e.g., using inter-layer prediction techniques) by referencing one or more pictures (including scaled versions thereof) in a base layer. It should be noted that layers may also be coded independent of each other. In this case, there may not be inter-layer prediction between two layers. Each NAL unit may include an identifier indicating a layer of video data the NAL unit is associated with. As described above, a sub-bitstream extraction process may be used to only decode and display a particular region of interest of a picture. Further, a sub-bitstream extraction process may be used to only decode and display a particular layer of video. Sub-bitstream extraction may refer to a process where a device receiving a compliant or conforming bitstream forms a new compliant or conforming bitstream by discarding and/or modifying data in the received bitstream. For example, sub-bitstream extraction may be used to form a new compliant or conforming bitstream corresponding to a particular representation of video (e.g., a high quality representation).

[0071] In JVET-P2001, each of a video sequence, a GOP, a picture, a slice, and CTU may be associated with metadata that describes video coding properties and some types of metadata an encapsulated in non-VCL NAL units. JVET-P2001 defines parameters sets that may be used to describe video data and/or video coding properties. In particular, JVET-P2001 includes the following five types of parameter sets: decoding parameter set (DPS), video parameter set (VPS), sequence parameter set (SPS), picture

parameter set (PPS), and adaption parameter set (APS), where a SPS applies to apply to zero or more entire CVSs, a PPS applies to zero or more entire coded pictures, a APS applies to zero or more slices, and a DPS and a VPS may be optionally referenced by a SPS. A PPS applies to an individual coded picture that refers to it. In JVET-P2001, parameter sets may be encapsulated as a non-VCL NAL unit and/or may be signaled as a message. JVET-P2001 also includes a picture header (PH) which is encapsulated as a non-VCL NAL unit. In JVET-P2001, a picture header applies to all slices of a coded picture. JVET-P2001 further enables supplemental enhancement information (SEI) messages to be signaled. In JVET-P2001, SEI messages assist in processes related to decoding, display or other purposes, however, SEI messages may not be required for constructing the luma or chroma samples according to a decoding process. In JVET-P2001, SEI messages may be signaled in a bitstream using non-VCL NAL units. Further, SEI messages may be conveyed by some mechanism other than by being present in the bitstream (i.e., signaled out-of-band).

[0072] FIG. 3 illustrates an example of a bitstream including multiple CVSs, where a CVS includes AUs, and AUs include picture units. The example illustrated in FIG. 3 corresponds to an example of encapsulating the slice NAL units illustrated in the example of FIG. 2 in a bitstream. In the example illustrated in FIG. 3, the corresponding picture unit for Pic.sub.3 includes the three VCL NAL coded slice NAL units, i.e., Slice.sub.0 NAL unit, Slice.sub.1 NAL unit, and Slice.sub.2 NAL unit and two non-VCL NAL units, i.e., a PPS NAL Unit and a PH NAL unit. It should be noted that in FIG. 3, HEADER is a NAL unit header (i.e., not to be confused with a slice header). Further, it should be noted that in FIG. 3, other non-VCL NAL units, which are not illustrated may be included in the CVSs, e.g., SPS NAL units, VPS NAL units, SEI message NAL units, etc. Further, it should be noted that in other examples, a PPS NAL Unit used for decoding Pic.sub.3 may be included elsewhere in the bitstream, e.g., in the picture unit corresponding to Pic.sup.0 or may be provided by an external mechanism. However, it should be noted that in JVET-P2001, the picture header for each picture is required to be in the picture unit corresponding to the picture. [0073] JVET-P2001 defines NAL unit header semantics that specify the type of Raw Byte Sequence Payload (RBSP) data structure included in the NAL unit. Table 1 illustrates the syntax of the NAL unit header provided in JVET-P2001

TABLE-US-00001 TABLE 1 Descriptor nal\_unit\_header() { forbidden\_zero\_bit f(1) [0074] JVFT-P2001 provides the following definitions for the respective syntax elements illustrated in Table 1. [0075] forbidden\_zero\_bit shall be equal to 0, [0076] nuh\_reserved\_zero\_bit shall be equal to '0' The value 1 of nuh\_reserved\_zero\_bit may be specified in the future by ITU-T|ISO/IFC. Decoders shall ignore (i.e. remove from the bitstream and discard) NAL units with nuh reserved zero bit equal to '1'. [0077] nuh\_layer\_id specifies the identifier of the layer to which a VCL NAL unit belongs or the identifier of a layer to which a non-VCL NAL unit applies. The value of nuh layer id shall be in the range of 0 to 55, inclusive. Other values for nuh layer id are reserved for future use by ITU-T | ISO/IEC. [0078] The value of nuh\_layer\_id shall be the same for all VCL NAL units of a coded picture. The value of nuh layer id of a coded picture or a PU is the value of the nuh layer id of the VCL NAL units of the coded picture or the PU. [0079] The value of nuh layer id for non-VCL NAL units is constrained as follows: [0080] If nal\_unit\_type is equal to PPS\_NUT, PREFIX\_APS\_NUT, or SUFFIX APS NUT, nuh\_layer\_id shall be equal to the lowest nuh\_layer\_id value of the coded slice NAL units that refer to the NAL unit. [0081] Otherwise, if nal\_unit\_type is equal to SPS\_NUT, nuh\_layer\_id shall be equal to the lowest nuh\_layer\_id value of the PPS NAL units that refer to the SPS NAL unit. [0082] Otherwise, if nal\_unil\_type is equal to AUD\_NUT, nuh\_layer\_id shall be equal to vps\_layer\_id[0]. [0083] Otherwise, when nal unit type is equal to PH NUT, EOS NUT, or FD NUT, nuh layer id shall be equal to the nuh\_layer\_id of associated VCL NAL unit. [0084] NOTE—The value of nuh\_layer\_id of DPS, VPS, and EOB NAL units is not constrained. [0085] The value of nal\_unit\_type shall be the same for all pictures of a CVSS AU. [0086] nuh temporal id plus1 minus 1 specifies a temporal identifier for the NAL unit. [0087] The value of nuh temporal id plus1 shall not be equal to 0. [0088] The variable TemporalId is derived as follows:

[00002]TemporalID = nuh\_temporal\_id\_plus1 - 1 [0089] When nal\_unit\_type is in the range of IDR\_W\_RADL to RSV\_IRAP\_12, inclusive, TemporalId shall be equal to 0. [0090] When nal\_unit\_type is equal to STSA\_NUT, TemporalId shall not be equal to 0. [0091] The value of TemporalId shall be the

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same for all VCL NAL units of an AU. The value of TemporalId of a coded picture, a PU, or an AU is the
value of the TemporalId of the VCL NAL units of the coded picture, PU, or AU. The value of TemporalId
of a sublayer representation is the greatest value of TemporalId of all VCL NAL units in the sublayer
representation. [0092] The value of TemporalId for non-VCL NAL units is constrained as follows: [0093]
If nal_unit_type is equal to DPS_NUT, VPS_NUT, or SPS_NUT TemporalId shall be equal to 0 and the
TemporalId of the AU containing the NAL unit shall be equal to 0. [0094] Otherwise, if nal_unit_type is
equal to PH NUT, TemporalId shall be equal to the TemporalId of the PU containing the NAL unit.
[0095] Otherwise, if nal unit type is equal to EOS NUT or EOB NUT, TemporalId shall be equal to 0.
[0096] Otherwise, if nal_unit_type is equal to AUD_NUT, FD_NUT, PREFIX_SEI_NUT, or
SUFFIX_SEI_NUT TemporalId shall be equal to the TemporalId of the AU containing the NAL unit.
[0097] Otherwise when nal unit type is equal to PPS NUT PREFIX APS NUT, or SUFFIX APS NUT,
TemporalId shall be greater than or equal to the TemporalId of the PU containing the NAL unit. [0098]
NOTE—When the NAL unit is a non-VCL NAL unit, the value of TemporalId is equal to the minimum
value of the TemporalID values of all AUs to which the non-VCL NAL unit applies. When nal unit type
is equal to PPS_NUT, PREFIX_APS_NUT, or SUFFIX_APS_NUT, TemporalId may be greater than or
equal to the TemporalId of the containing AU, as all PPSs and APSs may be included in the beginning of
the bitstream (e.g., when they are transported out-of-band, and the receiver places them at the bitstream),
wherein the first coded picture has TemporalId equal to 0. [0099] nal_unit_type specifies the NAL unit
type, i.e, the type of RBSP data structure contained in the NAL unit as specified in Table 2. [0100] NAT
units that have nal unit type in the range of UNSPEC28..UNSPEC31, inclusive, for which semantics are
not specified, shall not affect the decoding process specified in this Specification. [0101] NOTE—NAL
unit types in the range of UNSPEC_28..UNSPEC31 may be used as determined by the application. No
decoding process for these values of nal unit type is specified in this Specification. Since different
applications might use these NAL unit types for different purposes, particular care must be exercised in
the design of encoders that generate NAL units with these nal_unit_type values, and in the design of
decoders that interpret the content of NAL units with these nal unit type values. This Specification does
not define any management for these values. These nal_unit_type values might only be suitable for use in
contexts in which "collisions" of usage (i.e., different definitions of the meaning of the NAL unit content
for the same nal unit type value) are unimportant, or not possible, or are managed—e.g. defined or
managed in the controlling application or transport specification, or by controlling the environment in
which bitstreams are distributed. [0102] For purposes other than determining the amount of data in the
decoding units of the bitstream, decoders shall ignore (remove from the bitstream and discard) the
contents of all NAL units that use reserved values of nal unit type. [0103] NOTE—This requirement
allows future definition of compatible extensions to this Specification.
TABLE-US-00002 TABLE 2 Name of Content of NAL unit and RBSP NAL unit nal unit type
nal unit type syntax structure type class 0 TRAIL NUT Coded slice of a trailing picture VCL
slice_layer_rbsp() 1 STSA_NUT Coded slice of an STSA picture VCL slice_layer_rbsp() 2 RADL_NUT
Coded slice of a RADL picture VCL slice_layer_rbsp() 3 RASL_NUT Coded slice of a RASL picture
VCL slice layer rbsp() 4...6 RSV VCL 4... Reserved non-IRAP VCL NAL VCL RSV VCL 6 unit
types 7 IDR_W_RADL Coded slice of an IDR picture VCL 8 IDR_N_LP slice_layer_rbsp() 9
CRA_NUT Coded slice of a CRA picture VCL silce_layer_rbsp() 10 GDR_NUT Coded slice of a GDR
picture VCL slice_layer_rbsp() 11 RSV_IRAP_11 Reserved IRAP VCL NAL unit VCL 12
RSV IRAP 12 types 13 DPS_NUT Decoding parameter set non-VCL decoding_parameter_set_rbsp()
14 VPS_NUT Video parameter set non-VCL video_parameter_set_rbsp() 15 SPS_NUT Sequence
parameter set non-VCL seq_parameter_set_rbsp() 16 PPS_NUT Picture parameter set non-VCL
pic_parameter_set_rbsp() 17 PREFIX_APS_NUT Adaptation parameter set non-VCL 18
SUFFIX_APS_NUT adaptation_parameter_set_rbsp() 19 PH_NUT Picture header non-VCL
picture header rbsp() 20 AUD NUT AU delimiter non-VCL access unit delimiter rbsp() 21
EOS NUT End of sequence non-VCL end of seq rbsp() 22 EOB NUT End of bitstream non-VCL
end_of_bitstream_rbsp() 23 PREFIX_SEI_NUT Supplemental enhancement information non-VCL 24
SUFFIX_SEI_NUT sei_rbsp() 25 FD_NUT Filler data non-VCL filler_data_rbsp() 26 RSV_NVCL_26
Reserved non-VCL NAL unit types non-VCL 27 RSV NVCL 27 28 . . . 31 UNSPEC 28 . . . Unspecified
non-VCL NAL unit types non-VCL UNSPEC_31 [0104] NOTE—A clean random access (CRA) picture
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may have associated RASL or RADL pictures present in the bitstream. [0105] NOTE—An instantaneous decoding refresh (IDR) picture having nal\_unit\_type equal to IDR\_N\_LP does not have associated leading pictures present in the bitstream. An IDR picture having nal\_unit\_type equal to IDR\_W\_RADL does not have associated RASL pictures present in the bitstream, but may have associated RADL pictures in the bitstream. [0106] For VCL NAL units of any particular picture, the following applies: [0107] If mixed\_nalu\_types\_in\_pic\_flag is equal to 0, the value of nal\_unit\_type shall be the same for all coded slice NAL units of a picture. A picture or a PU is referred to as having the same NAL unit type as the coded slice NAL units of the picture or PU. [0108] Otherwise (mixed nalu types in pic flag equal to 1), one or more of the VCL NA L1 units shall all have a particular value of nal\_unit\_type in the range of IDR W RADL to CRA NUT, inclusive, and the other VCL NAL units shall all have a particular value of nal unit type in the range of TRAIL NUT to RSV VCL 6, inclusive, or equal to GRA NUT. [0109] For a single-layer Bitstream, the following constraints apply: [0110] Each picture, other than the first picture in the Bitstream in decoding order, is considered to be associated with the previous IRAP picture in decoding order. [0111] When a picture is a leading picture of an IRAP picture, it shall be a RADL or RASL picture. [0112] When a picture is a trailing picture of an IRAP picture, it shall not be a RADL or RASL picture. [0113] No RASL pictures shall be present in the bitstream that are associated with an IDR picture. [0114] No RADL pictures shall be present in the bitstream that are associated with an IDR picture having nal\_unit\_type equal to IDR\_N\_LP. [0115] NOTE—It is possible to perform random access a the position of an IRAP PU by discarding all PUs before the IRAP PU (and to correctly decode the IRAP picture and all the subsequent non-RASL pictures in decoding order), provided each parameter set is available (either in the bitstream or by external means not specified in this Specification) [0116] Any picture that precedes an IRAP picture in decoding order shall precede the IRAP picture in output order and shall precede any RADL picture associated wt h the IRAP picture in output order. [0117] Any RASL picture associated with a CRA picture shall precede any RADL picture associated with the CRA picture in output order. [0118] Any RASL picture associated with a CRA picture shall follow, in output order, any TRAP picture that precedes the CRA picture in decoding order. [0119] If field seg flag is equal to 0 and the current picture is a leading picture associated with an IRAP picture, it shall precede in decoding order, all non-leading pictures that are associated with the same IRAP picture. Otherwise, let picA and picB be the first and the last leading pictures, in decoding order, associated with an IRAP picture, respectively, there shall be at most one non-leading picture preceding picA in decoding order, and there shall be no non-leading picture between picA and picB in decoding order.

[0120] It should be noted that generally, an Intra Random Access Point (IRAP) picture is a picture that does not refer to any pictures other than itself for prediction in its decoding process. In JVET-P2001, an IRAP picture may be a clean random access (CRA) picture or an instantaneous decoder refresh (IDR) picture. In JVET-P2001, the first picture in the bitstream in decoding order must be an IRAP or a gradual decoding refresh (GDR) picture. JVET-P2001 describes the concept of a leading picture, which is a picture that precedes the associated IRAP picture in output order. JVET-P2001 further describes the concept of a trailing picture which is a non-IRAP picture that follows the associated IRAP picture in output order. Trailing pictures associated with an IRAP picture also follow the IRAP picture in decoding order. For IDR pictures, there are no trailing pictures that require reference to a picture decoded prior to the IDR picture. JVET-P2001 provides where a CRA picture may have leading pictures that follow the CRA picture in decoding order and contain inter picture prediction references to pictures decoded prior to the CRA picture. Thus, when the CRA picture is used as a random access point these leading pictures may not be decodable and are identified as random access skipped leading (RASL) pictures. The other type of picture that can follow an IRAP picture in decoding order and precede it in output order is the random access decodable leading (RADL) picture, which cannot contain references to any pictures that precede the IRAP picture in decoding order. A GDR picture, is a picture for which each VCL NAL unit has nal unit type equal to GDR NUT. If the current picture is a GDR picture that is associated with a picture header which signals a syntax element receovery\_poc\_cnt and there is a picture picA that follows the current GDR picture in decoding order in the CLVS and that has PicOrderCntVal equal to the PicOrderCntVal of the current GDR picture plus the value of recovery\_poc\_cnt, the picture picA is referred to as the recovery point picture.

[0121] As provided in Table 2, a NAL unit may include a sequence parameter set syntax structure. Table 3

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illustrates the syntax structure of the SPS provided in JVET-P2001.
TABLE-US-00003 TABLE 3 Descriptor seq_parameter_set_rbsp() {
                                                                 sps_decoding_parameter_set_id
      sps_reserved_zero_4bits
u(4)
      sps_ptl_dpb_hrd_params_present_flag u(1) if( sps_ptl_dpb_hrd_params_present_flag )
u(4)
profile_tier_level( 1, sps_max_sublayers_minus1 )
                                                 gdr_enabled_flag u(1)
                                                                        sps_seq_parameter_set_id
      chroma_format_idc u(2) if( chroma_format_idc == 3 )
u(4)
                                                                     separate_colour_plane_flag
      ref_pic_resampling_enabled_flag u(1) pic_width_max_in_luma_samples ue(v)
u(1)
                                       sps log2 ctu size minus5 u(2)
pic height max in luma samples ue(v)
                                                                       subpics_present_flag u(1)
                                                                      for( i = 0; i <=
  if( subpics_present_flag ) {
                                  sps_num_subpics_minus1 u(8)
sps_num_subpics_minus1; i++ ) {
                                         subpic_ctu_top_left_x[ i ] u(v)
subpic_ctu_top_left_y[ i ] u(v)
                                     subpic width minus1[i]u(v)
                                                                           subpic height minus1[
               subpic_treated_as_pic_flag[ i ] u(1)
                                                          loop_filter_across_subpic_enabled_flag[i
i ] u(v)
1 u(1)
                    sps_subpic_id_present_flag u(1) if( sps_subpics_id_present_flag ) {
sps subpic id signalling present flag u(1)
                                              if(sps subpics id signalling present flag) {
         sps_subpic_id_len_minus1 ue(v)
                                                for( i = 0; i <= sps_num_subpics_minus1; i++)
           sps_subpic_id[ i ] u(v)
                                            bit_depth_minus8 ue(v)
                                        }
                                                                     min_qp_prime_ts_minus4
                                    sps_weighted_bipred_flag u(1)
       sps_weighted_pred_flag u(1)
ue(v)
log2_max_pic_order_cnt_lsb_minus4 u(4) sps_poc_msb_flag u(1)
                                                                 if( sps_poc_msb_flag )
poc_msb_len_minus1 ue(v) if( sps_max_sublayers_minus1 > 0 )
                                                                  sps_sublayer_dpb_params_flag
      if( sps_ptl_dpb_hrd_params_present_flag )
                                                   dpb_parameters( 0, sps_max_sublayers_minus1,
                                                              inter_layer_ref_pics_present_flag
sps_sublayer_dpb_params_flag ) long_term_ref_pics_flag u(1)
                                                                for( i = 0; i <
      sps_idr_rpl_present_flag u(1) rpl1_same_as_rpl0_flag u(1)
!rpl1_same_as_rpl0_flag ? 2 : 1; i++ ) {
                                         num_ref_pic_lists_in_sps[ i ] ue(v)
                                                                              for(j = 0; j < 0
num_ref_pic_lists_in_sps[ i ]; j++)
                                         ref_pic_list_struct(i, j) } if( ChromaArrayType != 0 )
    qtbtt_dual_tree_intra_flag u(1) log2_min_luma_coding_block_size_minus2 ue(v)
partition_constraints_override_enabled_flag u(1) sps_log2_diff_min_qt_min_cb_intra_slice_luma
       sps_log2_diff_min_qt_min_cb_inter_slice ue(v) sps_max_mtt_hierarchy_depth_inter_slice
       sps_max_mtt_hierarchy_depth_intra_slice_luma_ue(v)
                                                            if(
ue(v)
sps_max_mtt_hierarchy_depth_intra_slice_luma != 0) {
sps_log2_diff_max_bt_min_qt_intra_slice_luma ue(v)
sps_log2_diff_max_tt_min_qt_intra_slice_luma_ue(v)
                                                       if(
sps_max_mtt_hierarchy_depth_inter_slice != 0) {
                                                        sps_log2_diff_max_bt_min_qt_inter_slice
            sps_log2_diff_max_tt_min_qt_inter_slice ue(v)
                                                              if( qtbtt dual tree intra flag ) {
ue(v)
       sps_log2_diff_min_qt_min_cb_intra_slice_chroma ue(v)
sps_max_mtt_hierarchy_depth_intra_slice_chroma ue(v)
                                                           if(
sps max mtt hierarchy depth intra slice chroma != 0) {
sps_log2_diff_max_bt_min_qt_intra_slice_chroma ue(v)
sps_log2_diff_max_tt_min_qt_intra_slice_chroma ue(v)
                                           sps_joint_cbcr_enabled_flag u(1) if( ChromaArrayType
sps_max_luma_transform_size_64_flag u(1)
              same_qp_table_for_chroma u(1)
                                                   numQpTables = same_qp_table_for_chroma ? 1 :
                                          for(i = 0; i < numQpTables; i++) {
(sps_joint_cbcr_enabled_flag?3:2)
qp_table_start_minus26[ i ] se(v)
                                        num_points_in_qp_table_minus1[ i ] ue(v)
                                                                                        for(j = 0;
                                                         delta_qp_in_val_minus1[ i ][ j ] ue(v)
j <= num_points_in_qp_table_minus1[ i ]; j++ ) {</pre>
           delta_qp_diff_val[ i ][ j ] ue(v)
                                                             } sps_sao_enabled_flag u(1)
                          sps_transform_skip_enabled_flag u(1) if(
sps alf enabled flag u(1)
sps_transform_skip_enabled_flag)
                                     sps_bdpcm_enabled_flag u(1) if( sps_bdpcm_enabled_flag
&& chroma_format_idc == 3)
                                      sps_bdpcm_chroma_enabled_flag u(1)
sps ref wraparound enabled flag u(1)
                                      if( sps_ref_wraparound_enabled_flag )
                                        sps_temporal_mvp_enabled_flag u(1)
sps ref wraparound offset minus1 ue(v)
sps_temporal_mvp_enabled_flag)
                                    sps_sbtmvp_enabled_flag u(1)
                                                                   sps_amvr_enabled_flag u(1)
sps_bdof_enabled_flag u(1) if( sps_bdof_enabled_flag )
                                                          sps_bdof_pic_present_flag u(1)
sps_smvd_enabled_flag u(1) sps_dmvr_enabled_flag u(1) if( sps_dmvr_enabled_flag)
sps_dmvr_pic_present_flag u(1) sps_mmvd_enabled_flag u(1) sps_isp_enabled_flag u(1)
```

```
sps_mip_enabled_flag u(1) if( ChromaArrayType != 0 )
sps_mrl_enabled_flag u(1)
sps_cclm_enabled_flag u(1)
                             if( chroma_format_idc == 1 ) {
sps_chroma_horizontal_collocated_flag u(1)
                                                   sps_chroma_vertical_collocated_flag u(1)
sps mts enabled flag u(1)
                             if( sps_mts_enabled_flag ) {
                                                                sps explicit mts intra enabled flag
            sps_explicit_mts_inter_enabled_flag u(1)
                                                           sps_sbt_enabled_flag u(1)
u(1)
                                                      }
                                                                    sps_affine_type_flag u(1)
sps_affine_enabled_flag u(1)
                              if( sps_affine_enabled_flag ) {
sps_affine_amvr_enabled_flag u(1)
                                          sps_affine_prof_enabled_flag u(1)
sps affine prof enabled flag)
                                        sps_prof_pic_present_flag u(1)
                                                                             if( chroma_format_idc
                                                                         }
                                                      sps_act_enabled_flag u(1)
                 sps_palette_enabled_flag u(1)
                             sps_ibc_enabled_flag u(1)
                                                          sps_ciip_enabled_flag u(1)
sps bcw enabled flag u(1)
sps mmvd enabled flag)
                                 sps_fpel_mmvd_enabled_flag u(1) sps_triangle_enabled_flag u(1)
  sps_lmcs_enabled_flag u(1)
                                sps_lfnst_enabled_flag u(1)
                                                              sps_ladf_enabled_flag u(1)
sps_ladf_enabled_flag ) {
                                sps_num_ladf_intervals_minus2 u(2)
sps_ladf_lowest_interval_qp_offset se(v)
                                               for(i = 0; i < sps num ladf intervals minus2 + 1; i++)
           sps_ladf_qp_offset[ i ] se(v)
                                                 sps_ladf_delta_threshold_minus1[ i ] ue(v)
  sps_scaling_list_enabled_flag u(1)
                                      sps_loop_filter_across_virtual_boundaries_disabled_present_flag
      if(sps loop filter across virtual boundaries disabled present flag) {
sps_num_ver_virtual_boundaries u(2)
                                          for( i = 0; i < sps_num_ver_virtual_boundaries; i++ )</pre>
         sps_virtual_boundaries_pos_x[i]u(13)
                                                     sps_num_hor_virtual_boundaries u(2)
                                                                                                for( i
= 0; i < sps num hor virtual boundaries; i++)
                                                        sps_virtual_boundaries_pos_y[ i ] u(13)
if( sps_ptl_dpb_hrd_params_present_flag ) {
                                                 sps_general_hrd_params_present_flag u(1)
                                                                                                if(
sps_general_hrd_params_present_flag ) {
                                                  general_hrd_parameters( )
sps_max_sublayers_minus1 > 0)
                                            sps_sublayer_cpb_params_present_flag u(1)
firstSubLayer = sps_sublayer_cpb_params_present_flag ? 0 :
                                                                          sps_max_sublayers_minus1
         ols_hrd_parameters( firstSubLayer, sps_max_sublayers_minus1 )
                                                                                      field_seq_flag
       vui_parameters_present_flag u(1) if( vui_parameters_present_flag )
                                                                                vui parameters()/*
Specified in ITU-T H.SEI | ISO/IEC 23002-7 */
                                                 sps_extension_flag u(1)
                                                                          if( sps_extension_flag )
       while( more_rbsp_data( ) )
                                           sps_extension_data_flag u(1)
                                                                          rbsp_trailing_bits() }
[0122] With respect to Table 3, JVET-P2001 provides the following semantics: [0123] An SPS RBSP shall
be available to the decoding process prior to it being referenced, included in at least one AU with
TemporalId equal to 0 or provided through external means. [0124] All SPS NAL units with a particular
value of sps_seq_parameter_set_id in a CVS shall have the same content. [0125]
sps decoding parameter set id, when greater than 0, specifies the value of
dps_decoding_parameter_set_id for the DPS referred to by the SPS. When
sps_decoding_parameter_set_id is equal to 0, the SPS does not refer to a DPS and no DPS is referred to
when decoding each CLVS referring to the SPS. The value of sps decoding parameter set id shall be the
same in all SPSs that are referred to by coded pictures in a bitstream. [0126] sps_video_parameter_set_id,
when greater than 0, specifies the value of vps_video_parameter_set_id for the VPS referred to by the
SPS. [0127] When sps_video_parameter_set_id is equal to 0, the following applies: [0128] The SPS does
not refer to a VPS. [0129] No VPS is referred to when decoding each CLVS referring to the SPS. [0130]
The value of vps_max_layers_minus1 is inferred to be equal to 0. [0131] The CVS shall contain only one
layer (i.e., all VCL NAL unit in the CVS shall have the same value of nuh_layer_id). [0132] The value of
GeneralLayerIdx[nuh_layer_id] is inferred to be equal to 0. [0133] The value of
vps_independent_layer_flag[GeneralLayerIdx[nuh_layer_id]] is inferred to be equal to 1. [0134] When
vps_independent_layer_flag[GeneralLayerIdx[nuh_layer_id]] is equal to 1, the SPS referred to by a CLVS
with a particular nuh_layer_id value nuhLayerId shall have nuh_layer_id equal to nuhLayerId. [0135]
sps_max_sublayers_minus1 plus 1 specifies the maximum number of temporal sublayers that may be
present in each CLVS referring to the SPS. The value of sps_max_sublayers_minus1 shall be in the range
of 0 to vps max sublayers minus1, inclusive. [0136] sps reserved zero 4 bits shall be equal to 0 in
bitstreams conforming to this version of this Specification. Other values for sps_reserved_zero_4 bits are
reserved for future use by ITU-T|ISO/IEC. [0137] sps_ptl_dpb_hrd_params_present_flag equal to 1
specifies that a profile_tier_level() syntax structure and a dpb_parameters() syntax structure are present
in the SPS, and a general_hrd_parameters( ) syntax structure and an ols_hrd_parameters( ) syntax
```

```
structure may also be present in the SPS. sps_ptl_dpb_hrd_params_present_flag equal to 0 specifies that
none of these syntax structures is present in the SPS. The value of sps_ptl_dpb_hrd_params_present_flag
shall be equal to vps_independent_layer_flag[nuh_layer_id]. [0138] If
vps independent layer flag[GeneralLayerIdx[nuh layer id]] is equal equal to 1, the variable
MaxDecPicBuffMinus1 is set equal to max_dec_pic_buffering_minus1[sps_max_sublayers_minus1] in
the dpb_parameters() syntax structure in the Otherwise, MaxDecPicBuffMinus1 is set SPS. equal to in
the max_dec_pic_buffering_minus1[sps_max_sublayers_minus1]
layer nonoutput dpb params idx[GeneralLayerIdx[nuh layer id]]-th dpb parameters() syntax structure
in the VPS. [0139] gdr_enabled_flag equal to 1 specifies that GDR pictures may be present in CLVSs
referring to the SPS. gdr_enabled_flag equal to 0 specifies that GDR pictures are not present in CLVSs
referring to the SPS. [0140] sps seg parameter set id provides an identifier for the SPS for reference by
other syntax elements. [0141] SPS NAL units, regardless of the nuh_layer_id values, share the same value
space of sps seg parameter set id. [0142] chroma format idc specifies the chroma sampling relative to
the luma sampling as specified. [0143] separate colour plane flag equal to 1 specifies that the three
colour components of the 4:4:4 chroma format are coded separately. separate_colour_plane_flag equal to
0 specifies that the colour components are not coded separately. When separate_colour_plane_flag is not
present, it is inferred to be equal to 0. When separate_colour_plane_flag is equal to 1, the coded picture
consists of three separate components, each of which consists of coded samples of one colour plane (Y,
Cb, or Cr) and uses the monochrome coding syntax. In this case, each colour plane is associated with a
specific colour plane id value. [0144] NOTE—There is no dependency in decoding processes between
the colour planes having different colour_plane_id values. For example, the decoding process of a
monochrome picture with one value of colour_plane_id does not use any data from monochrome pictures
having different values of colour plane id for inter prediction. [0145] Depending on the value of
separate_colour_plane_flag, the value of the variable ChromaArrayType is assigned as follows: [0146] If
separate_colour_plane_flag is equal to 0, ChromaArrayType is set equal to chroma_format_idc. [0147]
Otherwise (separate colour plane flag is equal to 1), ChromaArrayType is set equal to 0. [0148]
ref_pic_resampling_enabled_flag equal to 1 specifies that reference picture resampling may be applied
when decoding coded pictures in the CLVSs referring to the SPS. ref_pic_resampling_enabled_flag equal
to 0 specifies that reference picture resampling is not applied when decoding pictures in CLVSs referring
to the SPS. [0149] pic width max in luma samples specifies the maximum width, in units of luma
samples, of each decoded picture referring to the SPS. pic_width_max_in_luma_samples shall not be
equal to 0 and shall be an integer multiple of Max (8, MinCbSizeY). [0150]
pic height max in luma samples specifies the maximum height, in units of luma samples, of each
decoded picture referring to the SPS. pic_height_max_in_luma_samples shall not be equal to 0 and shall
be an integer multiple of Max (8, MinCbSizeY). [0151] sps_log 2_ctu_size_minus5 plus 5 specifies the
luma coding tree block size of each CTU. It is a requirement of bitstream conformance that the value of
sps_log 2_ctu_size_minus5 be less than or equal to 2. [0152] The variables CtbLog 2SizeY and CtbSizeY
are derived as follows:
[00003]CtbLog2SizeY = sps log2 ctu size minus5 + 5CtbSizeY = 1 << CtbLog2SizeY [0153]
subpics_present_flag equal to 1 specifies that subpicture parameters are present in in the SPS RBSP
syntax. subpics_present_flag equal to 0 specifies that subpicture parameters are not present in the SPS
RBSP syntax. [0154] NOTE—When a bitstream is the result of a sub-bitstream extraction process and
contains only a subset of the subpictures of the input bitstream to the sub-bitstream extraction process, it
might be required to set the value of subpics_present_flag equal to 1 in the RBSP of the SPSs. [0155]
sps num subpics minus1 plus 1 specifies the number of subpictures, sps num subpics minus1 shall be
in the range of 0 to 254. When not present, the value of sps_num_subpics_minus1 is inferred to be equal
to 0. [0156] subpic_ctu_top_left_x[i] specifies horizontal position of top left CTU of i-th subpicture in
unit of CtbSizeY. The length of the syntax element is Ceil (Log
```

2(pic\_width\_max\_in\_luma\_samples/CtbSizeY)) bits. When not present, the value of subpic\_ctu\_top\_left\_x[i] is inferred to be equal to 0. [0157] subpic\_ctu\_top\_left\_y[i] specifies vertical position of top left CTU of i-th subpicture in unit of CtbSizeY. The length of the syntax element is Ceil(Log 2(pic\_height\_max\_in\_luma\_samples/CtbSizeY)) bits. When not present, the value of subpic\_ctu\_top\_left\_y[i] is inferred to be equal to 0. [0158] subpic\_width\_minus1[i] plus 1 specifies the

```
2(pic_width_max_in_luma_samples/CtbSizeY)) bits. When not present, the value of
subpic_width_minus1[i] is inferred to be equal to Ceil (pic_width_max_in_luma_samples/CtbSizeY)-1.
[0159] subpic height minus1[i] plus 1 specifies the height of the i-th subpicture in units of CtbSizeY. The
length of the syntax element is Ceil(Log 2(pic_height_max_in_luma_samples/CtbSizeY)) bits. When not
present, the value of subpic_height_minus1[i] is inferred to be equal to Ceil
(pic_height_max_in_luma_samples/CtbSizeY)-1. [0160] subpic_treated_as_pic_flag[i] equal to 1
specifies that the i-th subpicture of each coded picture in the CLVS is treated as a picture in the decoding
process excluding in-loop filtering operations. subpic_treated_as_pic_flag[i] equal to 0 specifies that the i-
th subpicture of each coded picture in the CLVS is not treated as a picture in the decoding process
excluding in-loop filtering operations. When not present, the value of subpic treated as pic flag[i] is
inferred to be equal to 0. [0161] loop_filter_across_subpic_enabled_flag[i] equal to 1 specifies that in-
loop filtering operations may be performed across the boundaries of the i-th subpicture in each coded
picture in the CLVS. loop filter across subpic enabled flag[i] equal to 0 specifies that in-loop filtering
operations are not performed across the boundaries of the i-th subpicture in each coded picture in the
CLVS. When not present, the value of loop filter across subpic enabled pic flag[i] is inferred to be
equal to 1. [0162] It is a requirement of bitstream conformance that the following constraints apply:
[0163] 1 For any two subpictures subpicA and subpicB, when the subpicture index of subpicA is less than
that of subpicB, any coded slice NAL unit of subPicA shall precede any coded slice NAL unit of subPicB
in decoding order. [0164] The shapes of the subpictures shall be such that each subpicture, when decoded,
shall have its entire left boundary and entire top boundary consisting of picture boundaries or consisting
of boundaries of previously decoded subpictures. [0165] sps_subpic_id_present_flag equal to 1 specifies
that subpicture ID mapping is present in the SPS, sps subpic id present flag equal to 0 specifies that
subpicture ID mapping is not present in the SPS. [0166] sps_subpic_id_signalling_present_flag equal to 1
specifies that subpicture ID mapping is signalled in the SPS. sps_subpic_id_signalling_present_flag equal
to 0 specifies that subpicture ID mapping is not signalled in the SPS. When not present, the value of
sps_subpic_id_signalling_present_flag is inferred to be equal to 0. [0167] sps_subpic_id_len_minus1 plus
1 specifies the number of bits used to represent the syntax element sps_subpic_id[i]. The value of
sps subpic id len minus1 shall be in the range of 0 to 15, inclusive. [0168] sps subpic id[i] specifies
that subpicture ID of the i-th subpicture. The length of the sps subpic id[i] syntax element is
sps_subpic_id_len_minus1+1 bits. When not present, and when sps_subpic_id_present_flag equal to 0,
the value of sps_subpic_id[i] is inferred to be equal to i, for each i in the range of 0 to
sps num subpics minus1, inclusive [0169] bit depth minus8 specifies the bit depth of the samples of the
luma and chroma arrays, BitDepth, and the value of the luma and chroma quantization parameter range
offset, QpBdOffset, as follows:
[00004]BitDepth = 8 + bit depth minus8QpBdOffset = 6 * bit depth minus8 [0170] bit depth minus8
shall be in the range of 0 to 8, inclusive. [0171] min_qp_prime_ts_minus4 specifies the minimum allowed
quantization parameter for transform skip mode as follows:
[00005]QpPrimeTsMin = 4 + min_qp_prime_ts_minus4 [0172] The value of min_qp_prime_ts_minus4
shall be in the range of 0 to 48, inclusive. [0173] sps_weighted_pred_flag equal to 1 specifies that
weighted prediction may be applied to P slices referring to the SPS. sps_weighted_pred_flag equal to 0
specifies that weighted prediction is not applied to P slices referring to the SPS. [0174]
sps_weighted_bipred_flag equal to 1 specifies that explicit weighted prediction may be applied to B slices
referring to the SPS. sps_weighted_bipred_flag equal to 0 specifies that explicit weighted prediction is not
applied to B slices referring to the SPS. [0175] log 2 max pic order cnt lsb minus4 specifies the value
```

width of the i-th subpicture in units of CtbSizeY. The length of the syntax element is Ceil(Log

[00006]MaxPicOrderCntLsb = 2<sup>(log2\_max\_pic\_order\_cnt\_lsb\_minus4 + 4)</sup> [0176] The value of log 2\_max\_pic\_order\_cnt\_lsb\_minus4 shall be in the range of 0 to 12, inclusive. [0177] sps\_poc\_msb\_flag equal to 1 specifies that the ph\_poc\_msb\_cycle\_present\_flag syntax element is present in PHs referring to the SPS. sps\_poc\_msb\_flag equal to 0 specifies that the ph\_poc\_msb\_cycle\_present\_flag syntax element is not present in PHs referring to the SPS. [0178] poc\_msb\_len\_minus1 plus 1 specifies the length, in bits, of the poc\_msb\_val syntax elements, when present in the PHs referring to the SPS. The value of

of the variable MaxPicOrderCntLsb that is used in the decoding process for picture order count as

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poc_msb_len_minus1 shall be in the range of 0 to 32-log 2_max_pic_order_cnt_lsb_minus4-5, inclusive.
[0179] sps_sublayer_dpb_params_flag is used to control the presence of max_num_reorder pics[i],
max_dec_pic_buffering_minus1[i], and max_latency_increase_plus1[i] syntax elements in the
dpb parameters() syntax structure in the SPS. When not present, the value of
sps_sub_dpb_params_info_present_flag is inferred to be equal to 0. [0180] long_term_ref_pics_flag equal
to 0 specifies that no LTRP is used for interprediction of any coded picture in the CLVS.
long_term_ref_pics_flag equal to 1 specifies that LTRPs may be used for inter prediction of one or more
coded pictures in the CLVS. [0181] inter layer ref pics present flag equal to 0 specifies that no ILRP is
used for inter prediction of any coded picture in the CLVS. inter_layer_ref_pics_flag equal to 1 specifies
that ILRPs may be used for interprediction of one or more coded pictures in the CLVS. When
sps video parameter set id is equal to 0, the value of inter layer ref pics present flag is inferred to be
equal to 0. When vps_independent_layer_flag[GeneralLayerIdx[nuh_layer_id]] is equal to 1, the value of
inter_layer_ref_pics_present_flag shall be equal to 0. [0182] sps_idr_rpl_present_flag equal to 1 specifies
that reference picture list syntax elements are present in slice headers of IDR pictures.
sps_idr_rpl_present_flag equal to 0 specifies that reference picture list syntax elements are not present in
slice headers of IDR pictures. [0183] rpl1_same_as_rp10_flag equal to 1 specifies that the syntax element
num_ref_pic_lists_in_sps[1] and the syntax structure ref_pic_list_struct(1, rplsIdx) are not present and the
following applies: [0184] The value of num_ref_pic_lists_in_sps[1] is inferred to be equal to the value of
num_ref_pic_lists_in_sps[0]. [0185] The value of each of syntax elements in ref_pic_list_struct(1,
rplsIdx) is inferred to be equal to the value of corresponding syntax element in ref pic list struct(0,
rplsIdx) for rplsIdx ranging from 0 to num_ref_pic_lists_in_sps[0]-1. [0186] num_ref_pic_lists_in_sps[i]
specifies the number of the ref_pic_list_struct(listIdx, rplsIdx) syntax structures with listIdx equal to i
included in the SPS. The value of num_ref_pic_lists_in_sps[i] shall be in the range of 0 to 64, inclusive.
[0187] NOTE—For each value of listIdx (equal to 0 or 1), a decoder should allocate memory for a total
number of num_ref_pic_lists_in_sps[i]+1 ref_pic_list_struct(listIdx, rplsIdx) syntax structures since there
may be one ref pic list struct(listIdx, rplsIdx) syntax structure directly signalled in the slice headers of a
current picture. [0188] qtbtt_dual_tree_intra_flag equal to 1 specifies that for I slices, each CTU is split
into coding units with 64×64 luma samples using an implicit quadtree split and that these coding units are
the root of two separate coding_tree syntax structure for luma and chroma. qtbtt_dual_tree_intra_flag
equal to 0 specifies separate coding tree syntax structure is not used for I slices. When
gtbtt_dual_tree_intra_flag is not present, it is inferred to be equal to 0. [0189] log
2_min_luma_coding_block_size_minus2 plus 2 specifies the minimum luma coding block size. The value
range of log 2 min luma coding block size minus 2 shall be in the range of 0 to log
2_ctu_size_minus5+3, inclusive. [0190] The variables MinCbLog 2SizeY, MinCbSizeY, IbcBufWidthY,
IbcBufWidthC and Vsize are derived as follows:
[00007]MinCbLog2SizeY = log2 min luma coding block size minus2 + 2
MinCbSizeY = 1 << MinCbLog2SizeYIbcBufWidthY = 256 * 128 / CtbSizeY
IbcBufWidthC = IbcBufWidthY / SubWidthCVSize = Min(64, CtbSizeY) [0191] The value of
MinCbSizeY shall less than or equal to VSize. [0192] The variables CtbWidthC and CtbHeightC, which
specify the width and height, respectively, of the array for each chroma CTB, are derived as follows:
[0193] If chroma_format_ide is equal to 0 (monochrome) or separate_colour_plane_flag is equal to 1,
CtbWidthC and CtbHeightC are both equal to 0. [0194] Otherwise, CtbWidthC and CtbHeightC are
derived as follows:
[00008]CtbWidthC - CtbSizeY / SubWidthCCtbHeightC - CtbSizeY / SubHeightC [0195] For log 2Block
Width ranging from 0 to 4 and for log 2BlockHeight ranging from 0 to 4, inclusive, the up-right diagonal
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Width ranging from 0 to 4 and for log 2BlockHeight ranging from 0 to 4, inclusive, the up-right diagonal and raster scan order array initialization process as specified is invoked with 1<<log 2BlockWidth and 1<<log 2BlockHeight as inputs, and the output is assigned to DiagScanOrder[log 2BlockWidth][log 2BlockHeight]. [0196] For log 2BlockWidth ranging from 0 to 6 and for log 2BlockHeight ranging from 0 to 6, inclusive, the horizontal and vertical traverse scan order array initialization process as specified is invoked with 1<<log 2BlockWidth and 1<<log 2BlockHeight as inputs, and the output is assigned to HorTravScanOrder[log 2Block Width][log 2Block Height] and VerTravScanOrder[log 2Block Width][log 2BlockHeight]. [0197] partition\_constraints\_override\_enabled\_flag equal to 1 specifies the presence of partition\_constraints\_override\_flag in PHs referring to the SPS.

```
partition_constraints_override_enabled_flag equal to 0 specifies the absence of
partition_constraints_override_flag in PHs referring to the SPS. [0198] sps_log
2_diff_min_qt_min_cb_intra_slice_luma specifies the default difference between the base 2 logarithm of
the minimum size in luma samples of a luma leaf block resulting from quadtree splitting of a CTU and the
base 2 logarithm of the minimum coding block size in luma samples for luma CUs in slices with
slice_type equal to 2 (I) referring to the SPS. When partition_constraints_override_enabled_flag is equal
to 1, the default difference can be overridden by pic_log 2_diff_min_qt_min_cb_luma present in PHs
referring to the SPS. The value of sps log 2 diff min gt min cb intra slice luma shall be in the range
of 0 to CtbLog 2SizeY–MinCbLog 2SizeY, inclusive. The base 2 logarithm of the minimum size in luma
samples of a luma leaf block resulting from quadtree splitting of a CTU is derived as follows:
[00009]MinQtLog2SizeIntraY = sps log2 diff min qt min cb intra slice luma + MinCbLog2SizeY
[0199] sps_log 2_diff_min_qt_min_cb_inter_slice specifies the default difference between the base 2
logarithm of the minimum size in luma samples of a luma leaf block resulting from quadtree splitting of a
CTU and the base 2 logarithm of the minimum luma coding block size in luma samples for luma CUs in
slices with slice_type equal to 0 (B) or 1 (P) referring to the SPS. When
partition_constraints_override_enabled_flag is equal to 1, the default difference can be overridden by
pic log 2 diff min gt min cb luma present in PHs referring to the SPS. The value of sps log
2_diff_min_qt_min_cb_inter_slice shall be in the range of 0 to CtbLog 2SizeY-MinCbLog 2SizeY,
inclusive. The base 2 logarithm of the minimum size in luma samples of a luma leaf block resulting from
quadtree splitting of a CTU is derived as follows:
[00010]MinQtLog2SizeInterY = sps_log2_diff_min_qt_min_cb_inter_slice + MinCbLog2SizeY [0200]
sps_max_mtt_hierarchy_depth_inter_slice specifies the default maximum hierarchy depth for coding
units resulting from multi-type tree splitting of a quadtree leaf in slices with slice_type equal to 0 (B) or 1
(P) referring to the SPS. When partition_constraints_override_enabled_flag is equal to 1, the default
maximum hierarchy depth can be overridden by pic_max_mtt_hierarchy_depth_inter_slice present in PHs
referring to the SPS. The value of sps max mtt hierarchy depth inter slice shall be in the range of 0 to
2*(CtbLog 2SizeY-MinCbLog 2SizeY), inclusive. [0201]
sps_max_mtt_hierarchy_depth_intra_slice_luma specifies the default maximum hierarchy depth for
coding units resulting from multi-type tree splitting of a quadtree leaf in slices with slice_type equal to 2
(I) referring to the SPS. When partition constraints override enabled flag is equal to 1, the default
maximum hierarchy depth can be overridden by pic_max_mtt_hierarchy_depth_intra_slice_luma present
in PHs referring to the SPS. The value of sps_max_mtt_hierarchy_depth_intra_slice_luma shall be in the
range of 0 to 2*(CtbLog 2SizeY-MinCbLog 2SizeY), inclusive. [0202] sps log
2_diff_max_bt_min_qt_intra_slice_luma specifies the default difference between the base 2 logarithm of
the maximum size (width or height) in luma samples of a luma coding block that can be split using a
binary split and the minimum size (width or height) in luma samples of a luma leaf block resulting from
quadtree splitting of a CTU in slices with slice_type equal to 2 (I) referring to the SPS. When
partition_constraints_override_enabled_flag is equal to 1, the default difference can be overridden by
pic log 2 diff max bt min qt luma present in PHs referring to the SPS. The value of sps log
2_diff_max_bt_min_qt_intra_slice_luma shall be in the range of 0 to CtbLog 2SizeY-MinQtLog
2SizeIntraY, inclusive. When sps_log 2_diff_max_bt_min_qt_intra_slice_luma is not present, the value of
sps_log 2_diff_max_bt_min_qt_intra_slice_luma is inferred to be equal to 0. [0203] sps_log
2_diff_max_tt_min_qt_intra_slice_luma specifies the default difference between the base 2 logarithm of
the maximum size (width or height) in luma samples of a luma coding block that can be split using a
ternary split and the minimum size (width or height) in luma samples of a luma leaf block resulting from
quadtree splitting of a CTU in slices with slice_type equal to 2 (I) referring to the SPS. When
partition_constraints_override_enabled_flag is equal to 1, the default difference can be overridden by
pic_log 2_diff_max_tt_min_qt_luma present in PHs referring to the SPS. The value of sps_log
2 diff max tt min qt intra slice luma shall be in the range of 0 to CtbLog 2SizeY-MinQtLog
2SizeIntraY, inclusive. When sps_log 2_diff_max_tt_min_qt_intra_slice_luma is not present, the value of
sps_log 2_diff_max_tt_min_qt_intra_slice_luma is inferred to be equal to 0. [0204] sps_log
2 diff max bt min gt inter slice specifies the default difference between the base 2 logarithm of the
maximum size (width or height) in luma samples of a luma coding block that can be split using a binary
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split and the minimum size (width or height) in luma samples of a luma leaf block resulting from quadtree
splitting of a CTU in slices with slice_type equal to 0 (B) or 1 (P) referring to the SPS. When
partition_constraints_override_enabled_flag is equal to 1, the default difference can be overridden by
pic log 2 diff max bt min qt luma present in PHs referring to the SPS. The value of sps log
2_diff_max_bt_min_qt_inter_slice shall be in the range of 0 to CtbLog 2SizeY-MinQtLog 2SizeInterY,
inclusive. When sps_log 2_diff_max_bt_min_qt_inter_slice is not present, the value of sps_log
2_diff_max_bt_min_qt_inter_slice is inferred to be equal to 0. [0205] sps_log
2 diff max tt min gt inter slice specifies the default difference between the base 2 logarithm of the
maximum size (width or height) in luma samples of a luma coding block that can be split using a ternary
split and the minimum size (width or height) in luma samples of a luma leaf block resulting from quadtree
splitting of a CTU in slices with slice type equal to 0 (B) or 1 (P) referring to the SPS. When
partition_constraints_override_enabled_flag is equal to 1, the default difference can be overridden by
pic_log 2_diff_max_tt_min_qt_luma present in PHs referring to the SPS. The value of sps_log
2 diff max tt min qt inter slice shall be in the range of 0 to CtbLog 2SizeY-MinQtLog 2SizeInterY,
inclusive. When sps_log 2_diff_max_tt_min_qt_inter_slice is not present, the value of sps_log
2_diff_max_tt_min_qt_inter_slice is inferred to be equal to 0. [0206] sps_log
2 diff min qt min cb intra slice chroma specifies the default difference between the base 2 logarithm
of the minimum size in luma samples of a chroma leaf block resulting from quadtree splitting of a chroma
CTU with treeType equal to DUAL_TREE_CHROMA and the base 2 logarithm of the minimum coding
block size in luma samples for chroma CUs with treeType equal to DUAL TREE CHROMA in slices
with slice_type equal to 2 (I) referring to the SPS. When partition_constraints_override_enabled_flag is
equal to 1, the default difference can be overridden by pic_log 2_diff_min_qt_min_cb_chroma present in
PHs referring to the SPS. The value of sps_log 2_diff_min_qt_min_cb_intra_slice_chroma shall be in the
range of 0 to CtbLog 2SizeY-MinCbLog 2SizeY, inclusive. When not present, the value of sps_log
2_diff_min_qt_min_cb_intra_slice_chroma is inferred to be equal to 0. The base 2 logarithm of the
minimum size in luma samples of a chroma leaf block resulting from quadtree splitting of a CTU with
treeType equal to DUAL_TREE_CHROMA is derived as follows:
[00011]MinQtLog2SizeIntraC = sps_log2_diff_min_qt_min_cb_intra_slice_chroma + MinCbLog2SizeY
[0207] sps max mtt hierarchy depth intra slice chroma specifies the default maximum hierarchy depth
for chroma coding units resulting from multi-type tree splitting of a chroma quadtree leaf with treeType
equal to DUAL_TREE_CHROMA in slices with slice_type equal to 2 (I) referring to the SPS. When
partition_constraints_override_enabled_flag is equal to 1, the default maximum hierarchy depth can be
overridden by pic max mtt hierarchy depth chroma present in PHs referring to the SPS. The value of
sps_max_mtt_hierarchy_depth_intra_slice_chroma shall be in the range of 0 to 2*(CtbLog
2SizeY-MinCbLog 2SizeY), inclusive. When not present, the value of
sps max mtt hierarchy depth intra slice chroma is inferred to be equal to 0. [0208] sps log
2_diff_max_bt_min_qt_intra_slice_chroma specifies the default difference between the base 2 logarithm
of the maximum size (width or height) in luma samples of a chroma coding block that can be split using a
binary split and the minimum size (width or height) in luma samples of a chroma leaf block resulting from
quadtree splitting of a chroma CTU with treeType equal to DUAL_TREE_CHROMA in slices with
slice_type equal to 2 (I) referring to the SPS. When partition_constraints_override_enabled_flag is equal
to 1, the default difference can be overridden by pic_log 2_diff_max_bt_min_qt_chroma present in PHs
referring to the SPS. The value of sps_log 2_diff_max_bt_min_qt_intra_slice_chroma shall be in the
range of 0 to CtbLog 2SizeY-MinQtLog 2SizeIntraC, inclusive. When sps_log
2_diff_max_bt_min_qt_intra_slice_chroma is not present, the value of sps_log
2_diff_max_bt_min_qt_intra_slice_chroma is inferred to be equal to 0. [0209] sps_log
2_diff_max_tt_min_qt_intra_slice_chroma specifies the default difference between the base 2 logarithm
of the maximum size (width or height) in luma samples of a chroma coding block that can be split using a
ternary split and the minimum size (width or height) in luma samples of a chroma leaf block resulting
from quadtree splitting of a chroma CTU with treeType equal to DUAL_TREE_CHROMA in slices with
slice_type equal to 2 (I) referring to the SPS. When partition_constraints_override_enabled_flag is equal
to 1, the default difference can be overridden by pic log 2 diff max tt min gt chroma present in PHs
referring to the SPS. The value of sps_log 2_diff_max_tt_min_qt_intra_slice_chroma shall be in the range
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of 0 to CtbLog 2SizeY-MinQtLog 2SizeIntraC, inclusive. When sps_log
2_diff_max_tt_min_qt_intra_slice_chroma is not present, the value of sps_log
2_diff_max_tt_min_qt_intra_slice_chroma is inferred to be equal to 0. [0210]
sps max luma transform size 64 flag equal to 1 specifies that the maximum transform size in luma
samples is equal to 64. sps_max_luma_transform_size_64_flag equal to 0 specifies that the maximum
transform size in luma samples is equal to 32. [0211] When CtbSizeY is less than 64, the value of
sps max luma transform size 64 flag shall be equal to 0. [0212] The variables MinTbLog 2SizeY,
MaxTbLog 2SizeY, MinTbSizeY, and MaxTbSizeY are derived as follows:
[00012]MinTbLog2SizeY = 2MaxTbLog2SizeY = sps_max_luma_transform_size_64_flag?6:5
MinTbSizeY = 1 << MinTbLog2SizeYMaxTbSizeY = 1 << MaxTbLog2SizeY [0213]
sps joint cbcr enabled flag equal to 0 specifies that the joint coding of chroma residuals is disabled.
sps_joint_cber_enabled_flag equal to 1 specifies that the joint coding of chroma residuals is enabled.
[0214] same_qp_table_for_chroma equal to 1 specifies that only one chroma QP mapping table is
signalled and this table applies to Cb and Cr residuals and additionally to joint Cb-Cr residuals when
sps_joint_cber_enabled_flag is equal to 1. same_qp_table_for_chroma equal to 0 specifies that chroma
QP mapping tables, two for Cb and Cr, and one additional for joint Cb-Cr when
sps joint cbcr enabled flag is equal to 1, are signalled in the SPS. When not present in the
same_qp_table_for_chroma is bitstream, the value of same_qp_table_for_chroma is inferred to be equal
to 1. [0215] qp_table_start_minus26[i] plus 26 specifies the starting luma and chroma QP used to describe
the i-th chroma QP mapping table. The value of qp_table_start_minus26[i] shall be in the range of -26-
QpBdOffset to 36 inclusive. When qp_table_start_minus26[i] is not present in the bitstream, the value of
qp_table_start_minus26[i] is inferred to be equal to 0. [0216] num_points_in_qp_table_minus1[i] plus 1
specifies the number of points used to describe the i-th chroma QP mapping table. The value of
num_points_in_qp_table_minus1[i] shall be in the range of 0 to 63+QpBdOffset, inclusive. When
num_points_in_qp_table_minus1[0] is not present in the bitstream, the value of
num points in qp table minus1[0] is inferred to be equal to 0. [0217] delta qp in val minus1[i][j]
specifies a delta value used to derive the input coordinate of the j-th pivot point of the i-th chroma QP
mapping table. When delta_qp_in_val_minus1[0][j] is not present in the bitstream, the value of
delta_qp_in_val_minus1[0][j] is inferred to be equal to 0. [0218] delta_qp_diff_val [i][j] specifies a delta
value used to derive the output coordinate of the j-th pivot point of the i-th chroma QP mapping table.
[0219] The i-th chroma QP mapping table ChromaQpTable [i] for i=0..numQpTables-1 is derived as
follows:
TABLE-US-00004 qpInVal[ i ][ 0 ] = -qp_table_start_minus26[i] + 26 qpOutVal[i][ 0 ] = qpInVal[i][
0 ] for( j = 0; j \le num\_points\_in\_qp\_table\_minus1[i]; j + + ) { qpInVal[i][j + 1] = qpInVal[i][j] +
delta_qp_in_val_minus1[ i ][ j ] + 1
                                      qpOutVal[ i ][ i + 1] = qpOutVal[ i ][ j ] +
delta_qp_in_val_minus1[ i ][ j ] {circumflex over ( )} delta_qp_diff val[ i ][ j ] ) } ChromaQpTable[ i ][
qpInVal[i][0] = qpOutVal[i][0] for (k = qpInVal[i][0] - 1; k > = -QpBdOffset; k - - )
ChromaQpTable[ i ][ k ] = Clip3(-QpBdOffset, 63, ChromaQpTable[ i ][ k + 1 ] - 1 ) for( j = 0; j <+
num\_points\_in\_qp\_table\_minus1[i]; j++) { sh = (delta\_qp\_in\_val\_minus1[i][j] + 1) >> 1
= qpInVal[ i ][ j ] + 1, m = 1; k <= qpInval[ i ][ j + 1 ]; k++, m++ )
                                                                      ChromaQpTable[ i ][ k ] =
ChromaQpTable[ i ][ qpInVal[ i ][ j ] +
                                               ((qpOutVal[i][j+1]-qpOutVal[i][j])*m+sh)/(
delta_qp_in_val_minus1[ i ][j] + 1 ) } for( k = qpInVal[ i ][ num_points_in_qp_table_minus1[ i ] + 1 ] +
1; k \le 63; k++) ChromaQpTable[ i ][ k ] = Clip3( -QpBdOffset, 63, ChromaQpTable[ i ][ k - 1 ] + 1
) When same_qp_table_for_chroma is equal to 1, ChromaQpTable[ 1 ][ k ] and ChromaQpTable[ 2 ][ k ]
are set equal to ChromaQpTable[0][k] for k = -QpBdOffset..63. It is a requirement of bitstream
conformance that the values of qpInVal[i][j] and qpOutVal[i][j] shall be in the range of -QpBdOffset
to 63, inclusive for i = 0.. numQpTables – 1 and j = 0..num_points_in_qp_table_minus1[ i ] + 1. [0220]
sps_sao_enabled_flag equal to 1 specifies that the sample adaptive offset process is applied to the
reconstructed picture after the deblocking filter process, sps sao enabled flag equal to 0 specifies that the
sample adaptive offset process is not applied to the reconstructed picture after the deblocking filter
process. [0221] sps_alf_enabled_flag equal to 0 specifies that the adaptive loop filter is disabled.
sps alf enabled flag equal to 1 specifies that the adaptive loop filter is enabled. [0222]
sps_transform_skip_enabled_flag equal to 1 specifies that transform_skip_flag may be present in the
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transform unit syntax. sps_transform_skip_enabled_flag equal to 0 specifies that transform_skip_flag is
not present in the transform unit syntax [0223] sps_bdpcm_enabled_flag equal to 1 specifies that
intra_bdpcm_luma_flag may be present in the coding unit syntax for intra coding units.
sps bdpcm enabled flag equal to 0 specifies that intra bdpcm luma flag is not present in the coding unit
syntax for intra coding units. When not present, the value of sps_bdpcm_enabled_flag is inferred to be
equal to 0. [0224] sps_bdpcm_chroma_enabled_flag equal to 1 specifies that intra_bdpem_chroma_flag
may be present in the coding unit syntax for intra coding units. sps bdpem chroma enabled flag equal to
0 specifies that intra bdpcm chroma flag is not present in the coding unit syntax for intra coding units.
When not present, the value of sps_bdpcm_chroma_enabled_flag is inferred to be equal to 0. [0225]
sps_ref_wraparound_enabled_flag equal to 1 specifies that horizontal wrap-around motion compensation
is applied in inter prediction, sps ref wraparound enabled flag equal to 0 specifies that horizontal wrap-
around motion compensation is not applied. When the value of (CtbSizeY/MinCbSizeY+1) is less than or
equal to (pic_width_in_luma_samples/MinCbSizeY-1), where pic_width_in_luma_samples is the value
of pic width in luma samples in any PPS that refers to the SPS, the value of
sps_ref_wraparound_enabled_flag shall be equal to 0. [0226] sps_ref_wraparound_offset_minus1 plus 1
specifies the offset used for computing the horizontal wrap-around position in units of MinCbSizeY luma
samples. The value of ref wraparound offset minus1 shall be in the range of (CtbSizeY/MinCbSizeY)+1
to (pic_width_in_luma_samples/MinCbSizeY)-1, inclusive, where pic_width_in_luma_samples is the
value of pic_width_in_luma_samples in any PPS that refers to the SPS. [0227]
sps temporal mvp enabled flag equal to 1 specifies that temporal motion vector predictors may be used
in the CLVS. sps_temporal_mvp_enabled_flag equal to 0 specifies that temporal motion vector predictors
are not used in the CLVS. [0228] sps_sbtmvp_enabled_flag equal to 1 specifies that subblock-based
temporal motion vector predictors may be used in decoding of pictures with all slices having slice type
not equal to I in the CLVS. sps_sbtmvp_enabled_flag equal to 0 specifies that subblock-based temporal
motion vector predictors are not used in the CLVS. When sps_sbtmvp_enabled_flag is not present, it is
inferred to be equal to 0. [0229] sps_amvr_enabled_flag equal to 1 specifies that adaptive motion vector
difference resolution is used in motion vector coding. amvr_enabled_flag equal to 0 specifies that
adaptive motion vector difference resolution is not used in motion vector coding. [0230]
sps bdof enabled flag equal to 0 specifies that the bi-directional optical flow interprediction is disabled.
sps bdof enabled flag equal to 1 specifies that the bi-directional optical flow interprediction is enabled.
[0231] sps_bdof_pic_present_flag equal to 1 specifies that pic_disable_bdof_flag is present in PHs
referring to the SPS. sps_bdof_pic_present_flag equal to 0 specifies that pic_disable_bdof_flag is not
present in PHs referring to the SPS. When sps bdof pic present flag is not present, the value of
sps_bdof_pic_present_flag is inferred to be equal to 0. [0232] sps_smvd_enabled_flag equal to 1 specifies
that symmetric motion vector difference may be used in motion vector decoding, sps smvd enabled flag
equal to 0 specifies that symmetric motion vector difference is not used in motion vector coding. [0233]
sps_dmvr_enabled_flag equal to 1 specifies that decoder motion vector refinement based inter bi-
prediction is enabled. sps dmvr enabled flag equal to 0 specifies that decoder motion vector refinement
based inter bi-prediction is disabled. [0234] sps dmvr pic present flag equal to 1 specifies that
pic_disable_dmvr_flag is present in PHs referring to the SPS. sps_dmvr_pic_present_flag equal to 0
specifies that pic_disable_dmvr_flag is not present in PHs referring to the SPS. When
sps_dmvr_pic_present_flag is not present, the value of sps_dmvr_pic_present_flag is inferred to be equal
to 0. [0235] sps_mmvd_enabled_flag equal to 1 specifies that merge mode with motion vector difference
is enabled. sps_mmvd_enabled_flag equal to 0 specifies that merge mode with motion vector difference is
disabled. [0236] sps isp enabled flag equal to 1 specifies that intra prediction with subpartitions is
enabled. sps_isp_enabled_flag equal to 0 specifies that intra prediction with subpartitions is disabled.
[0237] sps_mrl_enabled_flag equal to 1 specifies that intra prediction with multiple reference lines is
enabled. sps mrl enabled flag equal to 0 specifies that intra prediction with multiple reference lines is
disabled. [0238] sps mip enabled flag equal to 1 specifies that matrix-based intra prediction is enabled.
sps_mip_enabled_flag equal to 0 specifies that matrix-based intra prediction is disabled. [0239]
sps_cclm_enabled_flag equal to 0 specifies that the cross-component linear model intra prediction from
luma component to chroma component is disabled. sps cclm enabled flag equal to 1 specifies that the
cross-component linear model intra prediction from luma component to chroma component is enabled.
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When sps_cclm_enabled_flag is not present, it is inferred to be equal to 0. [0240]
sps_chroma_horizontal_collocated_flag equal to 1 specifies that prediction processes operate in a manner
designed for chroma sample positions that are not horizontally shifted relative to corresponding luma
sample positions. sps chroma horizontal collocated flag equal to 0 specifies that prediction processes
operate in a manner designed for chroma sample positions that are shifted to the right by 0.5 in units of
luma samples relative to corresponding luma sample positions. When
sps chroma horizontal collocated flag is not present, it is inferred to be equal to 1. [0241]
sps chroma vertical collocated flag equal to 1 specifies that prediction processes operate in a manner
designed for chroma sample positions that are not vertically shifted relative to corresponding luma sample
positions, sps chroma vertical collocated flag equal to 0 specifies that prediction processes operate in a
manner designed for chroma sample positions that are shifted downward by 0.5 in units of luma samples
relative to corresponding luma sample positions. When sps_chroma_vertical_collocated_flag is not
present, it is inferred to be equal to 1. [0242] sps_mts_enabled_flag equal to 1 specifies that
sps explicit mts intra enabled flag is present in the sequence parameter set RBSP syntax and that
sps_explicit_mts_inter_enabled_flag is present in the sequence parameter set RBSP syntax.
sps_mts_enabled_flag equal to 0 specifies that sps_explicit_mts_intra_enabled_flag is not present in the
sequence parameter set RBSP syntax and that sps explicit mts inter enabled flag is not present in the
sequence parameter set RBSP syntax. [0243] sps_explicit_mts_intra_enabled_flag equal to 1 specifies that
mts_idx may be present in intra coding unit syntax, sps_explicit_mts_intra_enabled_flag equal to 0
specifies that mts idx is not present in intra coding unit syntax. When not present, the value of
sps_explicit_mts_intra_enabled_flag is inferred to be equal to 0. [0244]
sps_explicit_mts_inter_enabled_flag equal to 1 specifies that mts_idx may be present in inter coding unit
syntax, sps explicit mts inter enabled flag equal to 0 specifies that mts idx is not present in inter
coding unit syntax. When not present, the value of sps_explicit_mts_inter_enabled_flag is inferred to be
equal to 0. [0245] sps_sbt_enabled_flag equal to 0 specifies that subblock transform for inter-predicted
CUs is disabled, sps sbt enabled flag equal to 1 specifies that subblock transform for inter-predicteds
CU is enabled. [0246] sps_affine_enabled_flag specifies whether affine model based motion
compensation can be used for inter prediction. If sps_affine_enabled_flag is equal to 0, the syntax shall be
constrained such that no affine model based motion compensation is used in the CLVS, and
inter affine flag and cu affine type flag are not present in coding unit syntax of the CLVS. Otherwise
(sps_affine_enabled_flag is equal to 1), affine model based motion compensation can be used in the
CLVS. [0247] sps_affine_type_flag specifies whether 6-parameter affine model based motion
compensation can be used for interprediction. If sps affine type flag is equal to 0, the syntax shall be
constrained such that no 6-parameter affine model based motion compensation is used in the CLVS, and
cu_affine_type_flag is not present in coding unit syntax in the CLVS. Otherwise (sps_affine_type_flag is
equal to 1), 6-parameter affine model based motion compensation can be used in the CLVS. When not
present, the value of sps_affine_type_flag is inferred to be equal to 0. [0248]
sps affine amvr enabled flag equal to 1 specifies that adaptive motion vector difference resolution is
used in motion vector coding of affine inter mode, sps affine amvr enabled flag equal to 0 specifies that
adaptive motion vector difference resolution is not used in motion vector coding of affine inter mode.
When not present, the value of sps_affine_amvr_enabled_flag is inferred to be equal to 0. [0249]
sps affine prof enabled flag specifies whether the prediction refinement with optical flow can be used
for affine motion compensation. If sps_affine_prof_enabled_flag is equal to 0, the affine motion
compensation shall not be refined with optical flow. Otherwise (sps_affine_prof_enabled_flag is equal to
1), the affine motion compensation can be refined with optical flow. When not present, the value of
sps_affine_prof_enabled_flag is inferred to be equal to 0. [0250] sps_prof_pic_present_flag equal to 1
specifies that pic_disable_prof_flag is present in PHs referring to the SPS. sps_prof_pic_present_flag
equal to 0 specifies that pic disable prof flag is not present in PHs referring to the SPS. When
sps prof pic present flag is not present, the value of sps prof pic present flag is inferred to be equal to
0. [0251] sps_palette_enabled_flag equal to 1 specifies that pred_mode_plt_flag may be present in the
coding unit syntax. sps_palette_enabled_flag equal to 0 specifies that pred_mode_plt_flag is not present
in the coding unit syntax. When sps palette enabled flag is not present, it is inferred to be equal to 0.
[0252] sps_act_enabled_flag equal to 1 specifies that adaptive colour transform may be used and the
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that adaptive colour transform is not used and cu_act_enabled_flag is not present in the coding unit
syntax. When sps_act_enabled_flag is not present, it is inferred to be equal to 0. [0253]
sps bcw enabled flag specifies whether bi-prediction with CU weights can be used for inter prediction.
If sps_bcw_enabled_flag is equal to 0, the syntax shall be constrained such that no bi-prediction with CU
weights is used in the CLVS, and bew_idx is not present in coding unit syntax of the CLVS. Otherwise
(sps_bcw_enabled_flag is equal to 1), bi-prediction with CU weights can be used in the CLVS. [0254]
sps ibc enabled flag equal to 1 specifies that the IBC prediction mode may be used in decoding of
pictures in the CLVS. sps_ibc_enabled_flag equal to 0 specifies that the IBC prediction mode is not used
in the CLVS. When sps_ibc_enabled_flag is not present, it is inferred to be equal to [0255]
sps ciip enabled flag specifies that ciip flag may be present in the coding unit syntax for inter coding
units. sps_ciip_enabled_flag equal to 0 specifies that ciip_flag is not present in the coding unit syntax for
inter coding units. [0256] sps_fpel_mmvd_enabled_flag equal to 1 specifies that merge mode with motion
vector difference is using integer sample precision, sps fpel mmvd enabled flag equal to 0 specifies that
merge mode with motion vector difference can use fractional sample precision. [0257]
sps_triangle_enabled_flag specifies whether triangular shape based motion compensation can be used for
inter prediction, sps triangle enabled flag equal to 0 specifies that the syntax shall be constrained such
that no triangular shape based motion compensation is used in the CLVS, and merge_triangle_split_dir,
merge_triangle_idx0, and merge_triangle_idx1 are not present in coding unit syntax of the CLVS.
sps triangle enabled flag equal to 1 specifies that triangular shape based motion compensation can be
used in the CLVS. [0258] sps_lmcs_enabled_flag equal to 1 specifies that luma mapping with chroma
scaling is used in the CLVS. sps_lmcs_enabled_flag equal to 0 specifies that luma mapping with chroma
scaling is not used in the CLVS. [0259] sps_lfnst_enabled_flag equal to 1 specifies that Ifnst_idx may be
present in intra coding unit syntax. sps_lfnst_enabled_flag equal to 0 specifies that Ifnst_idx is not present
in intra coding unit syntax. [0260] sps_ladf_enabled_flag equal to 1, specifies that
sps_num_ladf_intervals_minus2, sps_ladf_qp_offset[i], and sps_ladf_lowest_interval_qp_offset,
sps_ladf_delta_threshold_minus1[i] are present in the SPS. [0261] sps_num_ladf_intervals_minus2 plus 1
specifies the number of sps_ladf_delta_threshold_minus1[i] and sps_ladf_qp_offset[i] syntax elements
that are present in the SPS. The value of sps_num_ladf_intervals_minus2 shall be in the range of 0 to 3,
inclusive. [0262] sps_ladf_lowest_interval_qp_offset specifies the offset used to derive the variable qP as
specified. The value of sps_ladf_lowest_interval_qp_offset shall be in the range of -63 to 63, inclusive.
[0263] sps_ladf_qp_offset[i] specifies the offset array used to derive the variable qP as specified. The
value of sps ladf qp offset[i] shall be in the range of -63 to 63, inclusive. [0264]
sps_ladf_delta_threshold_minus1[i] is used to compute the values of SpsLadfIntervalLowerBound[i],
which specifies the lower bound of the i-th luma intensity level interval. The value of
sps ladf delta threshold minus1[i] shall be in the range of 0 to 2.sup.BitDepth-3, inclusive. [0265] The
value of SpsLadfIntervalLowerBound [0] is set equal to 0. [0266] For each value of i in the range of 0 to
sps num ladf intervals minus2, inclusive, the variable SpsLadfIntervalLowerBound [i+1] is derived as
follows:
[00013]
SpsLadfIntervalLowerBound[i + 1] = SpsLadfIntervalLowerBound[i] + sps_ladf_delta_threshold_minus1[i] + 1
[0267] sps scaling list enabled flag equal to 1 specifies that a scaling list is used for the scaling process
for transform coefficients, sps_scaling_list_enabled_flag equal to 0 specifies that scaling list is not used
for the scaling process for transform coefficients. [0268]
sps_loop_filter_across_virtual_boundaries_disabled_present_flag equal to 1 specifies that the in-loop
filtering operations are disabled across the virtual boundaries in pictures referring to the SPS.
sps loop_filter_across_virtual_boundaries_disabled_present_flag equal to 0 specifies that no such
disabling of in-loop filtering operations is applied in pictures referring to the SPS. In-loop filtering
operations include the deblocking filter, sample adaptive offset filter, and adaptive loop filter operations.
[0269] sps_num_ver_virtual_boundaries specifies the number of sps_virtual_boundaries_pos_x[i] syntax
elements that are present in the SPS. When sps_num_ver_virtual_boundaries is not present, it is inferred
to be equal to 0. [0270] sps_virtual_boundaries_pos_x[i] is used to compute the value of
```

VirtualBoundariesPosX [i], which specifies the location of the i-th vertical virtual boundary in units of

cu\_act\_enabled\_flag may be present in the coding unit syntax. sps\_act\_enabled\_flag equal to 0 specifies

```
luma samples. The value of sps_virtual_boundaries_pos_x[i] shall be in the range of to Ceil
(pic_width_in_luma_samples+8)-1, inclusive. [0271] sps_num_hor_virtual_boundaries specifies the
number of sps_virtual_boundaries_pos_y[i] syntax elements that are present in the SPS. When
sps num hor virtual boundaries is not present, it is inferred to be equal to 0.
[0272] sps_virtual_boundaries_pos_v[i] is used to compute the value of VirtualBoundariesPosY[i], which
specifies the location of the i-th horizontal virtual boundary in units of luma samples. The value of
sps_virtual_boundaries_pos_y[i] shall be in the range of 1 to Ceil (pic_height_in_luma_samples+8)-1,
inclusive. [0273] sps general hrd params present flag equal to 1 specifies that the syntax structure
general_hrd_parameters() is present in the SPS RBSP syntax structure.
sps_general_hrd_params_present_flag equal to 0 specifies that the syntax structure
general hrd parameters() is not present in the SPS RBSP syntax structure. [0274]
sps_sublayer_cpb_params_present_flag equal to 1 specifies that the syntax structure old_hrd parameters(
) in the SPS RBSP includes HRD parameters for sublayer representations with TemporalId in the range of
0 to sps max sublayers minus1, inclusive, sps sublayer cpb params present flag equal to 0 specifies
that the syntax structure ols_hrd_parameters() in the SPS RBSP includes HRD parameters for the
sublayer representation with TemporalId equal to sps_max_sublayers_minus1 only. When
sps max sublayers minus1 is equal to 0, the value of sps sublayer cpb params present flag is inferred
to be equal to 0. [0275] When sps_sublayer_cpb_params_present_flag is equal to 0, the HRD parameters
for the sublayer representations with TemporalId in the range of 0 to sps_max_sublayers_minus1-1,
inclusive, are inferred to be the same as that for the sublayer representation with TemporalId equal to
sps_max_sublayers_minus1. These include the HRD parameters starting from the
fixed_pic_rate_general_flag[i] syntax element till the sublayer_hrd_parameters(i) syntax structure
immediately under the condition "if (general_vcl_hrd_params_present_flag)" in the ols_hrd_parameters
syntax structure. [0276] field_seq_flag equal to 1 indicates that the CLVS conveys pictures that represent
fields. field_seq_flag equal to 0 indicates that the CLVS conveys pictures that represent frames. When
general frame only constraint flag is equal to 1, the value of field seq flag shall be equal to 0. When
field_seq_flag is equal to 1, a frame-field information SEI message shall be present for every coded
picture in the CLVS. [0277] NOTE—The specified decoding process does not treat pictures that represent
fields or frames differently. A sequence of pictures that represent fields would therefore be coded with the
picture dimensions of an individual field. For example, pictures that represent 1080i fields would
commonly have cropped output dimensions of 1920×540, while the sequence picture rate would
commonly express the rate of the source fields (typically between 50 and 60 Hz), instead of the source
frame rate (typically between 25 and 30 Hz). [0278] vui parameters present flag equal to 1 specifies that
the syntax structure vui_parameters() is present in the SPS RBSP syntax structure.
vui_parameters_present_flag equal to 0 specifies that the syntax structure vui_parameters() is not present
in the SPS RBSP syntax structure. [0279] sps extension flag equal to 0 specifies that no
sps_extension_data_flag syntax elements are present in the SPS RBSP syntax structure.
sps_extension_flag equal to 1 specifies that there are sps_extension_data_flag syntax elements present in
the SPS RBSP syntax structure. [0280] sps extension data flag may have any value. Its presence and
value do not affect decoder conformance to profiles specified in this version of this Specification.
Decoders conforming to this version of this Specification shall ignore all sps_extension_data_flag syntax
elements. [0281] As described above, in JVET-P2001 when sps general hrd params present flag is
equal to 1 the syntax structure general_hrd_parameters() is present in the SPS RBSP syntax structure.
Table 4 illustrates the general_hrd_parameters() syntax structure provided in JVET-P2001.
TABLE-US-00005 TABLE 4 Descriptor general_hrd_parameters() {
                                                                      num units in tick u(32)
                   general_nal_hrd_params_present_flag u(1)
                                                                general_vcl_hrd_params_present_flag
time_scale u(32)
      general_decoding_unit_hrd_params_present_flag u(1)
general_decoding_unit_hrd_params_present_flag )
                                                       tick divisor minus2 u(8)
                                                                                   bit rate scale u(4)
  cpb size scale u(4) if (general decoding unit hrd params present flag )
                                                                                   cpb size du scale
       hrd_cpb_cnt_minus1 ue(v) }
[0282] With respect to Table 4, JVET-P2001 provides the following semantics: [0283] The
general_hrd_parameters() syntax structure provides HRD parameters used in the HRD operations. It is a
requirement of bitstream conformance that the content of the general_hrd_parameters() syntax structure
```

```
present in any VPSs or SPSs in the bitstream shall be identical. [0284] When included in a VPS, the
general_hrd_parameters() syntax structure applies to all OLSs specified by the VPS. When included in an
SPS, the general_hrd_parameters() syntax structure applies to the OLS that includes only the layer that is
the lowest layer among the layers that refer to the SPS, and this lowest layer is an independent layer.
[0285] num_units_in_tick is the number of time units of a clock operating at the frequency time scale Hz
that corresponds to one increment (called a clock tick) of a clock tick counter. num_units_in_tick shall be
greater than 0. A clock tick, in units of seconds, is equal to the quotient of num_units_in_tick divided by
time scale. For example, when the picture rate of a video signal is 25 Hz, time scale may be equal to 27
000 000 and num_units_in_tick may be equal to 1 080 000, and consequently a clock tick may be equal to
0.04 seconds. [0286] time scale is the number of time units that pass in one second. For example, a time
coordinate system that measures time using a 27 MHz clock has a time scale of 27 000 000. The value of
time_scale shall be greater than 0. [0287] general_nal_hrd_params_present_flag equal to 1 specifies that
NAL HRD parameters (pertaining to Type II bitstream conformance point) are present in the
general hrd parameters() syntax structure, general nal hrd params present flag equal to 0 specifies
that NAL HRD parameters are not present in the general_hrd_parameters() syntax structure, [0288]
NOTE—When general_nal_hrd_params_present_flag is equal to 0, the conformance of the bitstream
cannot be verified without provision of the NAL HRD parameters and all BP SEI messages, and, when
general_vcl_hrd_params_present_flag is also equal to 0, all PT and DU information SEI messages, by
some means not specified in this Specification. [0289] The variable NalHrdBpPresentFlag is derived as
follows: [0290] If one or more of the following conditions are true, the value of NalHrdBpPresentFlag is
set equal to 1: [0291] general_nal_hrd_params_present_flag is present in the bitstream and is equal to 1.
[0292] The need for presence of BPs for NAL HRD operation to be present in the bitstream in [0293] BP
SEI messages is determined by the application, by some means not specified in this Specification. [0294]
Otherwise, the value of NalHrdBpPresentFlag is set equal to 0. [0295]
general_vcl_hrd_params_present_flag equal to 1 specifies that VCL HRD parameters (pertaining to Type
I bitstream conformance point) are present in the general hrd parameters() syntax structure.
general_vcl_hrd_params_present_flag equal to 0 specifies that VCL HRD parameters are not present in
the general_hrd_parameters() syntax structure. [0296] NOTE—When
general vcl hrd params present flag is equal to 0, the conformance of the bitstream cannot be verified
without provision of the VCL HRD parameters and all BP SEI messages, and when
general_nal_hrd_params_present_flag is also equal to 0, all PT and DU information SEI messages, by
some means not specified in this Specification. [0297] The variable VclHrdBpPresentFlag is derived as
follows: [0298] If one or more of the following conditions are true, the value of VelHrdBpPresentFlag is
set equal to 1: [0299] general_vel_hrd_params_present_flag is present in the bitstream and is equal to 1.
[0300] The need for presence of BPs for VCL HRD operation to be present in the bitstream in [0301] BP
SEI messages is determined by the application, by some means not specified in this Specification. [0302]
Otherwise, the value of VclHrdBpPresentFlag is set equal to 0. [0303] The variable
CpbDpbDelaysPresentFlag is derived as follows: [0304] If one or more of the following conditions are
true, the value of CpbDpbDelaysPresentFlag is set equal to 1: [0305]
general_nal_hrd_params_present_flag is present in the bitstream and is equal to 1. [0306]
general_vcl_hrd_params_present_flag is present in the bitstream and is equal to 1. [0307] The need for
presence of CPB and DPB output delays to be present in the bitstream in [0308] PT SEI messages is
determined by the application, by some means not specified in this Specification. [0309] Otherwise, the
value of CpbDpbDelaysPresentFlag is set equal to 0. [0310] It is a requirement of bitstream conformance
that the values of general nal hrd params present flag and general vel hrd params present flag in
each general_hrd_parameters() syntax structure shall not be both equal to 0. [0311]
general_decoding_unit_hrd_params_present_flag equal to 1 specifies that DU level HRD parameters are
present and the HRD may operate at AU level or DU level.
general decoding unit hrd params present flag equal to 0 specifies that DU level HRD parameters are
not present and the HRD operates at AU level. When general_decoding_unit_hrd_params_present_flag is
not present, its value is inferred to be equal to 0. [0312] tick_divisor_minus2 is used to specify the clock
sub-tick. A clock sub-tick is the minimum interval of time that can be represented in the coded data when
general_decoding_unit_hrd_params_present_flag is equal to 1. [0313] bit_rate_scale (together with
```

```
[0314] cpb_size_scale (together with cpb_size_value_minus1[i][j]) specifies the CPB size of the j-th CPB
when Htid is equal to i and when the CPB operates at the AU level. [0315] cpb_size_du_scale (together
with cpb size du value minus1[i][j]) specifies the CPB size of the j-th CPB when Htid is equal to i and
when the CPB operates at DU level. [0316] hrd_cpb_cnt_minus1 plus 1 specifies the number of
alternative CPB delivery schedules. The value of hrd_cpb_cnt_minus1 shall be in the range of 0 to 31,
inclusive. [0317] As described above, in JVET-P2001 when sps_sublayer_cpb_params_present_flag equal
to 1 the syntax structure old_hrd_parameters() is present in the SPS RBSP syntax structure. Table 5
illustrates the ols_hrd_parameters() syntax structure provided in JVET-P2001.
TABLE-US-00006 TABLE 5 Descriptor ols_hrd_parameters( firstSubLayer, maxSubLayers ) {
                                                                                                 for(i =
firstSubLayer; i <= maxSubLayers; i++) {
                                                   fixed pic rate general flag[i] u(1)
                                                                                             if(
!fixed_pic_rate_general_flag[ i ] )
                                         fixed_pic_rate_within_cvs_flag[ i ] u(1)
fixed_pic_rate_within_cvs_flag[ i ] )
                                            elemental_duration_in_tc_minus1[ i ] ue(v)
                                                                                             else if(
hrd cpb cnt minus1 == 0)
                                       low delay hrd flag[i]u(1)
                                                                         if(
general_nal_hrd_params_present_flag )
                                               sublayer_hrd_parameters( i )
                                                                                 if(
general_vcl_hrd_params_present_flag )
                                               sublayer_hrd_parameters( i )
[0318] With respect to Table 5, JVET-P2001 provides the following semantics: [0319] When an
ols_hrd_parameters() syntax structure is included in a VPS, the OLSs to which the ols_hrd_parameters()
syntax structure applies are specified by the VPS. When an ols_hrd_parameters() syntax structure is
included in an SPS, the ols hrd parameters() syntax structure applies to the OLS that includes only the
layer that is the lowest layer among the layers that refer to the SPS, and this lowest layer is an
independent layer. [0320] fixed_pic_rate_general_flag[i] equal to 1 indicates that, when Htid is equal to i,
the temporal distance between the HRD output times of consecutive pictures in output order is
constrained as specified below. fixed_pic_rate_general_flag[i] equal to 0 indicates that this constraint may
not apply. [0321] When fixed_pic_rate_general_flag[i] is not present, it is inferred to be equal to 0.
fixed pic rate within cvs flag[i] equal to 1 indicates that, when Htid is equal to i, the temporal distance
between the HRD output times of consecutive pictures in output order is constrained as specified below.
fixed_pic_rate_within_cvs_flag[i] equal to 0 indicates that this constraint may not apply. [0322] When
fixed_pic_rate_general_flag[i] is equal to 1, the value of fixed_pic_rate_within_cvs_flag[i] is inferred to
be equal to 1. [0323] elemental duration in tc minus1[i] plus 1 (when present) specifies, when Htid is
equal to i, the temporal distance, in clock ticks, between the elemental units that specify the HRD output
times of consecutive pictures in output order as specified below. The value of
elemental duration in tc minus1[i] shall be in the range of 0 to 2047, inclusive. [0324] When Htid is
equal to i and fixed_pic_rate_general_flag[i] is equal to 1 for a CVS containing picture n, and picture n is
a picture that is output and is not the last picture in the bitstream (in output order) that is output, the value
of the variable DpbOutputElementalInterval[n] is specified by:
```

bit\_rate\_value\_minus1[i][j]) specifies the maximum input bit rate of the j-th CPB when Htid is equal to i.

DpbOutputElementalInterval[n]=DpbOutputInterval[n]+ElementalOutputPeriods [0325] where DpbOutputInterval[n] is specified below and ElementalOutputPeriods is specified as follows: [0326] If a frame-field information SEI message is present for picture n that contains a display\_elemental\_periods\_minus1 syntax element, ElementalOutputPeriods is equal to the value of display\_elemental\_periods\_minus1+1. [0327] Otherwise, ElementalOutputPeriods is equal to 1. [0328] When Htid is equal to i and fixed\_pic\_rate\_general\_flag[i] is equal to 1 for a CVS containing picture n, and picture n is a picture that is output and is not the last picture in the bitstream (in output order) that is output, the value computed for DpbOutputElementalInterval [n] shall be equal to ClockTick\* (elemental\_duration\_in\_tc\_minus1[i]+1), wherein ClockTick is as specified as ClockTick=num\_units\_in\_tick+time\_scale (using the value of ClockTick for the CVS containing picture n) when one of the following conditions is true for the following picture in output order nextPicInOutputOrder that is specified as provided below: [0329] picture nextPicInOutputOrder is in the same CVS as picture n. [0330] picture nextPicInOutputOrder is in a different CVS and fixed\_pic\_rate\_general\_flag[i] is equal to 1 in the CVS containing picture nextPicInOutputOrder, the value of ClockTick is the same for both CVSs, and the value of elemental duration in tc minus1[i] is the same for both CVSs. [0331] When picture n is a picture that is output and is not the last picture of the

```
bitstream that is output, the value of the variable DpbOutputInterval[n] is derived as follows: [0332]
DpbOutputInterval[n]=DpbOutputTime[nextPicInOutputOrder]-DpbOutputTime[n] [0333] where
nextPicInOutputOrder is the picture that follows picture n in output order and has PictureOutputFlag
equal to 1. [0334] When Htid is equal to i and fixed pic rate within cvs flag[i] is equal to 1 for a CVS
containing picture n, and picture n is a picture that is output and is not the last picture in the CVS (in
output order) that is output, the value computed for DpbOutputElementalInterval [n] shall be equal to
ClockTick*(elemental duration in tc minus1[i]+1), wherein ClockTick is as specified provided above
(using the value of ClockTick for the CVS containing picture n) when the following picture in output
order nextPicInOutputOrder that is specified for use in the equation specified above is in the same CVS as
picture n. [0335] low delay hrd flag[i] specifies the HRD operational mode, when Htid is equal to i, as
specified. When not present, the value of low delay hrd flag[i] is inferred to be equal to 0. [0336] NOTE
—When low_delay_hrd_flag[i] is equal to 1, "big pictures" that violate the nominal CPB removal times
due to the number of bits used by an AU are permitted. It is expected, but not required, that such "big
pictures" occur only occasionally. [0337] As described above, in JVET-P2001 when
sublayer_hrd_parameters() may be present in the ols_hrd_parameters() syntax structure. Table 6
illustrates the sub_layer_hrd_parameters() syntax structure provided in JVET-P2001.
TABLE-US-00007 TABLE 6 De- scriptor sublayer_hrd_parameters( subLayerId ) {
                                                                                     for(i = 0; i)
hrd_cpb_cnt_minus1; j++ ) {
                                  bit_rate_value_minus1[ subLayerId ][ j ] ue(v)
cpb_size_value_minus1[ subLayerId ][ j ] ue(v)
                                                    if(general_decoding_unit_hrd_params_present_flag
          cpb_size_du_value_minus1[ subLayerId ][ j ] ue(v)
                                                                     bit rate du value minus1[
subLayerId |[ j ] ue(v)
                           }
                                  cbr_flag[ subLayerId ][ j ] u(1)
[0338] With respect to Table 6, JVET-P2001 provides the following semantics: [0339]
bit rate value minus1[i][j] (together with bit rate scale) specifies the maximum input bit rate for the j-th
CPB with Htid equal to i when the CPB operates at the AU level. bit_rate_value_minus1[i][j] shall be in
the range of 0 to 282-2, inclusive. For any j greater than 0 and any particular value of i,
bit rate value minus1[i][j] shall be greater than bit rate value minus1[i][j-1]. [0340] When
DecodingUnitHrdFlag is equal to 0, the following applies: [0341] The bit rate in bits per second is given
by:
[00014]BitRate[i][j] = (bit_rate_value_minus1[i][j] + 1) * 2<sup>(6 + bit_rate_scale)</sup> [0342] When the bit_rate_value_minus1[i][j] syntax element is not present, it is inferred as follows: [0343] If
general_hrd_params_present_flag is equal to 1, bit_rate_value_minus1[i][j] is inferred to be equal to
bit_rate_value_minus1[sps_max_sublayers_minus1][j]. [0344] Otherwise
(general_hrd_params_present_flag is equal to 0), the value of BitRate[i][j] is inferred to be equal to
CpbBrVelFactor*MaxBR for VCL HRD parameters and to be equal to CpbBrNalFactor*MaxBR for NAL
HRD parameters, where MaxBR, CpbBrVclFactor and CpbBrNalFactor are specified for example as
provided in Tables 7A-7C.
TABLE-US-00008 TABLE 7A Max CPB size MaxCPB Max luma picture (CpbVclFactor or Max slices
Max # of size MaxLumaPs CpbNalFactor bits) per picture Max # of tile rows tile columns Level
(samples) Main tier High tier MaxSlicePerPicture MaxTileRows MaxTileCols 1
                                                                                   36 864
                                                                                             350 - 16
                                       245 760 3 000 — 20 1 1 3
                                                                     552 960 6 000 — 30 2 2 3.1
        122 880 1 500 — 16 1 1 2.1
983 040 10 000 — 40 3 3 4 2 228 224 12 000 30 000 75 5 5 4.1 2 228 224 20 000 50 000 75 5 5 5 8
912 896 25 000 100 000 200 11 10 5.1 8 912 896 40 000 160 000 200 11 10 5.2 8 912 896 60 000 240
000 200 11 10 6 35 651 584 60 000 240 000 600 22 20 6.1 35 651 584 120 000 480 000 600 22 20 6.2
35 651 584 240 000 800 000 600 22 20
TABLE-US-00009 TABLE 7B Max bit rate MaxBR (BrVclFactor Max luma or BrNalFactor Min
compression sample rate bits/s) ratio MinCrBase MaxLumaSr Main High Main High Level (samples/sec)
                       552 960
                                  128 — 2 2 2
                                                   3 686 400 1 500 — 2 2 2.1
                                                                                    7 372 800 3 000 —
tier tier tier 1
223
        16 588 800 6 000 — 2 2 3.1
                                        33 177 600 10 000 — 2 2 4
                                                                       66 846 720 12 000 30 000 4 4
     133 693 440 20 000 50 000 4 4 5 267 386 880 25 000 100 000 6 4 5.1 534 773 760 40 000 160
000 8 4 5.2 1 069 547 520 60 000 240 000 8 4 6 1 069 547 520 60 000 240 000 8 4 6.1 2 139 095 040 120
000 480 000 8 4 6.2 4 278 190 080 240 000 800 000 6 4
TABLE-US-00010 TABLE 7C Profile CpbVclFactor CpbNalFactor FormatCapabilityFactor
```

MinCrScaleFactor Main 10 1 000 1 100 1.875 1.0 Main 4:4:4 10 2 500 2 750 3.750 0.5 [0345]

```
cpb_size_value_minus1[i][j] is used together with cpb_size_scale to specify the j-th CPB size with Htid
equal to i when the CPB operates at the AU level. cpb_size_value_minus1[i][j] shall be in the range of 0
to 2.sup.32-2, inclusive. For any j greater than 0 and any particular value of i, cpb_size_value_minus1[i]
[j] shall less than or equal be to cpb_size_value_minus1[i][j-1]. [0346] When DecodingUnitHrdFlag is
equal to 0, the following apples: [0347] The CPB size in bits is given by:
[00015]CpbSizc[i][j] = (cpb_size_value_minus1[i][j] + 1) * 2<sup>(4 + cpb_size_scale)</sup> [0348] When the
cpb_size_value_minus1[i][j] syntax element is not present, it is inferred as follows: [0349] If
general_hrd_params_present_flag is equal to 1, cpb_size_value_minus1[i][j] is inferred to be equal to
cpb_size_value_minus1[sps_max_sublayers_minus1][j]. [0350] Otherwise
(general_hrd_params_present_flag is equal to 0), the value of CpbSize[i][j] is inferred to be equal to
CpbBrVelFactor*MaxCPB for VCL HRD parameters and to be equal to CpbBrNalFactor*MaxCPB for
NAL HRD parameters, where MaxCPB, CpbBrVclFactor and CpbBrNalFactor are specified above.
[0351] cpb_size_du_value_minus1[i][j] is used together with cpb_size_du_scale to specify the i-th CPB
size with Htid to equal when the CPB operates at DU level. [0352] cpb_size_du_value_minus1[i][j] shall
be in the range of 0 to 2.sup.32-2, inclusive. For any j greater than 0 and any particular value of i,
cpb_size_du_value_minus1[i][j] shall be less than or equal to cpb_size_du_value_minus1[i][j-1]. [0353]
When DecodingUnitHrdFlag is equal to 1, the following applies: [0354] The CPB size in bits is given by: [00016]CpbSize[i][j] = (cpb_size_du_value_minus1[i][j] + 1) * 2<sup>(4 + cpb_size_du_scale)</sup> [0355] When the
cpb size du value minus1[i][j] syntax element is not present, it is inferred as follows: [0356] If
general_hrd_params_present_flag is equal to 1, cpb_size_du_value_minus1[i][j] is inferred to be equal to
cpb_size_du_value_minus1[sps_max_sublayers_minus1][j]. [0357] Otherwise
(general hrd params present flag is equal to 0), the value of CpbSize[i][j] is inferred to be equal to
CpbVclFactor*MaxCPB for VCL HRD parameters and to be equal to CpbNalFactor*MaxCPB for NAL
HRD parameters, where MaxCPB, CpbVclFactor and CpbNalFactor are specified above. [0358]
bit rate du value minus1[i][j] (together with bit rate scale) specifies the maximum input bit rate for the
j-th CPB with Htid equal to i when the CPB operates at the DU level. bit_rate_du_value_minus1[i][j]
shall be in the range of 0 to 2.sup.32-2, inclusive. For any j greater than 0 and any particular value of i,
bit_rate_du_value_minus1[i][j] shall be greater than bit_rate_du_value_minus1[i][j-1]. [0359] When
DecodingUnitHrdFlag is equal to 1, the following applies: [0360] The bit rate in bits per second is given
[00017]BitRate[i][j] = (bit_rate_du_value_minus1[i][j] + 1) * 2^{(6 + \text{bit}\_rate\_scale)} [0361] When the
bit rate du value minus1[i][j] syntax element is not present, it is inferred as follows: [0362] If
general_hrd_params_present_flag is equal to 1, bit_rate_du_value_minus1[i][j] is inferred to be
bit_rate_du_value_minus1[sps_max_sublayers_minus1][j]. [0363] Otherwise
(general hrd params present flag is equal to 0), the value of BitRate[i][i] is inferred to be equal to
BrVclFactor*MaxBR for VCL HRD parameters and to be equal to BrNalFactor*MaxBR for NAL HRD
parameters, where MaxBR, BrVelFactor and BrNalFactor are specified above. [0364] cbr_flag[i][j] equal
to 0 specifies that to decode this bitstream by the HRD using the j-th CPB specification, the hypothetical
stream scheduler (HSS) operates in an intermittent bit rate mode. cbr_flag[i][j] equal to 1 specifies that the
HSS operates in a constant bit rate (CBR) mode. [0365] When not present, the value of cbr_flag[i][j] it is
inferred as follows: [0366] When the cbr_flag[i][j] syntax element is not present, it is inferred as follows:
[0367] If general_hrd_params_present_flag is equal to 1, cbr_flag[i][j] is inferred to be equal to
cbr_flag[sps_max_sublayers_minus1][j]. [0368] Otherwise (general_hrd_params_present_flag is equal to
0), the value of cbr flag[i][i] is inferred to be equal to 0.
[0369] As described above, JVET-P2001 enables SEI messages to be signaled which assist in processes
related to decoding, display or other purposes. Further, a type of SEI message for VCL HRD operations
includes buffering period SEI messages. Table 8 illustrates the buffering period() syntax structure
provided in JVET-P2001.
TABLE-US-00011 TABLE 8 Descriptor buffering_period( payloadSize ) {
bp_nal_hrd_params_present_flag u(1)
                                            bp_vcl_hrd_params_present_flag u(1)
initial_cpb_removal_delay_length_minus1 u(5)
                                                      cpb_removal_delay_length_minus1 u(5)
dpb_output_delay_length_minus1 u(5)
                                             alt_cpb_params_present_flag u(1)
bp_decoding_unit_hrd_params_present_flag u(1)
                                                       if( bp_decoding_unit_hrd_params_present_flag ) {
```

```
du_cpb_removal_delay_increment_length_minus1 u(5)
dpb_output_delay_du_length_minus1 u(5)
                                                    decoding unit cpb params in pic timing sei flag
                concatenation_flag u(1)
                                             additional_concatenation_info_present_flag u(1)
additional concatenation info present flag)
                                                      max initial removal delay for concatination
         cpb_removal_delay_delta_minus1 u(v)
                                                     cpb_removal_delay_deltas_present_flag u(1)
u(v)
if( cpb_removal_delay_deltas_present_flag ) {
                                                        num_cpb_removal_delay_deltas_minus1 ue(v)
         for(i = 0; i <= num_cpb_removal_delay_deltas_minus1; i++ )</pre>
cpb removal delay delta[i]u(v)
                                             bp max sublayers minus1 u(3)
                                                                                  bp cpb cnt minus1
          sublayer_initial_cpb_removal_delay_present_flag u(1)
                                                                     for(i = (
ue(v)
sublayer_initial_cpb_removal_delay_present_flag?
                                                                         0:bp max sublayers minus1
     <= bp max sublayers minus1; i++ ) {</pre>
                                                       if(bp nal hrd params present flag)
                 for( j = 0; j < bp_cpb_cnt_minus1 + 1; j++ ) {
nal_initial_cpb_removal_delay[ i ][ j ] u(v)
                                                              nal initial cpb removal offset[i][i]
u(v)
                        if(bp decoding unit hrd params present flag) {
nal_initial_alt_cpb_removal_delay[ i ][ j ] u(v)
nal_initial_alt_cpb_removal_offset[ i ][ j ] u(v)
                                                                                                  if(
                                                   for( j = 0; j < bp_cpb_cnt_minus1 + 1; j++ ) {
bp_vcl_hrd_params_present_flag )
                   vcl_initial_cpb_removal_delay[ i ][ j ] u(v)
vcl_initial_cpb_removal_offset[ i ][ j ] u(v)
                                                              if(
bp_decoding_unit_hrd_params_present_flag ) {
vcl_initial_alt_cpb_removal_delay[ i ][ j ] u(v)
vcl_initial_alt_cpb_removal_offset[ i ][ j ] u(v)
                                                                                }
                                                                                                  if(
alt_cpb_params_present_flag )
                                           use alt cpb params flag u(1)
[0370] With respect to Table 8, JVET-P2001 provides the following semantics: [0371] A BP SEI message
provides initial CPB removal delay and initial CPB removal delay offset information for initialization of
the HRD at the position of the associated AU in decoding order. When the BP SEI message is present, a
picture is said to be a notDiscardablePic picture when the picture has TemporalId equal to 0 and is not a
RASL or RADL picture. [0372] When the current picture is not the first picture in the bitstream in
decoding order, let prevNonDiscardablePic be the preceding picture in decoding order with TemporalId
equal to 0 that is not a RASL or RADL picture. [0373] The presence of BP SEI messages is specified as
follows: [0374] If NalHrdBpPresentFlag is equal to 1 or VclHrdBpPresentFlag is equal to 1, the following
applies for each AU in the CVS: [0375] If the AU is an IRAP or GDR AU, a BP SEI message applicable
to the operation point shall be associated with the AU. [0376] Otherwise, if the AU contains a
notDiscardablePic, a BP SEI message applicable to the operation point may or may not be associated with
the AU. [0377] Otherwise, the AU shall not be associated with a BP SEI message applicable to the
operation point. [0378] Otherwise (NalHrdBpPresentFlag and VclHrdBpPresentFlag are both equal to 0),
no AU in the CVS shall be associated with a BP SEI message. [0379] NOTE—For some applications,
frequent presence of BP SEI messages may be desirable (e.g., for random access at an IRAP picture or a
non-IRAP picture or for bitstream splicing). [0380] bp_nal_hrd_params_present_flag equal to 1 specifies
that a list of syntax element pairs nal_initial_cpb_removal_delay[i][j] and
nal_initial_cpb_removal_offset[i][j] are present in the BP SEI message. bp_nal_hrd_params_present_flag
equal to 0 specifies that no syntax element pairs nal_initial_cpb_removal_delay[i][j] and
nal_initial_cpb_removal_offset[i][j] are present in the BP SEI message, [0381] The value of
bp_nal_hrd_params_present_flag shall be equal to general_nal_hrd_params_present_flag. [0382]
bp vcl hrd params present flag equal to 1 specifies that a list of syntax element pairs
vel_initial_cpb_removal_delay[i][j] and vel_initial_cpb_removal_offset[i][j] are present in the BP SEI
message. bp_vcl_hrd_params_present_flag equal to 0 specifies that no syntax element pairs
vcl initial cpb removal delay[i][j] and vel initial cpb removal offset[i][j] are present in the BP SEI
message. [0383] The value of bp_vcl_hrd_params_present_flag shall be equal to
general_vcl_hrd_params_present_flag. [0384] bp_vcl_hrd_params_present_flag and
bp_nal_hrd_params_present_flag in a BP SEI message shall not be both equal to 0. [0385]
initial_cpb_removal_delay_length_minus1 plus 1 specifies the length, in bits, of the syntax elements
nal_initial_cpb_removal_delay[i][j], nal_initial_cpb_removal_offset[i][j],
```

```
vcl_initial_cpb_removal_delay[i][j], and vcl_initial_cpb_removal_offset[i][j] of the BP SEI messages in
the current buffering period. When not present, the value of initial_cpb_removal_delay_length_minus1 is
inferred to be equal to 23. [0386] cpb_removal_delay_length_minus1 plus 1 specifies the length, in bits,
of the syntax elements cpb removal delay delta minus1 and cpb removal delay delta[i] in the BP SEI
message and the syntax element cpb_removal_delay_minus1[i] in the PT SEI messages in the current
buffering period. When not present, the value of cpb_removal_delay_length_minus1 is inferred to be
equal to 23. [0387] dpb output delay length minus1 plus 1 specifies the length, in bits, of the syntax
element dpb output delay in the PT SEI messages in the current buffering period. When not present, the
value of dpb_output_delay_length_minus1 is inferred to be equal to 23. [0388]
alt cpb params present flag equal to 1 specifies the presence of the syntax element
use alt cpb params flag in the BP SEI message and the presence of the alternative timing information in
the PT SEI messages in the current buffering period. When not present, the value of
bp_alt_cpb_params_present_flag is inferred to be equal to 0. When the associated picture is neither a nor
CRA picture an IDR picture, the value of bp alt cpb params present flag shall be equal to 0. [0389]
bp_decoding_unit_hrd_params_present_flag equal to 1 specifies that DU level HRD parameters are
present and the HRD may operate at AU level or DU level. bp_decoding_unit_hrd_params_present_flag
equal to 0 specifies that DU level HRD parameters are not present and the HRD operates at AU level.
When bp_decoding_unit_hrd_params_present_flag is not present, its value is inferred to be equal to 0.
The value of bp_decoding_unit_hrd_params_present_flag shall be equal to
general decoding unit hrd params present flag. [0390]
du_cpb_removal_delay_increment_length_minus1 plus 1 specifies the length, in bits, of the
du_cpb_removal_delay_increment_minus1[ ][ ] and
du_common_cpb_removal_delay_increment_minus1[] syntax elements of the PT SEI messages in the
current buffering period and the du_spt_cpb_removal_delay_increment[] syntax element in the DU
information SEI messages in the current buffering period. When not present, the value of
du cpb removal delay increment length minus1 is inferred to be equal to 23. [0391]
dpb_output_delay_du_length_minus1 plus 1 specifies the length, in bits, of the pic_dpb_output_du_delay
syntax element in the PT SEI messages in the current buffering period and the
pic spt dpb output du delay syntax element in the DU information SEI messages in the current
buffering period. When not present, the value of dpb output delay du length minus1 is inferred to be
equal to 23. [0392] decoding unit cpb_params_in_pic_timing_sei_flag equal to 1 specifies that DU level
CPB removal delay parameters are present in PT SEI messages and no DU information SEI message is
available (in the CVS or provided through external means not specified in this Specification).
decoding_unit_cpb_params_in_pic_timing_sei_flag equal to 0 specifies that DU level CPB removal delay
parameters are present in DU information SEI messages and PT SEI messages do not include DU level
CPB removal delay parameters. When the decoding unit cpb params in pic timing sei flag syntax
element is not present, it is inferred to be equal to 0. [0393] concatenation_flag indicates, when the current
picture is not the first picture in the bitstream in decoding order, whether the nominal CPB removal time
of the current picture is determined relative to the nominal CPB removal time of the preceding picture
with a BP SEI message or relative to the nominal CPB removal time of the picture
prevNonDiscardablePic. [0394] additional_concatenation_info_present_flag equal to 1 specifies that the
syntax element max_initial_removal_delay_for_concatination is present in the BP SEI message and the
syntax element delay_for_concatenation_ensured_flag is present in the PT SEI messages.
additional_concatenation_info_present_flag equal to 0 specifies that the syntax element
max initial removal delay for concatination is not present in the BP SEI message and the syntax
element delay_for_concatenation_ensured_flag is not present in the PT SEI messages. [0395]
max_initial_removal_delay_for_concatination may be used together with
delay for concatenation ensured flag in a PT SEI message to identify whether the nominal removal time
from the CPB of the first AU of a following BP computed with cpb removal delay delta minus1 applies.
The length of max_initial_removal_delay_for_concatination is
initial_cpb_removal_delay_length_minus1+1 bits. [0396] cpb_removal_delay_delta_minus1 plus 1, when
the current picture is not the first picture in the bitstream in decoding order, specifies a CPB removal
delay increment value relative to the nominal CPB removal time of the picture prevNonDiscardablePic.
```

```
The length of this syntax element is cpb_removal_delay_length_minus1+1 bits. [0397] When the current
picture contains a BP SEI message and concatenation_flag is equal to 0 and the current picture is not the
first picture in the bitstream in decoding order, it is a requirement of bitstream conformance that the
following constraint applies: [0398] If the picture prevNonDiscardablePic is not associated with a BP SEI
message, the cpb_removal_delay_minus1 of the current picture shall be equal to the
cpb_removal_delay_minus1 of prevNonDiscardablePic plus cpb_removal_delay_delta_minus1+1. [0399]
Otherwise, cpb_removal_delay_minus1 shall be equal to cpb_removal_delay_delta_minus1. [0400]
NOTE—When the current picture contains a BP SEI message and concatenation flag is equal to 1, the
cpb_removal_delay_minus1 for the current picture is not used. The above-specified constraint can, under
some circumstances, make it possible to splice bitstreams (that use suitably-designed referencing
structures) by simply changing the value of concatenation flag from 0 to 1 in the BP SEI message for an
IRAP or GDR picture at the splicing point. When concatenation_flag is equal to 0, the above-specified
constraint enables the decoder to check whether the constraint is satisfied as a way to detect the loss of the
picture prevNonDiscardablePic. [0401] cpb removal delay deltas present flag equal to 1 specifies that
the BP SEI message contains CPB removal delay deltas, cpb_removal_delay_deltas_present_flag equal to
0 specifies that no CPB removal delay deltas are present in the BP SEI message. [0402]
num cpb removal delay deltas minus1 plus 1 specifies the number of syntax elements
cpb_removal_delay_delta[i] in the BP SEI message. The value of num_cpb_removal_offsets_minus1 shall
be in the range of 0 to 15, inclusive. [0403] cpb_removal_delay_delta[i] specifies the i-th CPB removal
delay delta. The length of this syntax element is cpb_removal_delay_length_minus1+1 bits. [0404]
bp_max_sublayers_minus1 plus 1 specifies the maximum number of temporal sublayers for which CPB
removal delay and CPB removal offset are indicated in the BP SEI message. The value of
bp max sublayers minus1 shall be in the range of 0 to vps max sublayers minus1, inclusive. [0405]
bp_cpb_cnt_minus1 plus 1 specifies the number of syntax element pairs nal_initial_cpb_removal_delay[i]
[j] and nal_initial_cpb_removal_offset[i][j] of the i-th temporal sublayer when
bp nal hrd params present flag is equal to 1, and the number of syntax element pairs
vcl_initial_cpb_removal_delay[i][j] and vcl_initial_cpb_removal_offset[i][j] of the i-th temporal sublayer
when bp_vcl_hrd_params_present_flag is equal to 1. The value of bp_cpb_cnt_minus1 shall be in the
range of 0 to 31, inclusive. [0406] The value of bp_cpb_cnt_minus1 shall be equal to the value of
hrd cpb ent minus1. [0407] sublayer initial cpb removal delay present flag equal to 1 specifies that
initial CPB removal delay related syntax elements are present for temporal sublayer representation(s) in
the range of 0 to bp_max_sublayers_minus1, inclusive. sublayer_initial_cpb_removal_delay_present_flag
equal to 0 specifies that initial CPB removal delay related syntax elements are present for the
bp_max_sublayers_minus1-th temporal sublayer representation. [0408] nal_initial_cpb_removal_delay[i]
[j] and nal_initial_alt_cpb_removal_delay[i][j] specify the j-th default and alternative initial CPB removal
delay for the NAL HRD in units of a 90 kHz clock of the i-th temporal sublayer. The length of
nal_initial_cpb_removal_delay[i][j] and nal_initial_alt_cpb_removal_delay[i][j] is
initial_cpb_removal_delay_length_minus1+1 bits. The value of nal_initial_cpb_removal_delay[i][j] and
nal initial alt cpb removal delay[i][j] shall not be equal to 0 and shall be less than or equal to 90000*
(CpbSize[i][j]+BitRate[i][j]), the time-equivalent of the CPB size in 90 kHz clock units. When not
present, the values of nal_initial_cpb_removal_delay[i][j] and nal_initial_alt_cpb_removal_delay[i][j] are
inferred to be equal to 90000*(CpbSize[i][j]+BitRate[i][j]). [0409] nal_initial_cpb_removal_offset[i][j]
and nal_initial_alt_cpb_removal_offset[i][j] specify the j-th default and alternative initial CPB removal
offset of the i-th temporal sublayer for the NAL HRD in units of a 90 kHz clock. The length of
nal initial cpb removal offset[i][j] and nal initial alt cpb removal offset[i][j] is
initial_cpb_removal_delay_length_minus1+1 bits. When not present, the values of
nal_initial_cpb_removal_offset[i][j] and nal_initial_alt_cpb_removal_offset[i][j] are inferred to be equal
to 0. [0410] Over the entire CVS, for each value pair of i and j, the sum of
nal initial cpb removal delay[i][j] and nal initial cpb removal offset[i][j] shall be constant, and the
sum of nal_initial_alt_cpb_removal_delay[i][j] and nal_initial_alt_cpb_removal_offset[i][j] shall be
constant. [0411] vcl_initial_cpb_removal_delay[i][j] and vel_initial_alt_cpb_removal_delay[i][j] specify
the j-th default and alternative initial CPB removal delay of the i-th temporal sublayer for the VCL HRD
in units of a 90 kHz clock. The length of vel_initial_cpb_removal_delay[i][j] and
```

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vcl_initial_alt_cpb_removal_delay[i][j] is initial_cpb_removal_delay_length_minus1+1 bits. The value of
vel_initial_cpb_removal_delay[i][j] and vel_initial_alt_cpb_removal_delay[i][j] shall not equal to 0 and
be shall less than or equal to 90000*(CpbSize[i][j]+BitRate[i][j]), the time-equivalent of the CPB size in
90 kHz clock units. When not present, the values of vel initial cpb removal delay[i][j] and
vcl_initial_alt_cpb_removal_delay[i][j] are inferred to be equal to 90000*(CpbSize[i][j]+BitRate[i][j]).
[0412] vcl_initial_cpb_removal_offset[i][j] and vcl_initial_alt_cpb_removal_offset[i][j] specify the j-th
default and alternative initial CPB removal offset of the i-th temporal sublayer for the VCL HRD in units
of a 90 kHz clock. The length of vel initial cpb removal offset[i] and
vcl_initial_alt_cpb_removal_offset[i][j] is initial_cpb_removal_delay_length_minus1+1 bits. When not
present, the values of vcl_initial_cpb_removal_offset[i][j] and vcl_initial_alt_cpb_removal_offset[i][j] are
inferred to be equal to 0. [0413] Over the entire CVS, for value pair of i and j of each the sum of
vcl_initial_cpb_removal_delay[i][j] and vel_initial_cpb_removal_offset[i][j] shall be constant, and the
sum of vcl_initial_alt_epb_removal_delay[i][j] and vcl_initial_alt_cpb_removal_offset[i][j] shall be
constant. [0414] use alt cpb params flag may be used to derive the value of UseAltCpbParamsFlag.
When use_alt_cpb_params_flag is not present, it is inferred to be equal to 0. [0415] When one or more of
the following conditions apply, UseAltCpbParamsFlag is set to 1: [0416] use_alt_cpb_params_flag is
equal to 1. [0417] When some external means not specified in this Specification is available to set
UseAltCpbParamsFlag and the value of UseAltCpbParamsFlag is set equal to 1 by the external means.
[0418] As described above, JVET-P2001 enables SEI messages to be signaled which assist in processes
related to decoding, display or other purposes. Further, a type of SEI message for VCL HRD operations
includes picture timing SEI messages. Table 9 illustrates the pic_timing() syntax structure provided in
JVET-P2001.
TABLE-US-00012 TABLE 9 Descriptor pic_timing( payloadSize ) {
                                                                        cpb removal delay minus1[
bp_max_sublayers_minus1 ] u(v)
                                      if( alt_cpb_params_present_flag ) {
                                             if( cpb_alt_timing_info_present_flag ) {
                                                                                                 for(i
cpb_alt_timing_info_present_flag u(1)
= 0; i < bp cpb cnt minus1 + 1; i++) {
                                                        cpb_alt_initial_cpb_removal_delay_delta[ i ]
                     cpb_alt_initial_cpb_removal_offset_delta[ i ] u(v)
u(v)
cpb_delay_offset u(v)
                                    dpb_delay_offset u(v)
                                                                               for( i = TemporalId; i <
                                          pt_sublayer_delays_present_flag[ i ] u(1)
bp_max_sublayers_minus1; i++ ) {
                                                   if( cpb_removal_delay_deltas_present_flag )
pt sublayer delays present flag[i]) {
                 cpb_removal_delay_delta_enabled_flag[i] u(1)
                                                             cpb_removal_delay_delta_idx[ i ] u(v)
cpb_removal_delay_delta_enabled_flag[ i ] )
                                 cpb_removal_delay_minus1[ i ] u(v)
dpb_output_delay u(v)
                           if( bp_decoding_unit_hrd_params_present_flag )
pic_dpb_output_du_delay u(v)
                                  if(bp_decoding_unit_hrd_params_present_flag &&
decoding unit cpb params in pic timing sei flag) {
                                                                num decoding units minus1 ue(v)
         du_common_cpb_removal_delay_flag u(1)
                                                             if( du_common_cpb_removal_delay_flag
                for( i = TemporalId; i <= bp_max_sublayers_minus1; i++)
                                                                                                if(
pt_sublayer_delays_present_flag[ i ] )
du_common_cpb_removal_delay_increment_minus1[ i ] u(v)
                                                                      for( i= 0; i <=
num_decoding_units_minus1; i++ ) {
                                                   num_nalus_in_du_minus1[ i ] ue(v)
if(!du_common_cpb_removal_delay_flag && i < num_decoding_units_minus1)
for( j = TemporalId; j <= bp_max_sublayers_minus1; j++ )</pre>
                                                                                   if(
pt_sublayer_delays_present_flag[ j ] )
du_cpb_removal_delay_increment_minus1[ i ][ j ] u(v)
                                                                               if(
additional_concatenation_info_present_flag )
                                                      delay_for_concatenation_ensured_flag u(1)
                                                                                                   }
[0419] With respect to Table 9, JVET-P2001 provides the following semantics: [0420] The PT SEI
message provides CPB removal delay and DPB output delay information for the AU associated with the
SEI message. [0421] If bp_nal_hrd_params_present_flag or bp_vel_hrd_params_present_flag of the BP
SEI message applicable for the current AU is equal to 1, the variable CpbDpbDelaysPresentFlag is set
equal to 1. Otherwise, CpbDpbDelaysPresentFlag is set equal to 0. [0422] The presence of PT SEI
messages is specified as follows: [0423] If CpbDpbDelaysPresentFlag is equal to 1, a PT SEI message
shall be associated with the current AU. [0424] Otherwise (CpbDpbDelaysPresentFlag is equal to 0), there
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shall not be a PT SEI message associated with the current AU. [0425] The TemporalId in the PT SEI
message syntax is the TemporalId of the SEI NAL unit containing the PT SEI message. [0426]
cpb_removal_delay_minus1[i] plus 1 is used to calculate the number of clock ticks between the nominal
CPB removal times of the AU associated with the PT SEI message and the preceding AU in decoding
order that contains a BP SEI message when Htid is equal to i. This value is also used to calculate an
earliest possible time of arrival of AU data into the CPB for the HSS. The length of
cpb_removal_delay_minus1[i] is cpb_removal_delay_length_minus1+1 bits. [0427]
cpb alt timing info present flag equal to 1 specifies the presence of the syntax elements
cpb_alt_initial_cpb_removal_delay_delta[i], cpb_delay_offset, and dpb_delay_offset. When the
associated picture is a RASL picture, the value of cpb_alt_timing_info_present_flag shall be equal to 0.
[0428] NOTE—The value of cpb alt timing info present flag might be equal to 1 for more than one AU
following an IRAP picture in decoding order. However, the alternative timing is only applied to the first
AU that has cpb_alt_timing_info_present_flag equal to 1 and follows the IRAP picture in decoding order.
[0429] cpb alt initial cpb removal delay delta[i] specifies the alternative initial CPB removal delay
delta for the i-th CPB. The length of cpb_alt_initial_cpb_removal_delay_delta[i] is
initial_cpb_removal_delay_length_minus1+1 bits. [0430] cpb_alt_initial_cpb_removal_offset_delta[i]
specifies the alternative initial CPB removal offset delta for the i-th CPB. The length of
cpb_alt_initial_cpb_removal_offset_delta[i] is initial_cpb_removal_delay_length_minus1+1 bits. [0431]
cpb_delay_offset specifies an offset to be used in the derivation of the nominal CPB removal times of the
AU associated with the PT SEI message and of the AUs following in decoding order, when the AU
associated with the PT SEI message directly follows in decoding order the AU associated with the BP SEI
message. The length of cpb_delay_offset is au_cpb_removal_delay_length_minus1+1 bits. When not
present, the value of cpb delay offset is inferred to be equal to 0. [0432] dpb delay offset specifies an
offset to be used in the derivation of the DPB output times of the IRAP AU associated with the BP SEI
message when the AU associated with the PT SEI message directly follows in decoding order the IRAP
AU associated with the BP SEI message. The length of dpb delay offset is
dpb_output_delay_length_minus1+1 bits. When not present, the value of dpb_delay_offset is inferred to
be equal to 0. [0433] The variable BpResetFlag of the current picture is derived as follows: [0434] 1 If the
current picture is associated with a BP SEI message, BpResetFlag is set equal to 1. [0435] Otherwise,
BpResetFlag is set equal to 0. [0436] pt sublayer delays present flag[i] equal to 1 specifies that
cpb_removal_delay_delta_idx[i] cpb_removal_delay_minus1[i], and that
du_common_cpb_removal_delay_increment_minus1[i] or du_cpb_removal_delay_increment_minus1[ ][
are present for the sublayer with TemporalId equal to i. sublayer delays present flag[i] equal to 0
specifies that neither cpb_removal_delay_delta_idx[i] nor cpb_removal_delay_minus1[i] and that neither
du_common_cpb_removal_delay_increment_minus1[i] nor du_cpb_removal_delay_increment_minus1[]
[ ] are present for the sublayer with TemporalId equal to i. The value of
pt_sublayer_delays_present_flag[bp_max_sublayers_minus1] is inferred to be equal to 1. When not
present, the value of pt_sublayer_delays_present_flag[i] for any i in the range of 0 to
bp max sublayers minus1-1, inclusive, is inferred to be equal to 0. [0437]
cpb_removal_delay_delta_enabled_flag[i] equal to 1 specifies that cpb_removal_delay_delta_idx[i] is
present in the PT SEI message. cpb_removal_delay_delta_enabled_flag[i] equal to 0 specifies that
cpb_removal_delay_delta_idx[i] is not present in the PT SEI message. When not present, the value of
cpb_removal_delay_delta_enabled_flag[i] is inferred to be equal to 0. [0438]
cpb_removal_delay_delta_idx[i] specifies the index of the CPB removal delta that applies to Htid equal to
i in the list of cpb_removal_delay_delta[j] for j ranging from 0 to
num_cpb_removal_delay_deltas_minus1, inclusive. The length of cpb_removal_delay_delta_idx[i] is
Ceil(Log 2(num_cpb_removal_delay_deltas_minus1+1)) bits. [0439] The variables
CpbRemovalDelayMsb[i] and CpbRemovalDelayVal[i] of the current picture are derived as follows:
[0440] If the current AU is the AU that initializes the HRD, CpbRemovalDelayMsb[i] and
CpbRemovalDelayVal[i] are both set equal to 0, and the value of cpbRemovalDelayValTmp[i] is set equal
to cpb_removal_delay_minus1[i]+1. [0441] Otherwise, let the picture prevNonDiscardablePic be the
previous picture in decoding order that has TemporalId equal to 0 that is not a RASL or RADL, let
prevCpbRemovalDelayMinus1[i], prevCpbRemovalDelayMsb[i], and prevBpResetFlag be set equal to
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the values of cpbRemovalDelayValTmp[i]-1, CpbRemovalDelayMsb[i], and BpResetFlag, respectively,
for the picture prevNonDiscardablePic, and the following applies: [0442] CpbRemovalDelayMsb[i] is
derived as follows:
TABLE-US-00013 cpbRemovalDelayValTmp[i] = cpb removal delay delta enabled flag[i]?
cpb_removal_delay_minus1[ sps_max_sublayers_minus1 ] + 1 +
                                                                 cpb_removal_delay_delta[
cpb_removal_delay_delta_idx[i]]: cpb_removal_delay_minusl[i] + 1 if( prevBpResetFlag )
CpbRemovalDelayMsb[ i ] = 0 else if( cpbRemovalDelayValTmp[ i ] < prevCpbRemovalDelayMinus1[ i
     CpbRemovalDelayMsb[ i ] = prevCpbRemovalDelayMsb[ i ] + 2.sup.cpb.sup.—.sup.removal.sup.
—.sup.delay.sup.—.sup.length.sup.—.sup.minus1 + 1 else
                                                          CpbRemovalDelayMsb[ i ] =
prevCpbRemovalDelayMsb[ i ] [0443] CpbRemovalDelayVal is derived as follows:
[00018]CpbRemovalDelayVal[i] = CpbRemovalDelayMsb[i] + cpbRemovalDelayValTmp[i] [0444] The
value of CpbRemovalDelayVal[i] shall be in the range of 1 to 2.sup.32, inclusive. [0445] The variable
picDpbOutputDelta[i] is derived as follows: [0446] If pt_sublayer_delays_present_flag[i] is equal to 0,
picDpbOutputDelta[i] is set equal to 0. [0447] Otherwise (pt sublayer delays present flag[i] is equal to
1), picDpbOutputDelta[i] is set equal to CpbRemovalDelayVal[i]-
(cpb_removal_delay_minus1[sps_max_sublayers_minus1]+1). [0448] dpb_output_delay is used to
compute the DPB output time of the picture. It specifies how many clock ticks to wait after removal of an
AU from the CPB before the decoded picture is output from the DPB. [0449] NOTE—A picture is not
removed from the DPB at its output time when it is still marked as "used for short-term reference" or
"used for long-term reference". [0450] The length of dpb output delay is
dpb_output_delay_length_minus1+1 bits. When max_dec_pic_buffering_minus1[Htid] is equal to 0, the
value of pic_dpb_output_delay shall be equal to 0. [0451] The output time derived from the
dpb output delay of any picture that is output from an output timing conforming decoder shall precede
the output time derived from the dpb_output_delay of all pictures in any subsequent CVS in decoding
order. [0452] The picture output order established by the values of this syntax element shall be the same
order as established by the values of PicOrderCntVal. [0453] For pictures that are not output by the
"bumping" process because they precede, in decoding order, a CLVSS picture that has
no_output_of_prior_pics_flag equal to 1 or inferred to be equal to 1, the output times derived from
dpb_output_delay shall be increasing with increasing value of PicOrderCntVal relative to all pictures
within the same CVS. [0454] pic dpb output du delay is used to compute the DPB output time of the
picture when DecodingUnitHrdFlag is equal to 1. It specifies how many sub clock ticks to wait after
removal of the last DU in an AU from the CPB before the decoded picture is output from the DPB. The
length of the syntax element pic dpb output du delay is given in bits by
dpb_output_delay_du_length_minus1+1. [0455] The output time derived from the
pic_dpb_output_du_delay of any picture that is output from an output timing conforming decoder shall
precede the output time derived from the pic dpb output du delay of all pictures in any subsequent CVS
in decoding order. [0456] The picture output order established by the values of this syntax element shall
be the same order as established by the values of PicOrderCntVal. [0457] For pictures that are not output
by the "bumping" process because they precede, in decoding order, a CLVSS picture that has
no_output_of_prior_pics_flag equal to 1 or inferred to be equal to 1, the output times derived from
pic_dpb_output_du_delay shall be increasing with increasing value of PicOrderCntVal relative to all
pictures within the same CVS. [0458] For any two pictures in the CVS, the difference between the output
times of the two pictures when DecodingUnitHrdFlag is equal to 1 shall be identical to the same
difference when DecodingUnitHrdFlag is equal to 0. [0459] num_decoding_units_minus1 plus 1 specifies
the number of DUs in the AU the PT SEI message is associated with. The value of
num_decoding_units_minus1 shall be in the range of 0 to PicSizeInCtbsY-1, inclusive. [0460]
du_common_cpb_removal_delay_flag equal to 1 specifies that the syntax elements
du_common_cpb_removal_delay_increment_minus1[i] are present.
du common cpb removal delay flag equal to 0 specifies that the syntax elements
du_common_cpb_removal_delay_increment_minus1[i] are not present. [0461]
du_common_cpb_removal_delay_increment_minus1[i] plus 1 specifies the duration, in units of clock
sub-ticks, between the nominal CPB removal times of any two consecutive DUs in decoding order in the
AU associated with the PT SEI message when Htid is equal to i. This value is also used to calculate an
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earliest possible time of arrival of DU data into the CPB for the HSS, as specified. The length of this
syntax element is du_cpb_removal_delay_increment_length_minus1+1 bits. [0462] When
du_common_cpb_removal_delay_increment_minus1[i] is not present for any value of i less than
bp max sublayers minus1, its value is inferred to be equal to
du_common_cpb_removal_delay_increment_minus1[bp_max_sublayers_minus1]. [0463]
num_nalus_in_du_minus1[i] plus 1 specifies the number of NAL units in the i-th DU of the AU the PT
SEI message is associated with. The value of num nalus in du minus1[i] shall be in the range of 0 to
PicSizeInCtbsY-1, inclusive. [0464] The first DU of the AU consists of the first
num_nalus_in_du_minus1[0]+1 consecutive NAL units in decoding order in the AU. The i-th (with i
greater than 0) DU of the AU consists of the num nalus in du minus1[i]+1 consecutive NAL units
immediately following the last NAL unit in the previous DU of the AU, in decoding order. There shall be
at least one VCL NAL unit in each DU. All non-VCL NAL units associated with a VCL NAL unit shall be
included in the same DU as the VCL NAL unit. [0465] du cpb removal delay increment minus1[i][j]
plus 1 specifies the duration, in units of clock sub-ticks, between the nominal CPB removal times of the
(i+1)-th DU and the i-th DU, in decoding order, in the AU associated with the PT SEI message when Htid
is equal to j. This value is also used to calculate an earliest possible time of arrival of DU data into the
CPB for the HSS, The length of this syntax element is as specified. [0466]
du_cpb_removal_delay_increment_length_minus1+1 bits. [0467] When
du_cpb_removal_delay_increment_minus1[i][j] is not present for any value of j less than
bp max sublayers minus1, its value is inferred to be equal to
du_cpb_removal_delay_increment_minus1[i][bp_max_sublayers_minus1]. [0468]
delay_for_concatenation_ensured_flag equal to 1 specifies that the difference between the final arrival
time and the CPB removal time of the AU associated with the PT SEI message is such that when followed
by an AU with a BP SEI message with concatenation_flag equal to 1 and InitCpbRemovalDelay[][] less
than or equal to the value of max_initial_removal_delay_for_concatination, the nominal removal time of
the following AU from the CPB computed with cpb removal delay delta minus1 applies.
delay_for_concatenation_ensured_flag equal to 0 specifies that the difference between the final arrival
time and the CPB removal time of the AU associated with the PT SEI message may or may not exceed the
value of max_val_initial_removal_delay_for_splicing. [0469] As described above, JVET-P2001 enables
SEI messages to be signaled which assist in processes related to decoding, display or other purposes.
Further, a type of SEI message for VCL HRD operations includes decoding unit information SEI
messages. Table 10 illustrates the decoding_unit_info() syntax structure provided in JVET-P2001.
TABLE-US-00014 TABLE 10 Descriptor decoding unit info(payloadSize) {
                                                                              decoding unit idx
       if(!decoding_unit_cpb_params_in_pic_timing_sei_flag)
                                                                    for( i = TemporalId; i
bp max sublayers minus1; i++ ) {
                                         dui sublayer delays present flag[i]u(1)
                                                                                          if(
                                               du spt cpb removal delay increment[i]u(v)
dui sublayer delays present flag[i])
                                                                                                   }
  dpb_output_du_delay_present_flag u(1) if( dpb_output_du_delay_present_flag )
pic spt dpb output du delay u(v) }
[0470] With respect to Table 10, JVET-P2001 provides the following semantics: [0471] The DU
information SEI message provides CPB removal delay information for the DU associated with the SEI
message. [0472] The following applies for the DU information SEI message syntax and semantics: [0473]
The syntax elements bp_decoding_unit_hrd_params_present_flag,
decoding_unit_cpb_params_in_pic_timing_sei_flag and dpb_output_delay_du_length_minus1 are found
in the BP SEI message that is applicable to at least one of the operation points to which the DU
information SEI message applies. [0474] The bitstream (or a part thereof) refers to the bitstream subset (or
a part thereof) associated with any of the operation points to which the DU information SEI message
applies. [0475] The presence of DU information SEI messages for an operation point is specified as
follows: [0476] If CpbDpbDelaysPresentFlag is equal to 1, bp decoding unit hrd params present flag
is equal to 1 and decoding unit cpb params in pic timing sei flag is equal to 0, one or more DU
information SEI messages applicable to the operation point shall be associated with each DU in the CVS.
[0477] Otherwise, if CpbDpbDelaysPresentFlag is equal to 1,
bp_decoding_unit_hrd_params_present_flag is equal to 1 and
decoding_unit_cpb_params_in_pic_timing_sei_flag is equal to 1, one or more DU information SEI
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messages applicable to the operation point may or may not be associated with each DU in the CVS.
[0478] Otherwise (CpbDpbDelaysPresentFlag is equal to 0 or
bp_decoding_unit_hrd_params_present_flag is equal to 0), in the CVS there shall be no DU that is
associated with a DU information SEI message applicable to the operation point. [0479] The set of NAL
units associated with a DU information SEI message consists, in decoding order, of the SEI NAL unit
containing the DU information SEI message and all subsequent NAL units in the AU up to but not
including any subsequent SEI NAL unit containing a DU information SEI message with a different value
of decoding unit idx. Each DU shall include at least one VCL NAL unit. All non-VCL NAL units
associated with a VCL NAL unit shall be included in the DU containing the VCL NAL unit. [0480] The
TemporalId in the DU information SEI message syntax is the TemporalId of the SEI NAL unit containing
the DU information SEI message. [0481] decoding unit idx specifies the index, starting from 0, to the list
of DUs in the current AU, of the DU associated with the DU information SEI message. The value of
decoding_unit_idx shall be in the range of 0 to PicSizeInCtbsY-1, inclusive. [0482] A DU identified by a
particular value of duIdx includes and only includes all NAL units associated with all DU information SEI
messages that have decoding unit idx equal to duIdx. Such a DU is also referred to as associated with the
DU information SEI messages having decoding_unit_idx equal to duIdx. [0483] For any two DUs duA
and duB in one AU with decoding unit idx equal to duIdxA and duIdxB, respectively, where duIdxA is
less than duIdxB, duA shall precede duB in decoding order. [0484] A NAL unit of one DU shall not be
present, in decoding order, between any two NAL units of another DU. [0485]
dui sublayer delays present flag[i] equal to 1 specifies that du spt cpb removal delay increment[i] is
present for the sublayer with TemporalId equal to i. dui_sublayer_delays_present_flag[i] equal to 0
specifies that du_spt_cpb_removal_delay_increment [i] is not present for the sublayer with TemporalId
equal to i. When not present, the value of dui sublayer delays present flag[i] is inferred to be equal to 0.
[0486] du_spt_cpb_removal_delay_increment [i] specifies the duration, in units of clock sub-ticks,
between the nominal CPB times of the last DU in decoding order in the current AU and the DU associated
with the DU information SEI message when Htid is equal to i. This value is also used to calculate an
earliest possible time of arrival of DU data into the CPB for the HSS, as specified in Annex C. The length
of this syntax element is du_cpb_removal_delay_increment_length_minus1+1. When the DU associated
with the DU information SEI message is the last DU in the current AU, the value of
du spt cpb removal delay increment[i] shall be equal to 0. When
du_spt_cpb_removal_delay_increment[i] is not present for any value of i less than
bp_max_sublayers_minus1, its value is inferred to be equal to
du_spt_cpb_removal_delay_increment[bp_max_sublayers_minus1]. [0487]
dpb_output_du_delay_present_flag equal to 1 specifies the presence of the pic_spt_dpb_output_du_delay
syntax element in the DU information SEI message. dpb_output_du_delay_present_flag equal to 0
specifies the absence of the pic spt dpb output du delay syntax element in the DU information SEI
message. [0488] pic_spt_dpb_output_du_delay is used to compute the DPB output time of the picture
when DecodingUnitHrdFlag is equal to 1. It specifies how many sub clock ticks to wait after removal of
the last DU in an AU from the CPB before the decoded picture is output from the DPB. When not present,
the value of pic_spt_dpb_output_du_delay is inferred to be equal to pic_dpb_output_du_delay. The length
of the syntax element pic_spt_dpb_output_du_delay is given in bits by
dpb_output_delay_du_length_minus1+1. [0489] It is a requirement of bitstream conformance that all DU
information SEI messages that are associated with the same AU, apply to the same operation point, and
have dpb_output_du_delay_present_flag equal to 1 shall have the same value
pic spt dpb output du delay. [0490] The output time derived from the pic spt dpb output du delay of
any picture that is output from an output timing conforming decoder shall precede the output time derived
from the pic_spt_dpb_output_du_delay of all pictures in any subsequent CVS in decoding order. The
picture output order established by the values of this syntax element shall be the same order as established
by the values of PicOrderCntVal. [0491] For pictures that are not output by the "bumping" process
because they precede, in decoding order, a CLVSS picture that has no_output_of_prior_pics_flag equal to
1 or inferred to be equal to 1, the output times derived from pic_spt_dpb_output_du_delay shall be
increasing with increasing value of PicOrderCntVal relative to all pictures within the same CVS. [0492]
For any two pictures in the CVS, the difference between the output times of the two pictures when
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DecodingUnitHrdFlag is equal to 1 shall be identical to the same difference when DecodingUnitHrdFlag is equal to 0.

[0493] Further, JVET-P2001, provides the following for picture output for a decoding picture buffer: [0494] The processes specified in this clause happen instantaneously at the CPB removal time of AU n, CpbRemovalTime[n]. [0495] When picture n has PictureOutputFlag equal to 1, its DPB output time DpbOutputTime[n] is derived as follows, where the variable firstPicInBufferingPeriodFlag is equal to 1 if AU n is the first AU of a BP and 0 otherwise:

$$\label{thm:condition} \begin{split} TABLE-US-00015 & \text{ if (!DecodingUnitHrdFlag ) \{ & DpbOutputTime[ \ n \ ] = CpbRemovalTime[ \ n \ ] + \\ ClockTick*(picDpbOutputDelay- & picDpbOutputDelta) & \text{ if (firstPicInBufferingPeriodFlag )} \end{split}$$

DpbOutputTime[ n ] -= ClockTick \* DpbDelayOffset } else DpbOutputTime[ n ] = AuCpbRemovalTime[ n ] + ClockSubTick \* picSptDpbOutputDuDelay [0496] where picDpbOutputDelay is the value of pic\_dpb\_output\_delay and picDpbOutputDelta is the value of picDpbOutputDelta[Htid] derived according to epb\_removal\_delay\_minus1[Htid], and cpb removal delay delta idx[Htid] in the PT SEI message associated with AU n, and cpb\_removal\_delay\_delta[cpb\_removal\_delay\_delta\_idx[Htid]] in the BP SEI message associated with AU n, and picSptDpbOutputDuDelay the of is value pic\_spt\_dpb\_output\_du\_delay, when present, in the DU information SEI messages associated with AU n, or the value of pic dpb output du delay in the PT SEI message associated with AU n when there is no DU information SEI message associated with AU n or no DU information SEI message associated with AU n has pic\_spt\_dpb\_output\_du\_delay present. [0497] NOTE—When the syntax element pic spt dpb output du delay is not present in any DU information SEI message associated with AU n, the value is inferred to be equal to pic\_dpb\_output\_du\_delay in the PT SEI message associated with AU n. [0498] The output of the current picture is specified as follows: [0499] If PictureOutputFlag is equal to 1 and DpbOutputTime[n] is equal to CpbRemovalTime[n], the current picture is output. [0500] Otherwise, if PictureOutputFlag is equal to 0, the current picture is not output, but will be stored in the DPB as specified in the clause below. [0501] Otherwise (PictureOutputFlag is equal to 1 and DpbOutputTime[n] is greater than CpbRemovalTime[n]), the current picture is output later and will be stored in the DPB (as specified below) and is output at time DpbOutputTime[n] unless indicated not to be output by NoOutputOfPriorPicsFlag equal to 1. [0502] When output, the picture is cropped, using the conformance cropping window for the picture. When picture n is a picture that is output and is not the last picture of the bitstream that is output, the value of the variable DpbOutputInterval[n] is derived as follows:

DpbOutputInterval[n]=DpbOutputTime[nextPicInOutputOrder]-DpbOutputTime[n] [0503] where nextPicInOutputOrder is the picture that follows picture n in output order and has PictureOutputFlag equal to 1. [0504] The current decoded picture is stored in the DPB in an empty picture storage buffer, the DPB fullness is incremented by one, and the current picture is marked as "used for short-term reference". [0505] NOTE—Unless more memory than required by the level limit is available for storage of decoded pictures, decoders should start storing decoded parts of the current picture into the DPB when the first slice is decoded and continue storing more decoded samples as the decoding process proceeds. [0506] The signaling of syntax elements pic\_dpb\_output\_du\_delay and pic\_spt\_dpb\_output\_du\_delay in JVET-P2001 is less than ideal. In particular, if DU level HRD parameters are present (i.e., bp\_decoding\_unit\_hrd\_params\_present\_flag is equal to 1), picture dpb output delay information (i.e. pic\_dpb\_output\_du\_delay) is always signalled in picture timing SEI message. Additionally, the picture dpb output delay information can be signalled in each of the one or more DU information SEI messages in syntax element pic spt dpb output du delay, if dpb output du delay present flag is equal to 1. If the picture dpb output delay information is signaled in DU information SEI messages, the value signalled shall be the same. It is asserted that this way of information signalling about picture DPB output delay is unnecessarily redundant and overly complicated. According to the techniques herein, simplification is proposed for signalling picture DPB output delay information. In one example, according to the techniques herein, if DU level HRD parameters are present, a flag is signalled in Buffering period SEI message (or picture timing SEI message) to control if DU level picture DPB output delay information is signalled only in picture timing SEI message or in DU information SEI message. In addition, in one example, an additional flag is signaled in picture timing SEI message (or in buffering period SEI message) to indicate that when DU level picture DPB output delay information is signalled in picture timing SEI message it may be updated in DU information SEI message. In one example, a flag is signalled in buffering period SEI message (or in picture timing SEI message) specifying if DU level picture DPB output delay information is signalled or is inferred to be same as the information for AU level.

### **Claims**

- 1. A method of decoding a picture timing message, the method comprising: receiving a buffering period message; parsing a first syntax element in the buffering period message, wherein the first syntax element specifies whether a list of syntax element pairs specifying an initial coded picture buffer removal delay and an initial coded picture buffer removal offset are present in the buffering period message; receiving the picture timing message; parsing a second syntax element in the picture timing message in a case that a value of the first syntax element is equal to one, wherein the second syntax element specifies an initial coded picture buffer removal delay delta; and parsing a third syntax element in the picture timing message in a case that the value of the first syntax element is equal to one, wherein the third syntax element specifies an initial coded picture buffer removal offset delta.
- 2. A decoder for decoding coded data, the decoder comprising: a processor; and a memory associated with the processor, wherein the processor is configured to: receive a buffering period message; parse a first syntax element in the buffering period message, wherein the first syntax element specifies whether a list of element pairs specifying an initial coded picture buffer removal delay and an initial coded picture buffer removal offset are present in the buffering period message; receive the picture timing message; parse a second syntax element in the picture timing message in a case that a value of the first syntax element is equal to one, wherein the second syntax element specifies an initial coded picture buffer removal delay delta; and parse a third syntax element in the picture timing message in a case that the value of the first syntax element is equal to one, wherein the third syntax element specifies an initial coded picture buffer removal offset delta.
- **3**. An encoder for encoding image data, the encoder comprising: a processor; and a memory associated with the processor, wherein the processor is configured to: signal a buffering period message, wherein the buffering period message includes a first syntax element, wherein the first syntax element specifies whether a list of syntax element pairs specifying an initial coded picture buffer removal delay and an initial coded picture buffer removal offset are present in the buffering period message; and signal the picture timing message, wherein the picture timing message includes: a second syntax element in a case that a value of the first syntax element is equal to one, wherein the second syntax element specifies an initial coded picture buffer removal delay delta, and a third syntax element in a case that a value of the first syntax element is equal to one, wherein the third syntax element specifies an initial coded picture buffer removal offset delta.