

# Assembly instructions

## Production data information:

- Components with a generic name (i.e. Res\_0603MD\_11k\_1%) can be chosen freely by the assembler within the stated specifications.
- Markings on diodes and LEDs always indicate the Cathode.

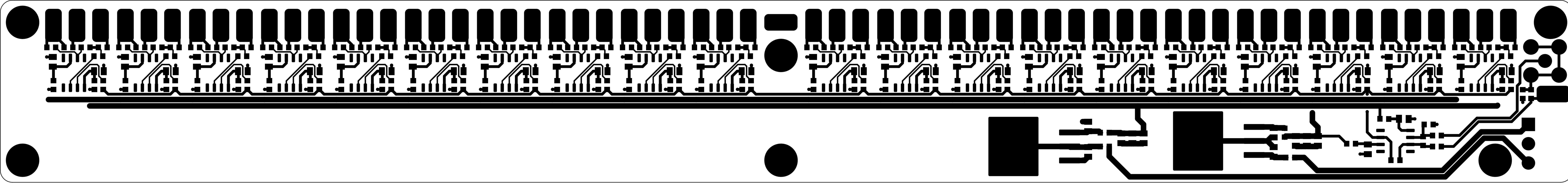
## Layer Stackup:

| Layer | Name           | Material      | Thickness | Constant | Board Layer Stack   |
|-------|----------------|---------------|-----------|----------|---|
| 1     | Top Overlay    |               |           |          |  |
| 2     | Top Solder     | Solder Resist | 0,010mm   | 3,5      |  |
| 3     | Top Layer      | Copper        | 0,018mm   |          |  |
| 4     | Dielectric1    | FR-4          | 1,550mm   | 4,2      |  |
| 5     | Bottom Layer   | Copper        | 0,018mm   |          |  |
| 6     | Bottom Solder  | Solder Resist | 0,010mm   | 3,5      |  |
| 7     | Bottom Overlay |               |           |          |  |



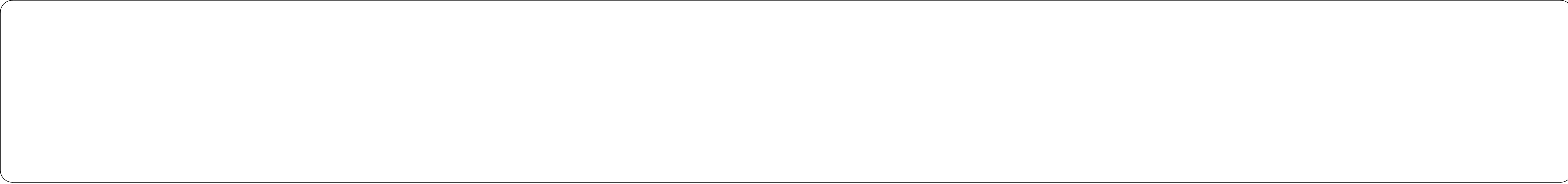
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| Tjeerd Weijers / TvR                        |
| 20Ch FET Driver board                       |
| PCB Template: Eurocircuits 6C 2Layer        |

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20Ch FET Driver board  
Vv1.1  
PCB Template: Eurocircuits 6C 2Layer





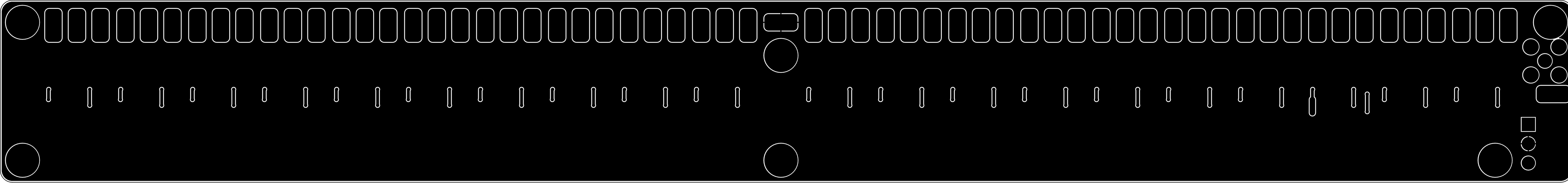


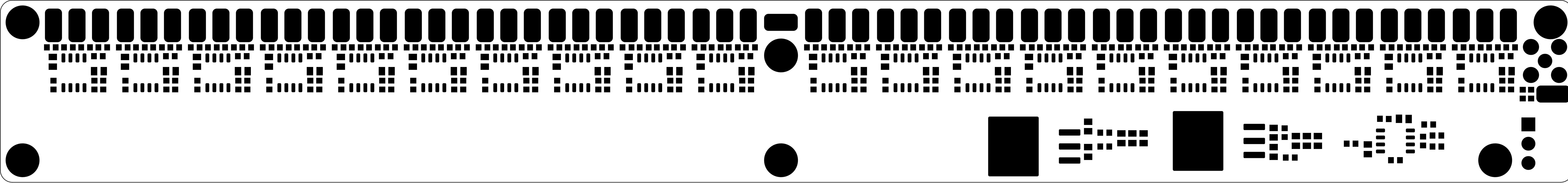


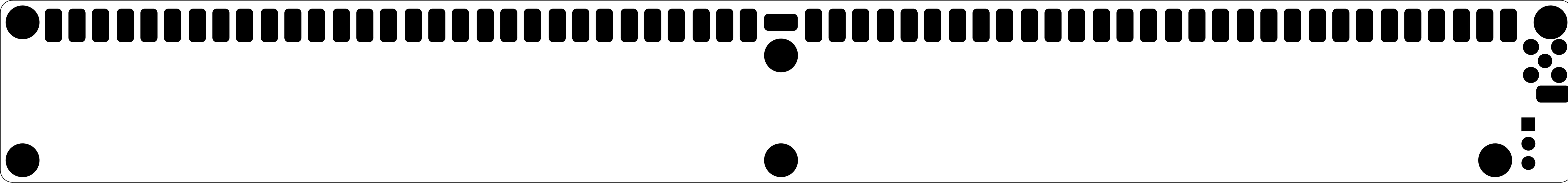


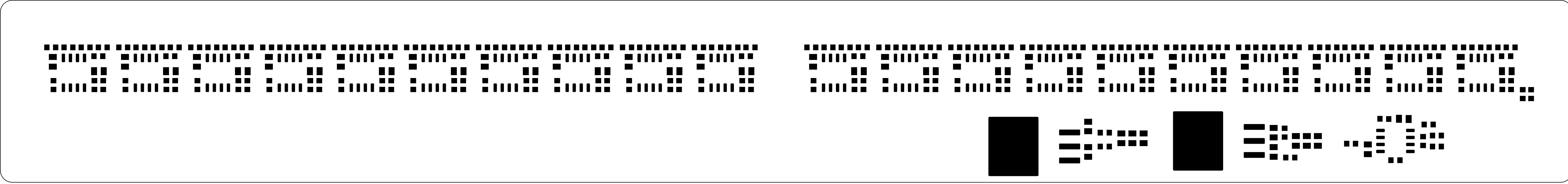




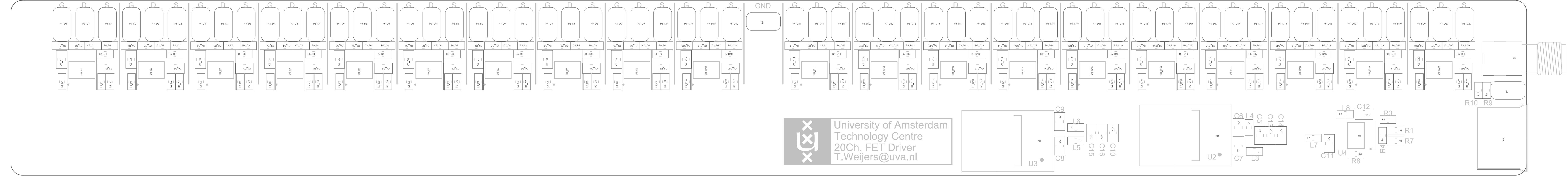




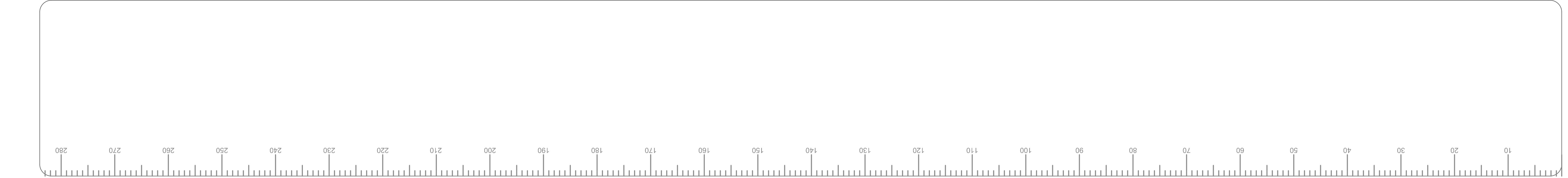




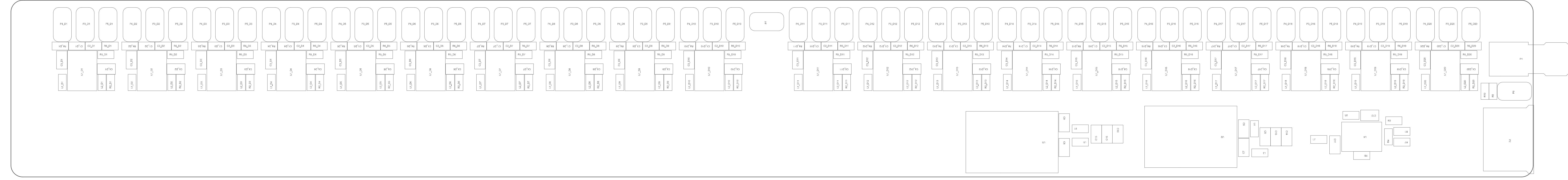




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| 20Ch FET Driver board                       | Vv1.1   |
| PCB Template: Eurocircuits 6C 2Layer        |         |



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|--|---------|---------------------|---------|--|
| PCB Tempplate: Euocicunis GC JFgaler       | AVI.J   | JGerd Meijers \ TAr | 16-1-18 | Universit of Amsterdam \ Technology Centre |
| ZOCF FET Driver posr                       |         |                     |         |  |
| JGerd Meijers \ TAr                        | 16-1-18 |                     |         |  |
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