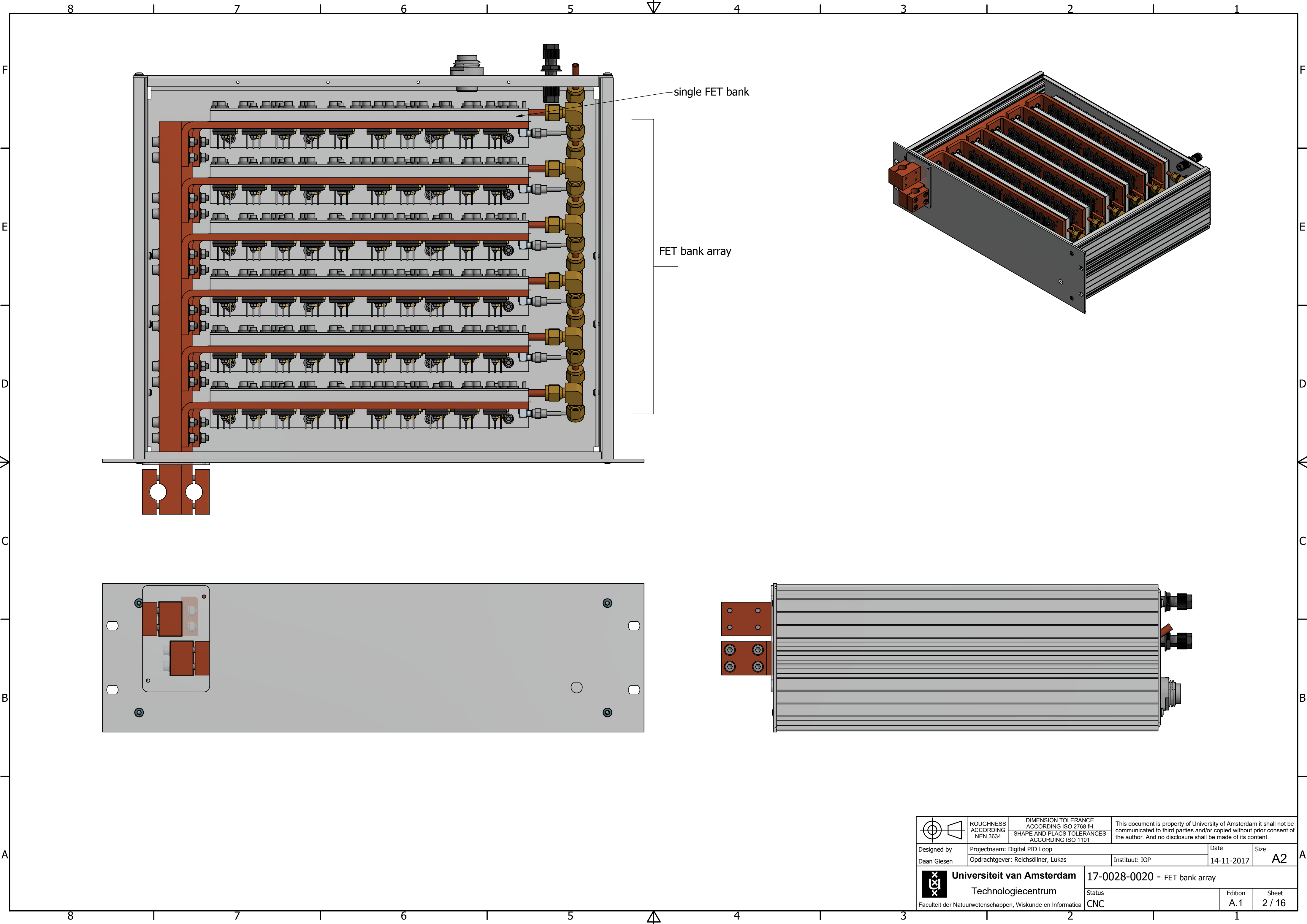
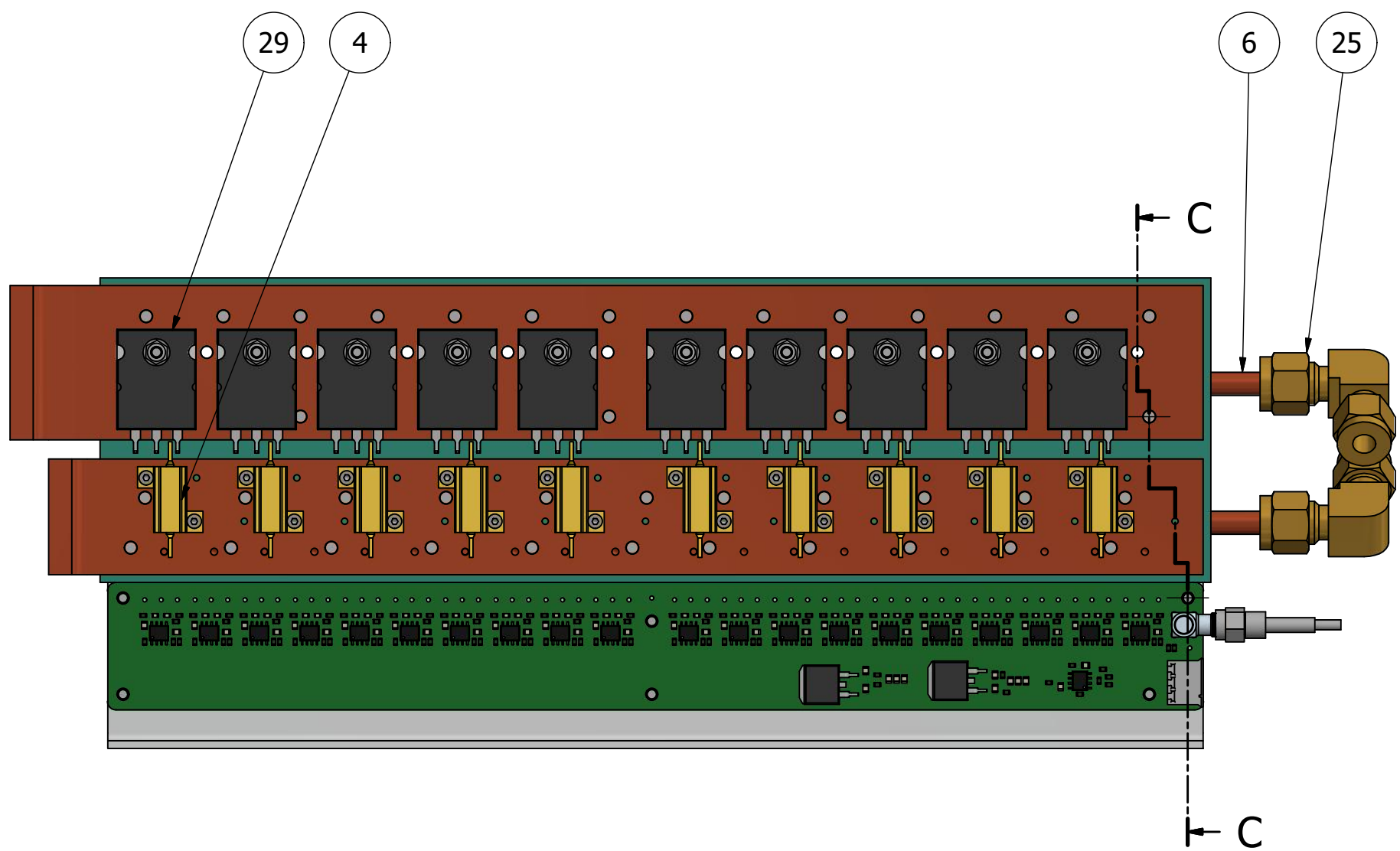
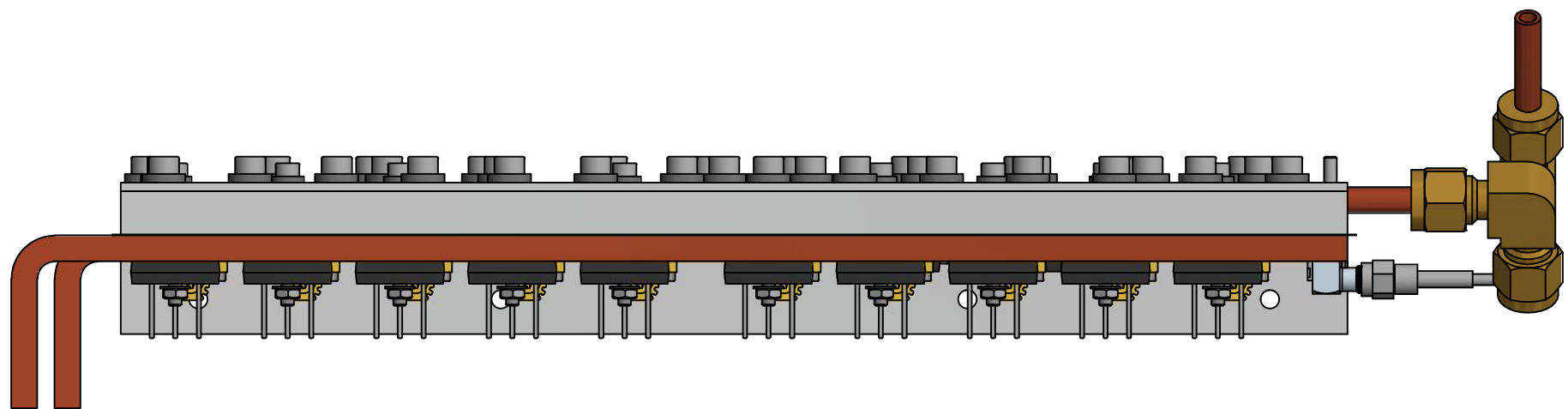


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Designed by	Projectnaam: Digital PID Loop		Date	Size	
Daan Giesen	Opdrachtgever: Reichsöllner, Lukas		Instituut: IOP	14-11-2017	A2
	Universiteit van Amsterdam Technologiecentrum		17-0028-0032 -		
			Status	Edition	Sheet
Faculteit der Natuurwetenschappen, Wiskunde en Informatica		CNC	A.1	1 / 16	

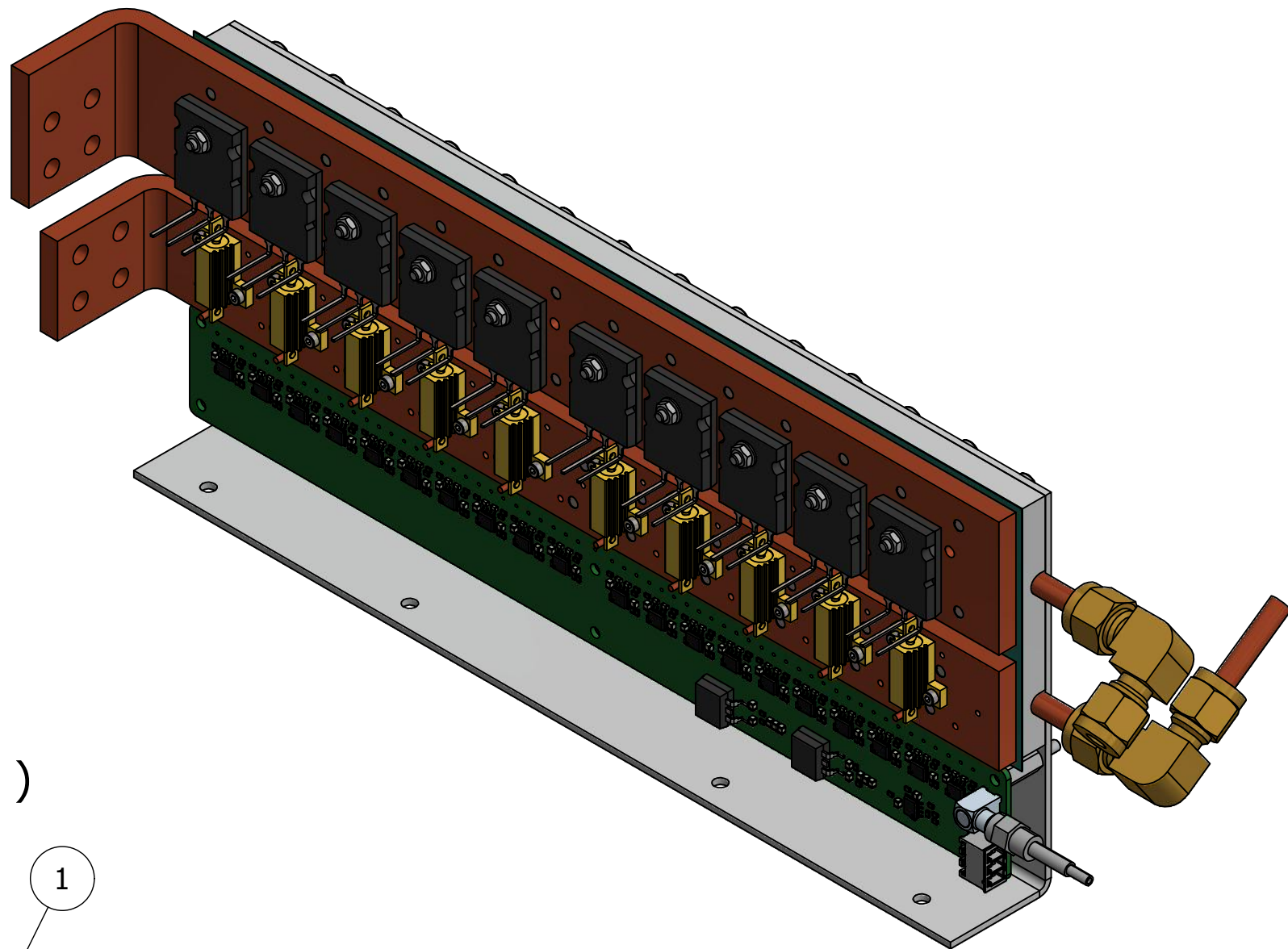
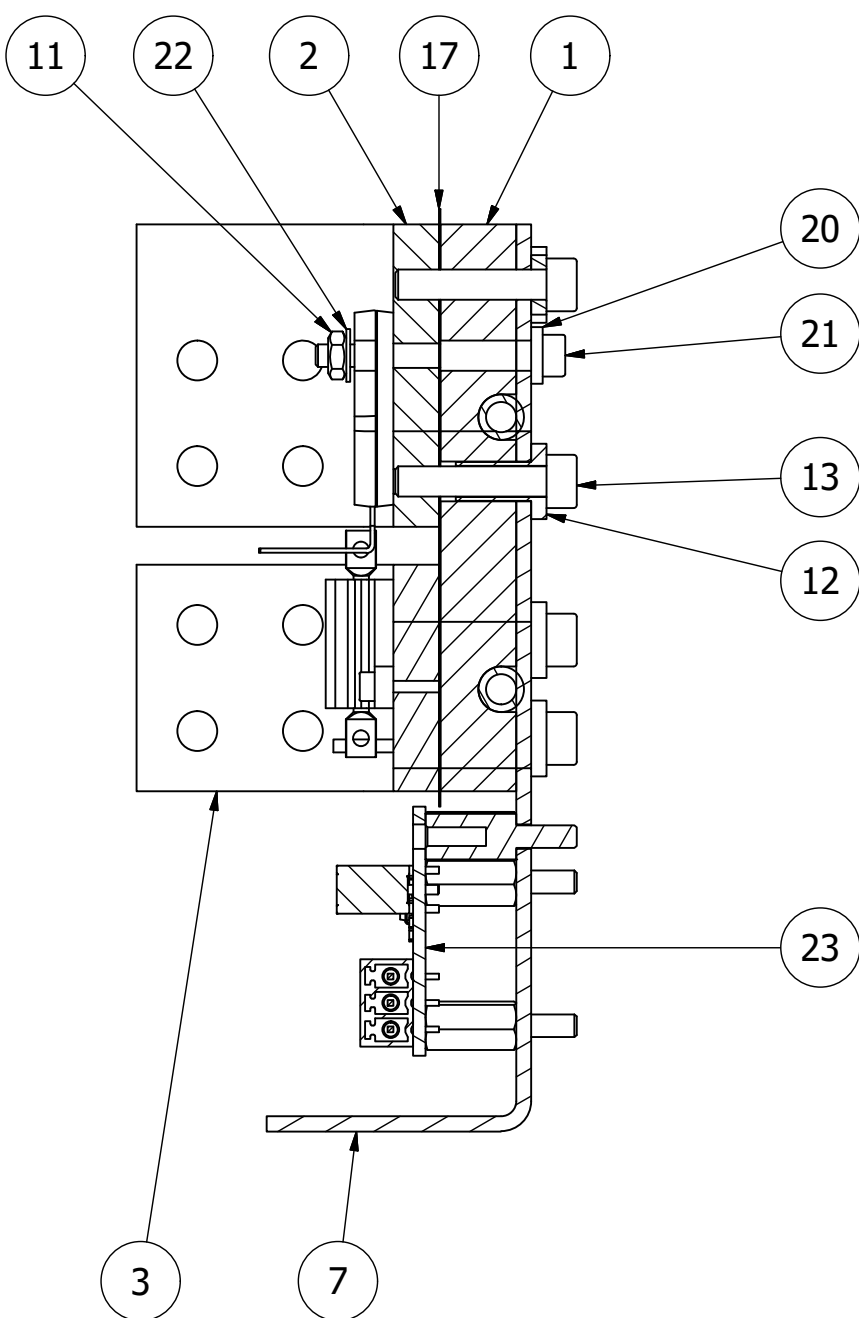


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			Status CNC	Edition A.1	Sheet 2 / 16





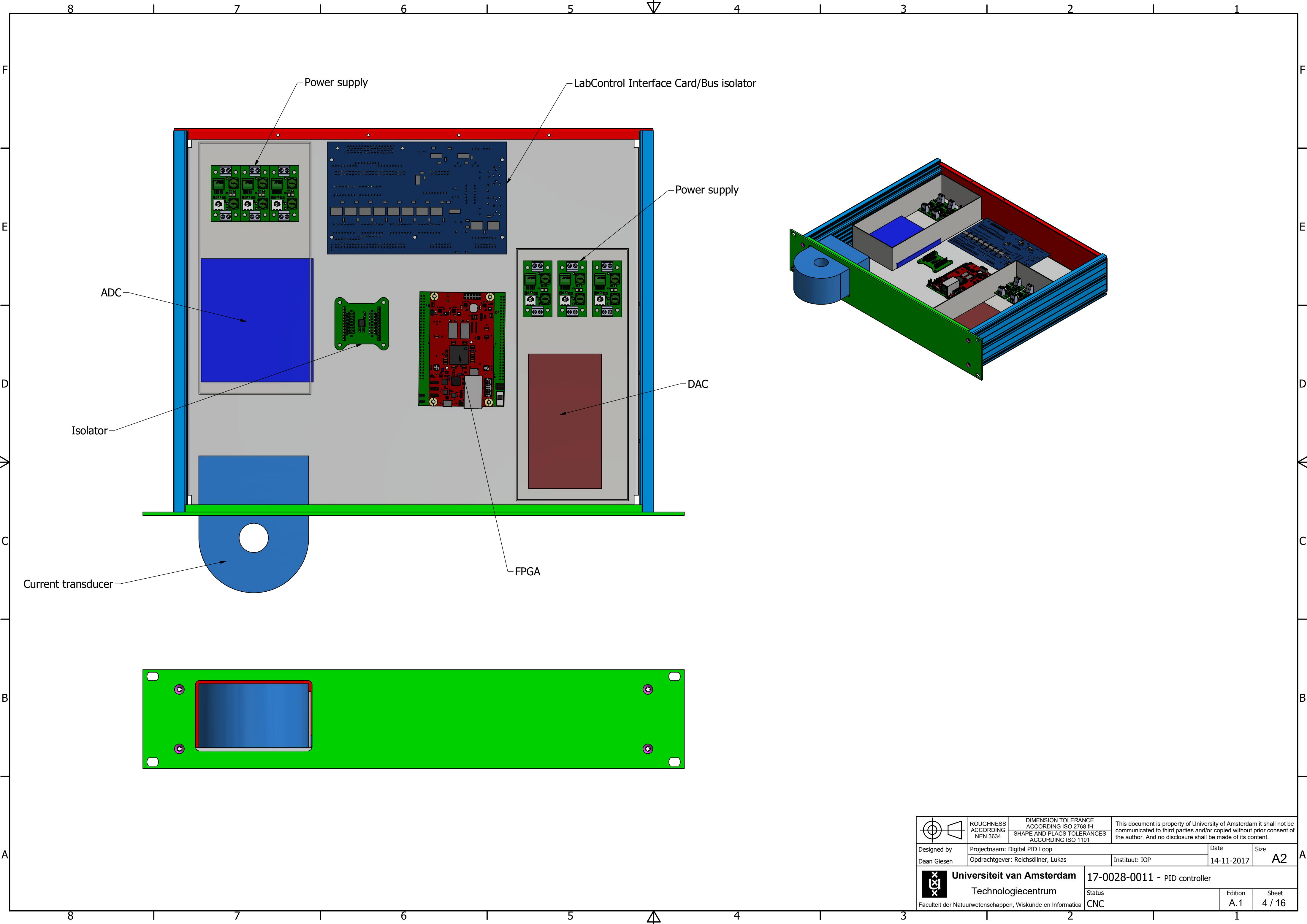
C-C ( 1 : 1 )



1	1	17-0028-0016.ipt	Heat sink plate
2	1	17-0028-0017.ipt	Upper bus bar
3	1	17-0028-0018.ipt	Lower bus bar
4	10	hsa5r10j.ipt	Shunt resistor
6	1	17-0028-0022.ipt	water cooling tube
7	1	17-0028-0024.ipt	single bank frame
11	10	DIN EN ISO 4032 - M3.ipt	Hex Nut. Product grades A and B
12	50	17-0028-0026.ipt	FJ01.2543.0520
13	50	DIN EN ISO 4762 - M4 x 20.ipt	Hexagon Socket Head Cap Screw
17	1	17-0028-0027.ipt	Insulating layer
20	10	17-0028-0031.ipt	FJ01.1543.0390
21	10	DIN EN ISO 4762 - M3 x 30.ipt	Hexagon Socket Head Cap Screw
22	10	DIN EN ISO 7089 - 3.ipt	Plain washers - Normal series - Product grade A
23	1	17-0028-0096.iam	Circuit board (control electronics)
25	2	B-6M0-9-SwagelokCompany-12-13-2017.ipt	Water cooling connection
26	1	17-0028-0104.ipt	Cooling tube
27	10	17-0028-0105.ipt	Solder connection
28	20	DIN EN ISO 4762 - M2 x 6.ipt	Hexagon Socket Head Cap Screw
29	10	17-0028-0118.ipt	FET
ITE	QTY	FILE NAME	DESCRIPTION

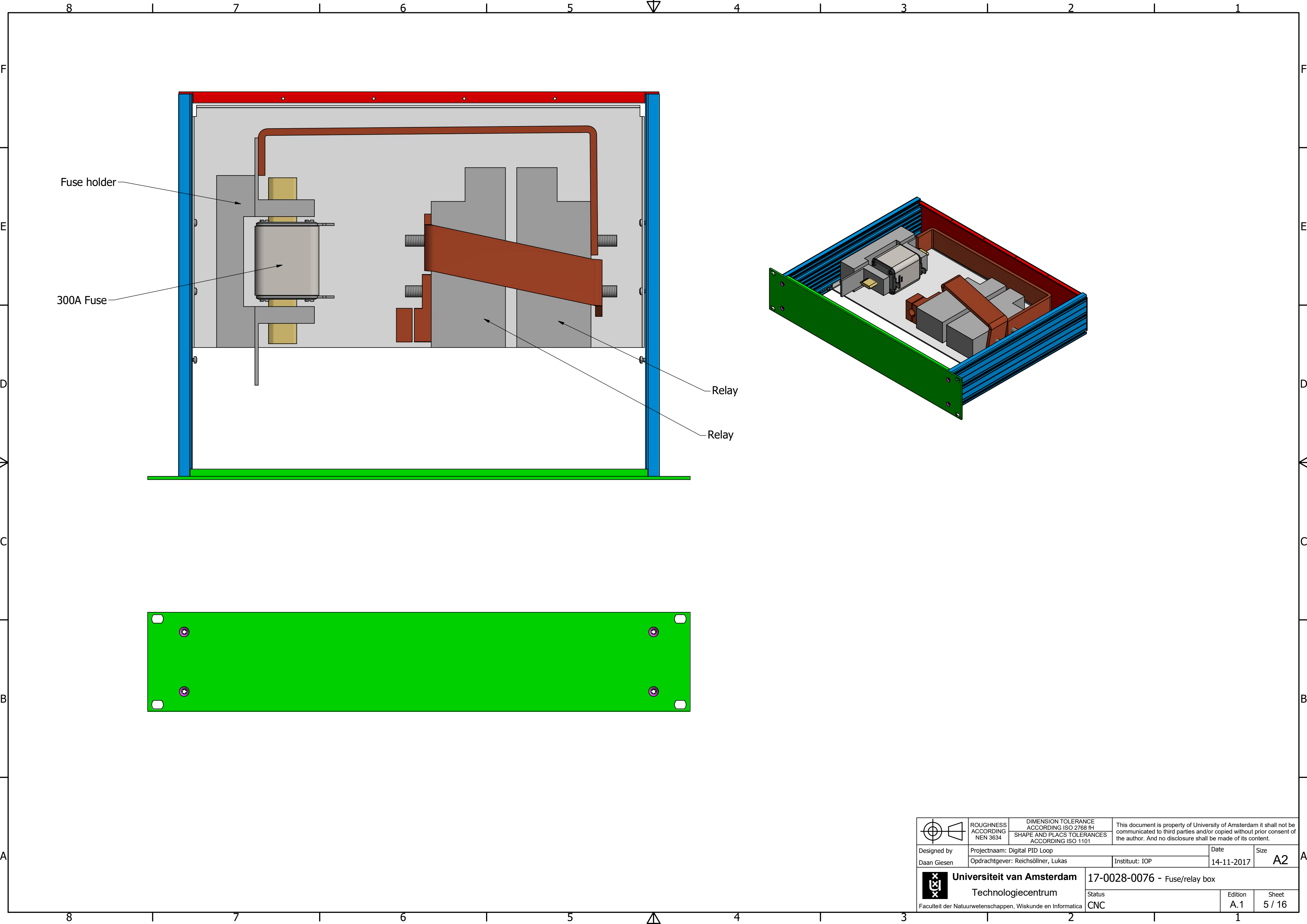
Assam List

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Opdrachtgever: Reichsöllner, Lukas		Instituut: IOP			
 <b>Universiteit van Amsterdam</b> Technologiecentrum Faculteit der Natuurwetenschappen, Wiskunde en Informatica		17-0028-0019 - Single FET bank			
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Daan Giesen	Opdrachtgever: Reichsöllner, Lukas		Instituut: IOP		
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