

Author: L. Reichsöllner

see also Application Note AN1264  
3-stage design (InBuffer - Filter - ADCdriver)

**RbSr**

Sheet: /Power/  
File: Power.sch

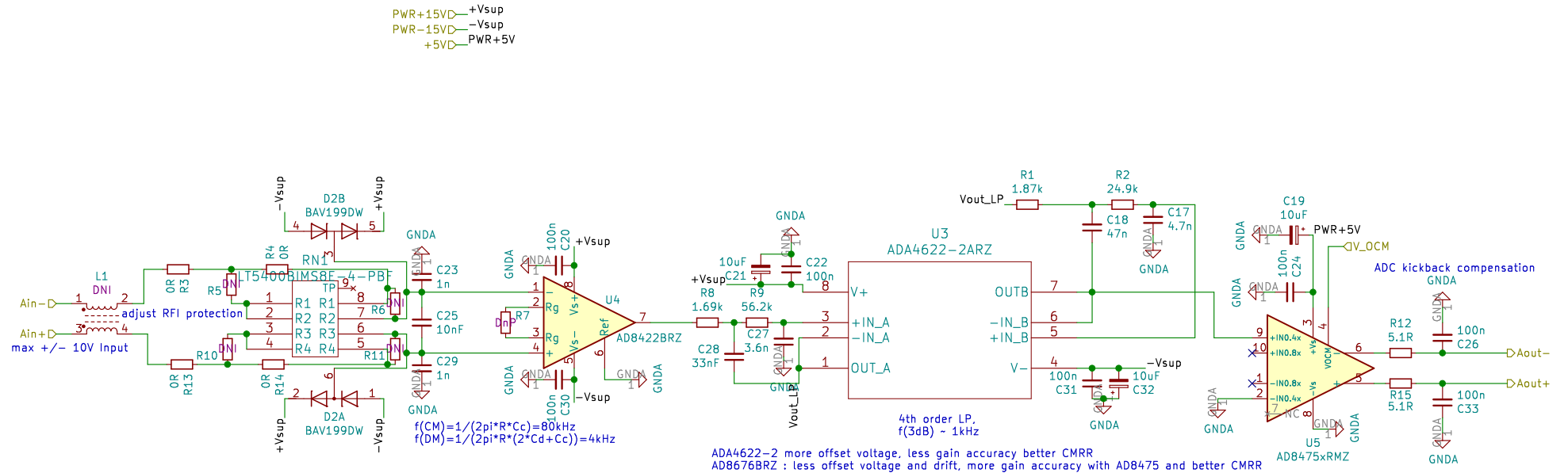
**Title: High-Precision, low noise Analog Front End**

Size: A4 Date: 2020-04-11

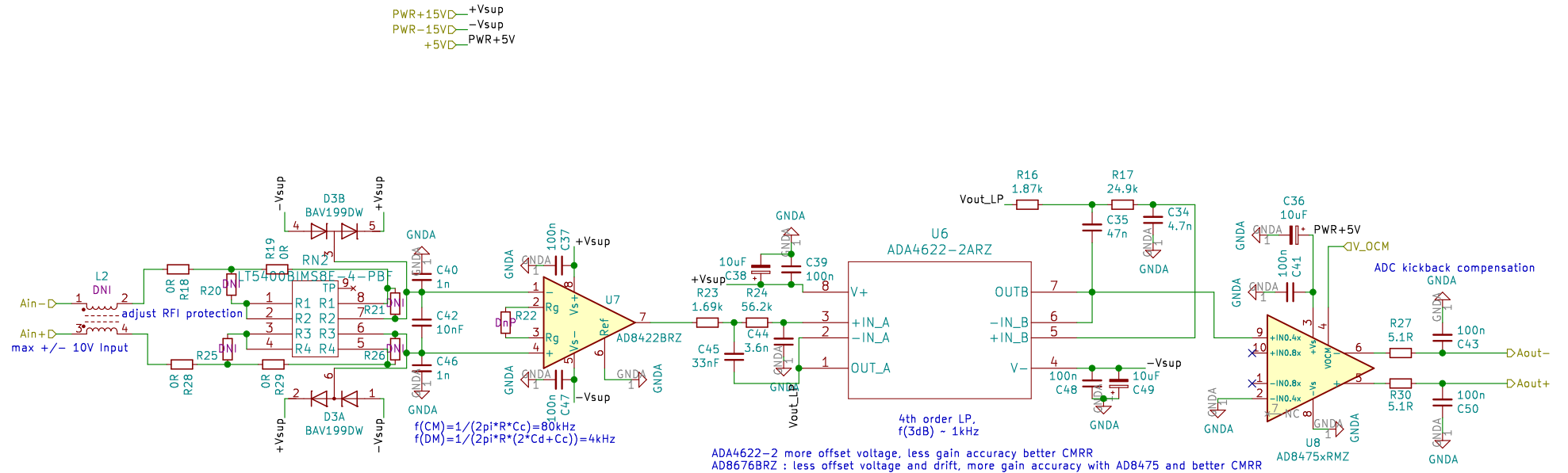
KiCad E.D.A. kicad (5.1.2)-1

Rev: 3

Id: 2/6



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3-stage design (InBuffer - Filter - ADCdriver)		
<b>RbSr</b>		
Sheet: /AFE1/		
File: AnalogFrontEnd.sch		
<b>Title: High-Precision, low noise Analog Front End</b>		
Size: A4	Date: 2020-05-29	Rev: 3
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**RbSr**

Sheet: /AFE2/  
 File: AnalogFrontEnd.sch

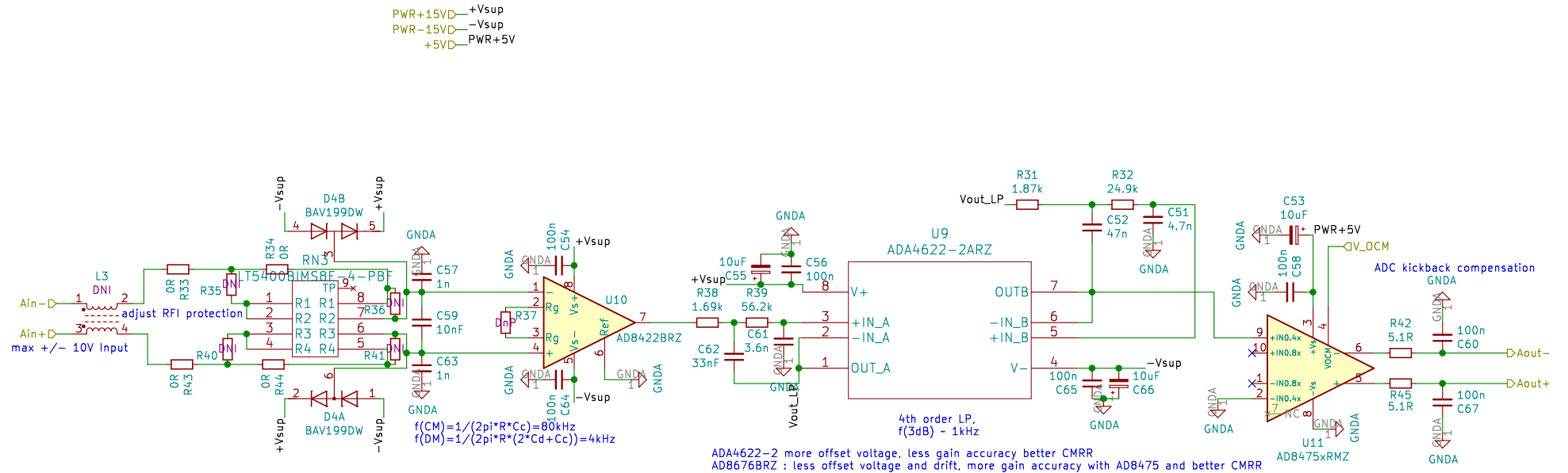
**Title: High-Precision, low noise Analog Front End**

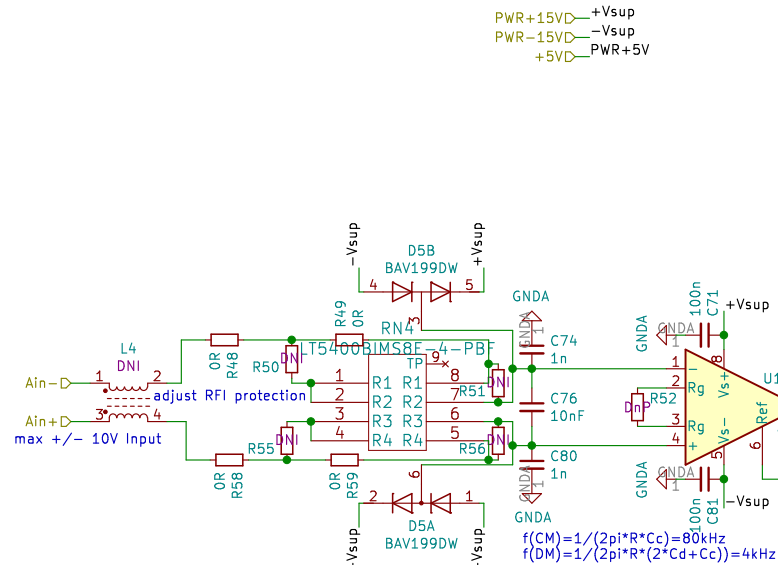
Size: A4 Date: 2020-05-29

KiCad E.D.A. kicad (5.1.2)-1

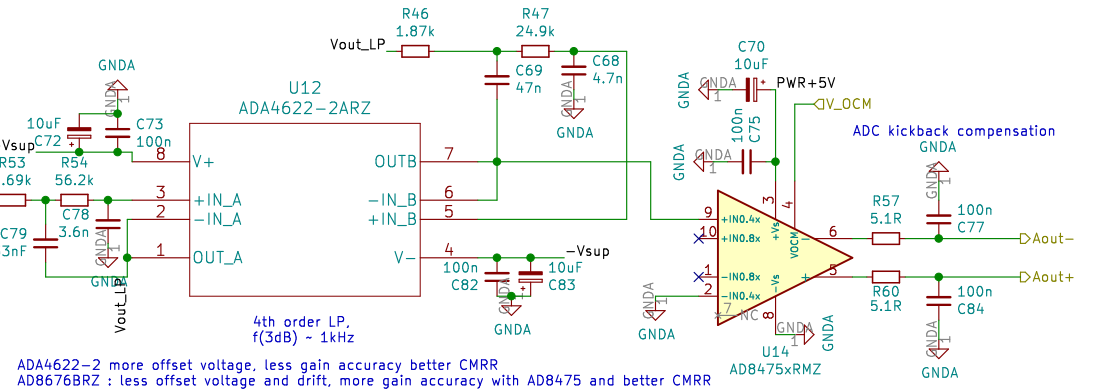
**Rev: 3**

Id: 4/6





set R3–R6 and to select total resistance of RN1:500R or 1k or 2k(default)  
 make path R10,R11,R13,R14 identical!!! (otherwise CMRR compromised)



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**RbSr**

Sheet: /AFE4/  
 File: AnalogFrontEnd.sch

**Title: High–Precision, low noise Analog Front End**

Size: A4 Date: 2020–05–29

KiCad E.D.A. kicad (5.1.2)–1

**Rev: 3**

Id: 6/6