

# README

## 1. Input Criteria:

- Start each line in input file without leaving space.
- Do not leave a blank line.
- If using labels , add space after ':'.
- The instruction and label must be on same line.
- The configuration file must not contain any replacement policies apart from (RANDOM,FIFO,LRU).
- The configuration file must not contain any write policies apart from (WT,WB).

## 2. Files:

- mysimulator.cpp (source code)
- Makefile
- report.pdf
- README

## 3. Instructions to run my code:

- Create a file with the assembly code in it in the same folder as the code.
- Using the Makefile - In the terminal run: make all
- Now to run the executable run ./riscv\_sim in the terminal.
- Continue entering commands as long as you wish.

#### 4. Messages printed:

- When `cache_sim enable filename` is executed.  
Cache Simulator Enabled
- When `cache_sim disable` is executed.  
Cache Simulator Disabled