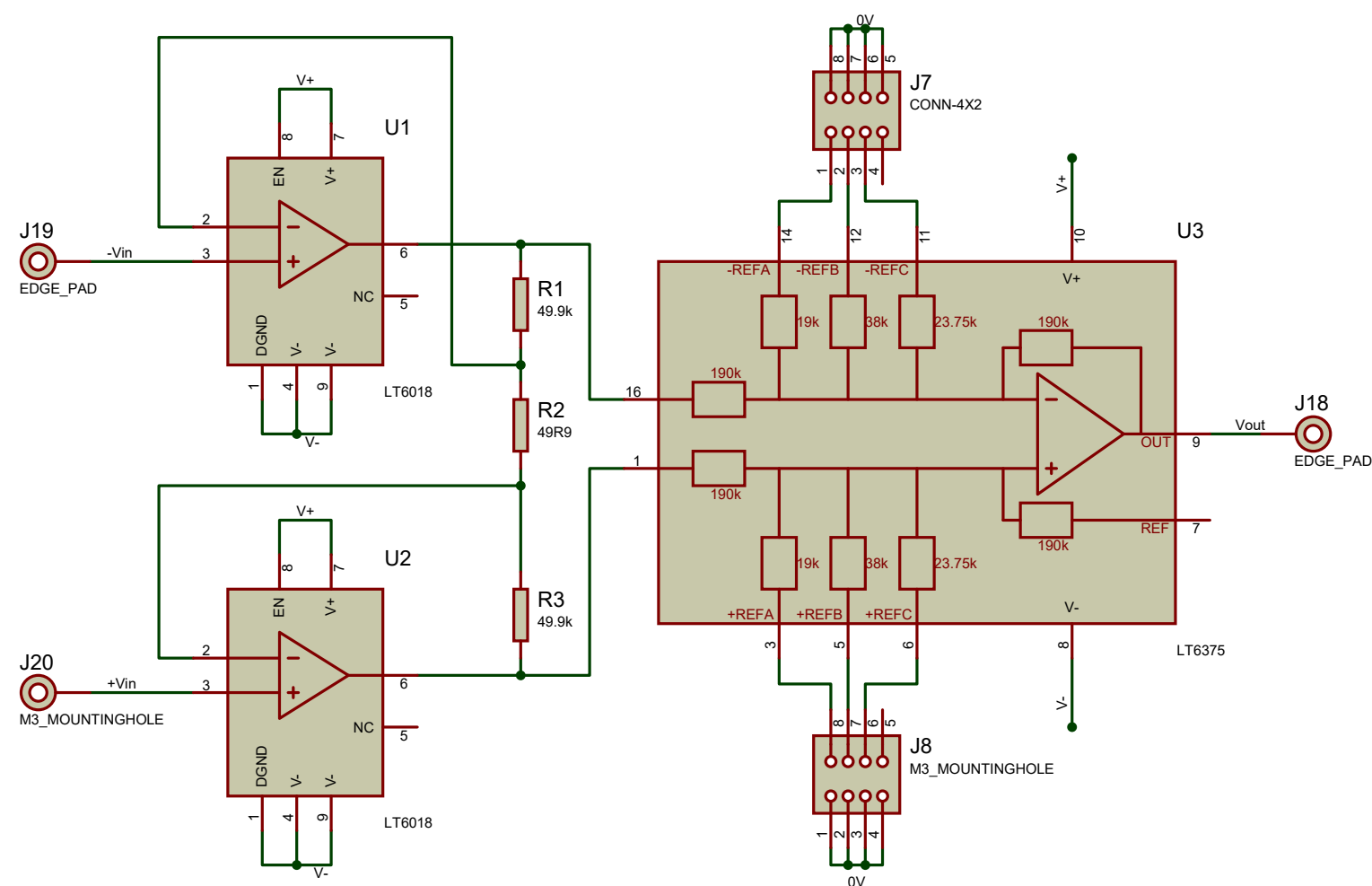
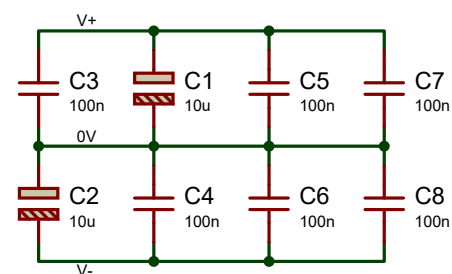


Amplification



Decoupling



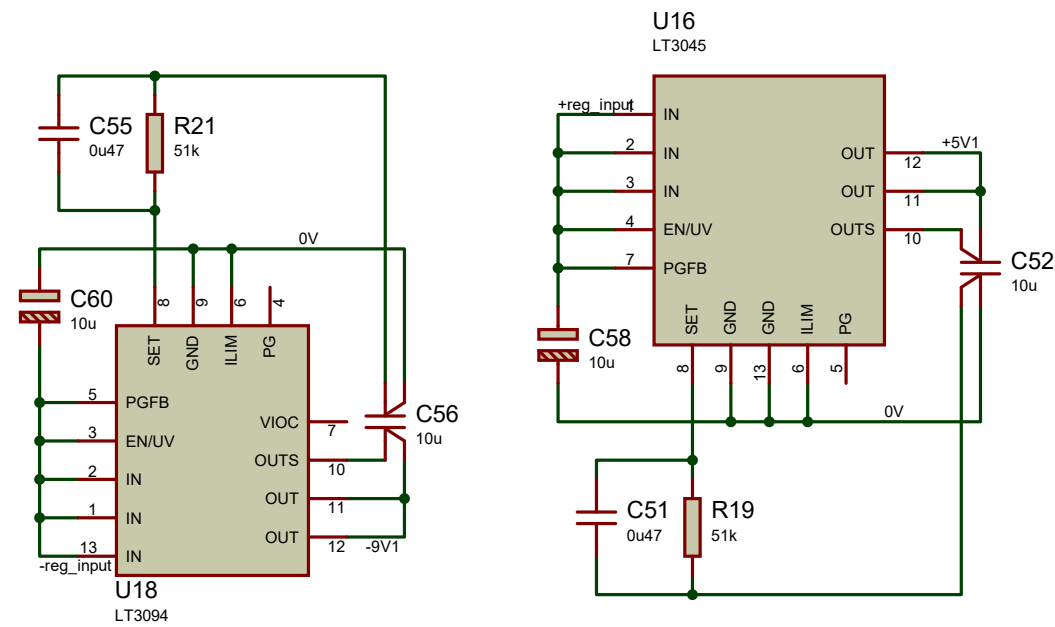
Jumper settings - J7 and J8

Bypass regulators
Pins 2&3
Pins 6&7

Regulators in-line
Pins 1&2
Pins 3&4
Pins 5&6
Pins 7&8

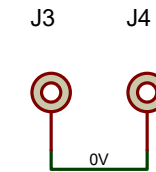
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DESIGN TITLE: Low Noise Amplifier	PAGE: 1
PATH: C:\Users\ShiftOneDrive\PhD\CAD\LNA\LNA.pdsprj	TIME: 12:39:05
BY: Stuart Kenny	REV: @REV

Power supply



+5V1 and -5V1 regulators @500mA each

Mounting posts and ground stitching



Jumper settings - J11

Bypass regulators
Pins 2&3
Pins 6&7

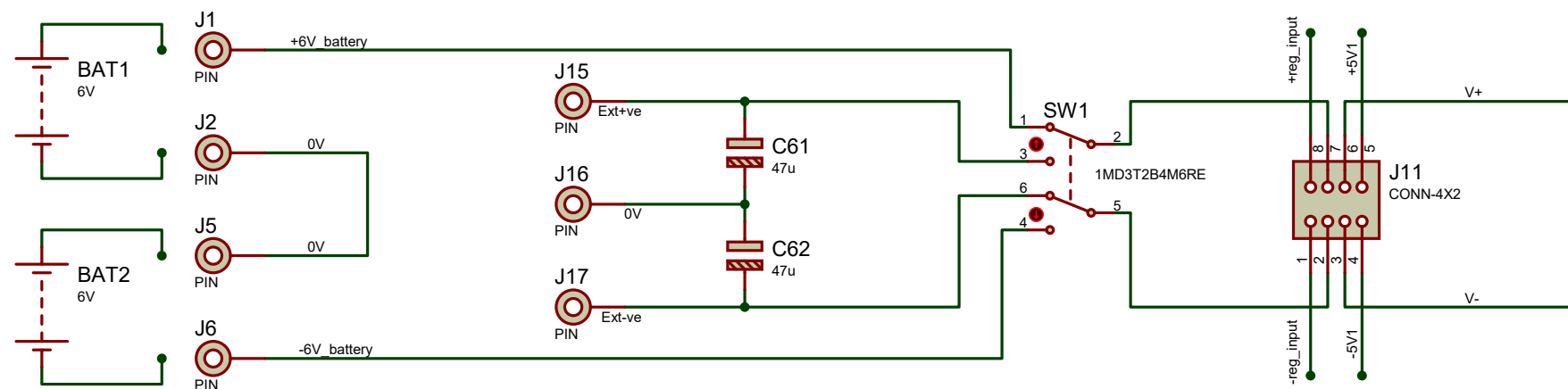
Regulators in-line
Pins 1&2
Pins 3&4
Pins 5&6
Pins 7&8

Connections for 2x 6V batteries

External supply

Internal/external selector

To amplifier



Power connections via chassis-mounted, filtered, pass-through pins

All caps rated to 50V

FILE NAME: LNA.pdsprj	DATE: 30/10/2018
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PATH: C:\Users\ShiftOneDrive\PhD\CAD\LNA\LNA.pdsprj	TIME: 12:39:05
BY: Stuart Kenny	REV: @REV