

# <Project Documentation B>

SWPP 2020 Spring LLVM Project  
Version 1 ( 2020.05.02)

## Team 4

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## B. Planning

Four members of the team will work on following optimization passes per each sprint. Note that the numbers and titles of optimization refer to optimization requirement and specification described in documentation A.

	Sprint 1	Sprint 2	Sprint 3
Hyoung Uk Sul	1	5	9
	Function Outlining	Tail Call Elimination	Function Inlining
Ahyoung Oh	3	7	11
	Arithmetic Optimizations	Reordering Memory Accesses	Lowering Allocas to IR registers
Jaewook Lee	2	6	10
	Malloc to Alloca Conversion	Dead Argument Elimination	Register Allocation
Jaeun Lee	4	8	12
	Packing Registers	Rewriting Loop Counter to Use Cheaper Operation	Loop Invariant Code Motion