

Power and FTDI

Power.sch

MCU

MCU.sch

Sensors

Peripherals.sch

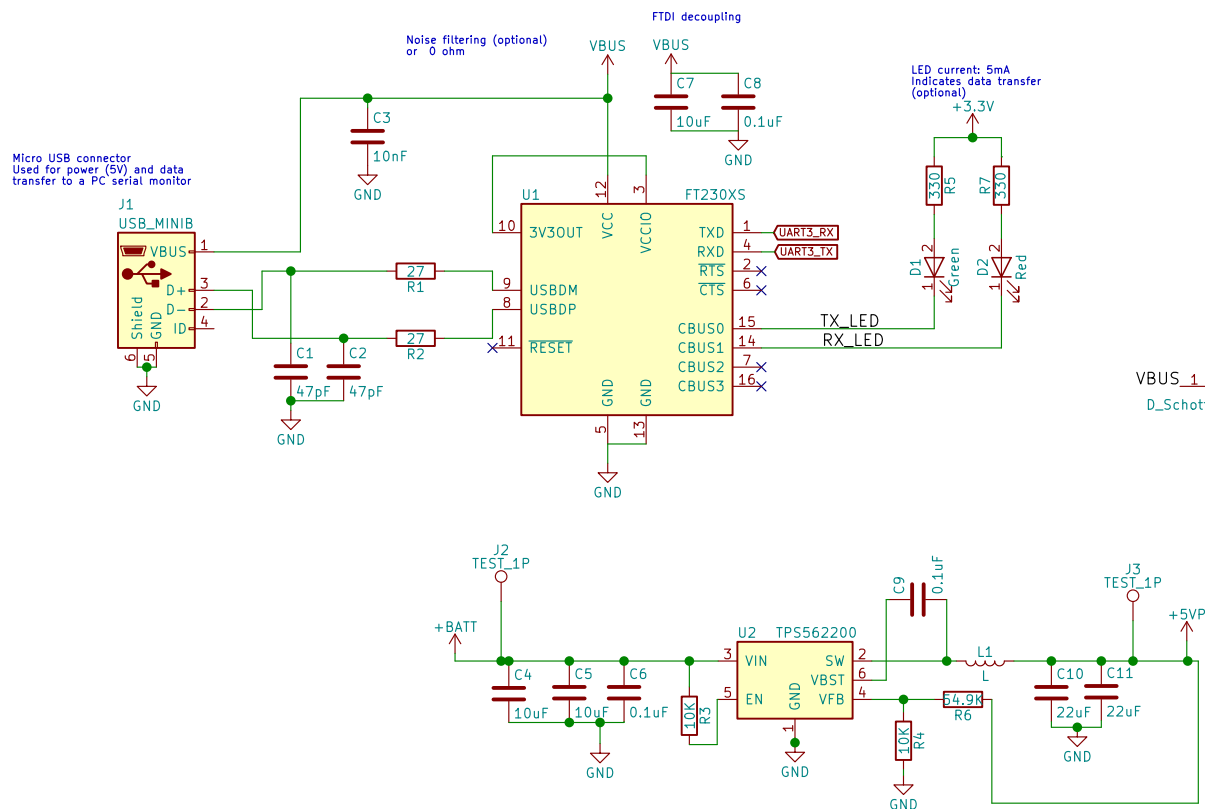
Headers_and_Debug

Headers.sch

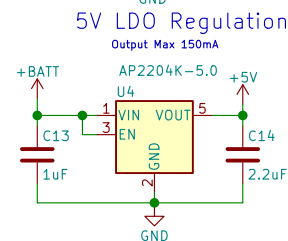
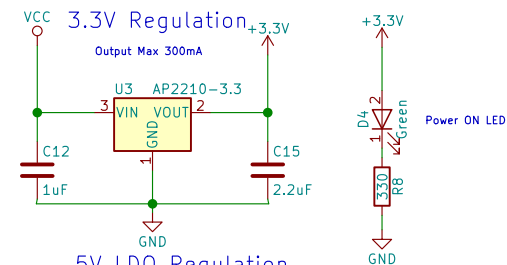
TO DO:
-Fix sheet file names

Garnet Tanner Andromeda SOAR		
Sheet: / File: AndromedaV2.sch		
Title: SOAR Avionics Flight		
Size: USLetter	Date: 2018-04-29	Rev: 1
KiCad E.D.A. - kicad 4.0.7		Id: 1/5

USB Power



CAUTION!
V1 Boards have incorrect footprint for 3.3V regulator.
Pin 1 is VIN and Pin 3 is VOUT.
Board placement needs to be mirrored.



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SOAR

Sheet: /Power and FTDI/
File: Power.sch

Title: SOAR Avionics Development Board

Size: A4 Date: 2017-12-03

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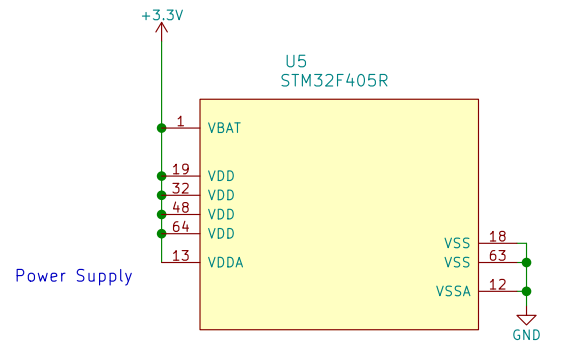
Rev: 1

Id: 2/5

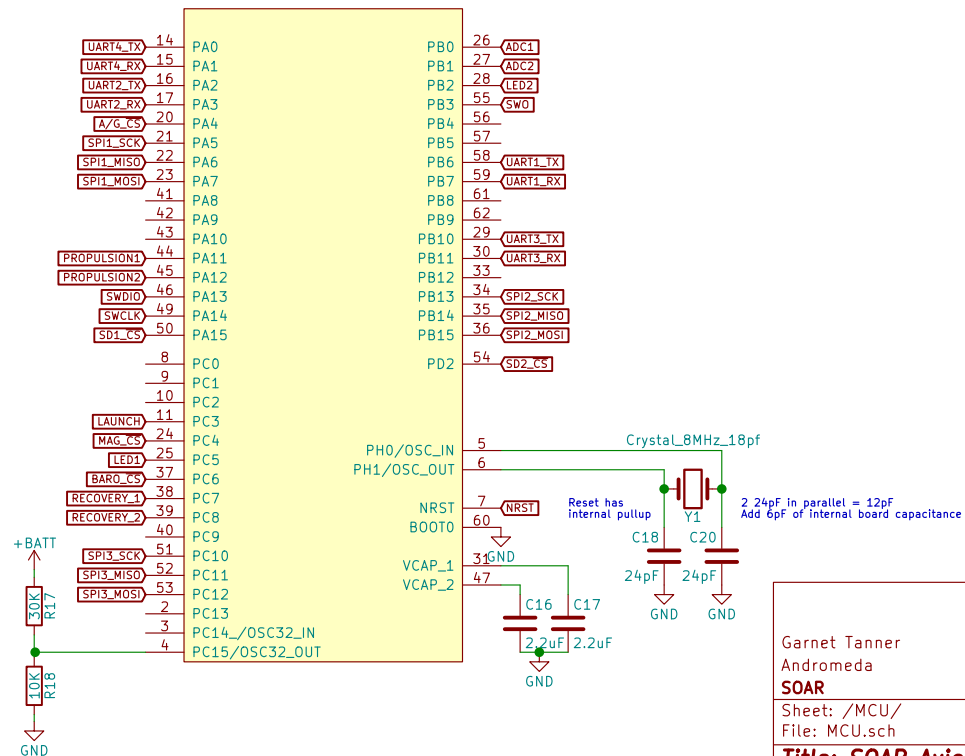
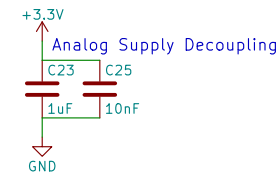
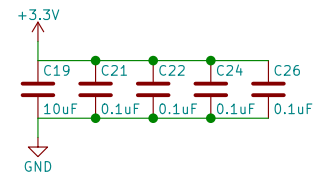
See STM32 Hardware Design Guide for schematic example

STM32F405

Same family as F407 on Discovery boards



Digital Decoupling
1 100nF for each power supply (place close to each VDD pin)
plus 1 10uF



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Sheet: /MCU/
File: MCU.sch

Title: SOAR Avionics Development Board

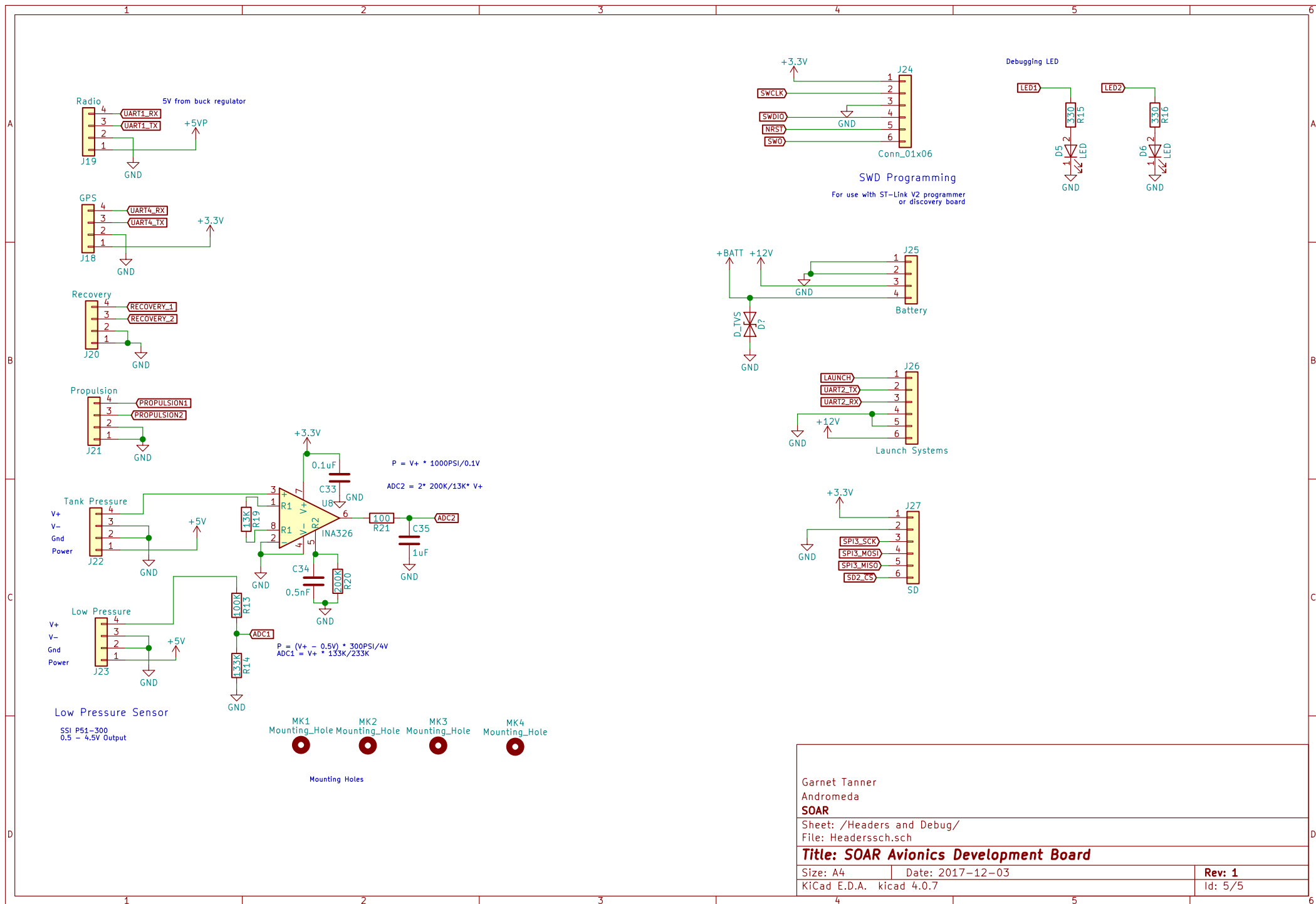
Size: A4 Date: 2017-12-03

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Rev: 1
Id: 3/5



Rev: 1
Id: 4/5



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SOAR

Sheet: /Headers and Debug/
File: Headerssch.sch

Title: SOAR Avionics Development Board

Size: A4 Date: 2017-12-03

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Rev: 1

Id: 5/5