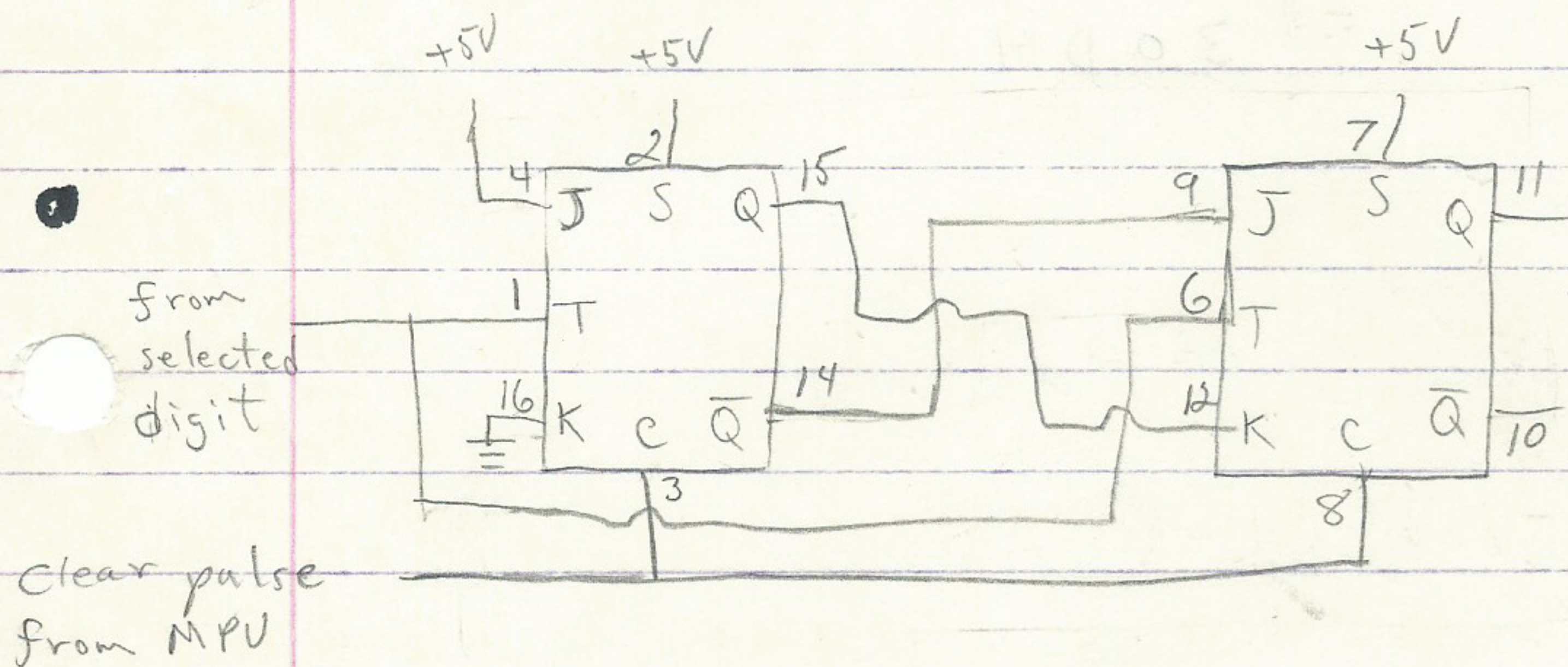
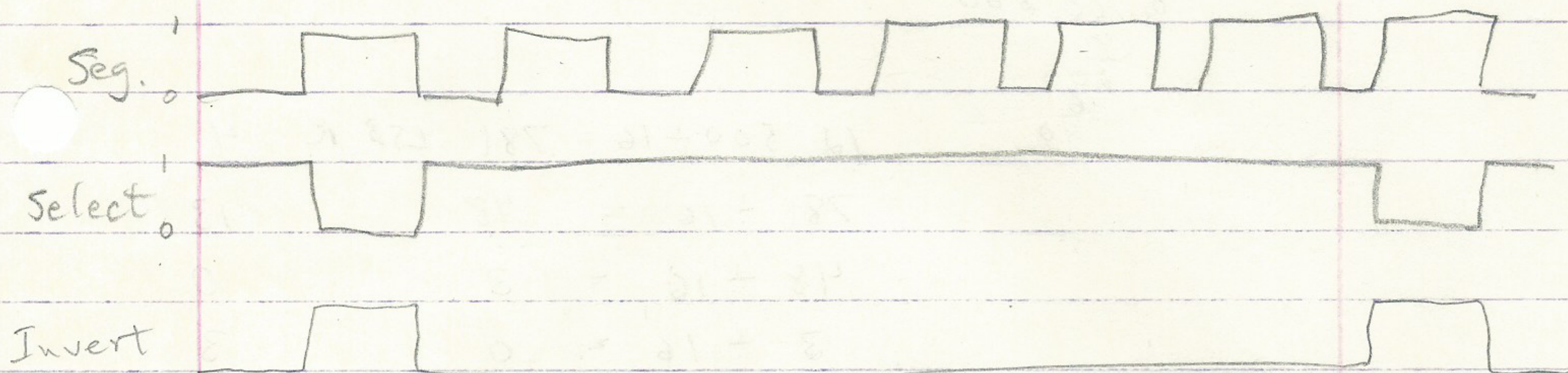


3-25-83

# Flip-flop circuit



5 -  $V_{cc}$  +5V

13 - Gnd

74176

Note! After a low clear pulse the left flip-flop will set & stay set, the right one will set on the first clock signal to T & then reset & stay reset on the second clock signal.