	Decoder	Logic for ut.	tra Sonice sen	sor selector
	NAND	AND	0 8	NOR
	001	0000	000	00/1
	011	010	01/1	010
	10	100	101	100
				1110
2 1				
00)o- 1st Tra	nsducer Front	
	41			
01	Qo)0 - 1 2 N d TY	-andrier Right	
	Q,	· · · · · · · · · · · · · · · · · · ·		
YA				
10	90	1.70-3vd T	randucer Left	
	QI			
4.4	Q ₀		insducer Front	
	01	15 Tro	insancer 1	
	Note: There is always one relay activated			
	Note: There is always one relay activated so that sultrasonic board will not be damaged			
	4			