**Day 1**

PICORV32A PREP DONE

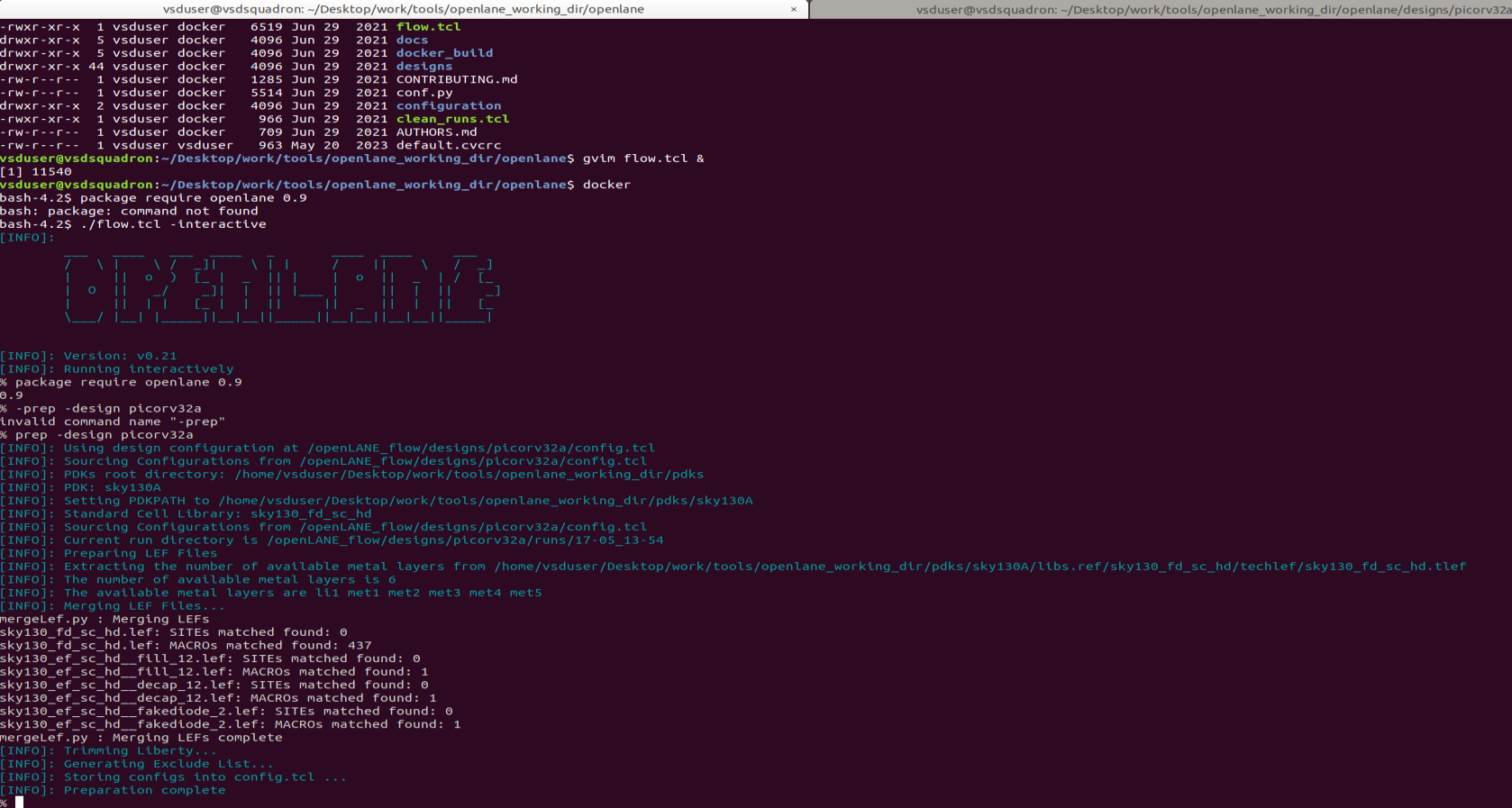
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FIG 1. 1

Run\_synthesis done

A screen shot of a computer code

Description automatically generated

FIG 1. 2

Synthesis and ABC runs have completed.

**TASK 1 : To find flop ratio.**

A screenshot of a computer program

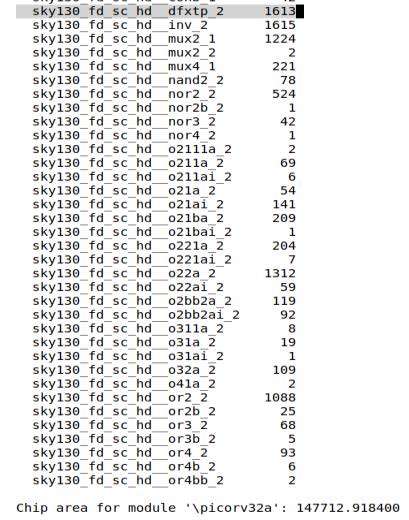
Description automatically generated

FIG 1. 3

**Design name: picorv32a.**

Chip module area : 147712.918400

Flop ratio = no of DFFs (D flip-flop)/Total no of cells.

Total no of cells = 14876 (as seen in FIG 1.2).

No of DFFs (count of **sky130\_fd\_sc\_hd\_\_dfxtp\_2**) = 1613 (as seen in FIG 1.3).

Flop ratio = 1613/14876 = 0.1084296853993009 = 10.84 %

Results path: /home/vsduser/Desktop/work/tools/openlane\_working\_dir/openlane/designs/picorv32a/runs/17-05\_13-54/results

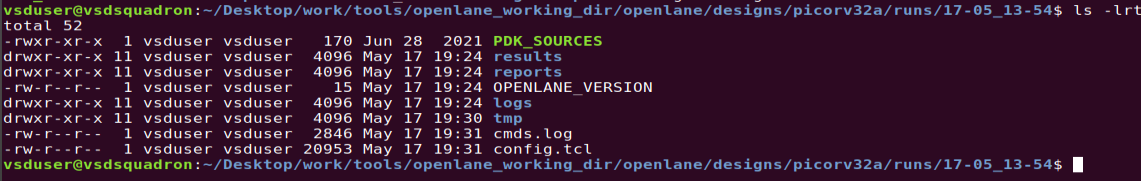


Synthesized netlist:

/home/vsduser/Desktop/work/tools/openlane\_working\_dir/openlane/designs/picorv32a/runs/17-05\_13-54/results/synthesis/picorv32a.synthesis.v



Run directory structure:



Yosys report path:

/home/vsduser/Desktop/work/tools/openlane\_working\_dir/openlane/designs/picorv32a/runs/17-05\_13-54/reports/synthesis/1-yosys\_4.stat.rpt



OpenSTA report path:

/home/vsduser/Desktop/work/tools/openlane\_working\_dir/openlane/designs/picorv32a/runs/17-05\_13-54/reports/synthesis/2-opensta.timing.rpt



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