CE220290 - PSoC 6 MCU TCPWM Breathing LED

Objective

This code example demonstrates the implementation of an LED breathing effect using the TCPWM Component on a PSoC® 6 MCU.

Overview

This code example implements a breathing LED using two TCPWM Components (configured as PWM) and an XOR gate, without CPU involvement. The LED breathing effect is implemented by changing the duty cycle of the PWM signal from low to high and vice-versa cyclically.

Requirements

Tool: PSoC Creator™ 4.2

Programming Language: C (Arm® GCC 5.4.1, Arm MDK 5.22)

Associated Parts: All PSoC 6 MCU parts with UDB

Related Hardware: CY8CKIT-062-BLE PSoC 6 BLE Pioneer Kit

Hardware Setup

The code example works with the default settings on the CY8CKIT-062-BLE PSoC 6 BLE Pioneer Kit. If the settings are different from the default values, see the “Selection Switches” table in the kit guide to reset to the default settings.

Software Setup

None.

Operation

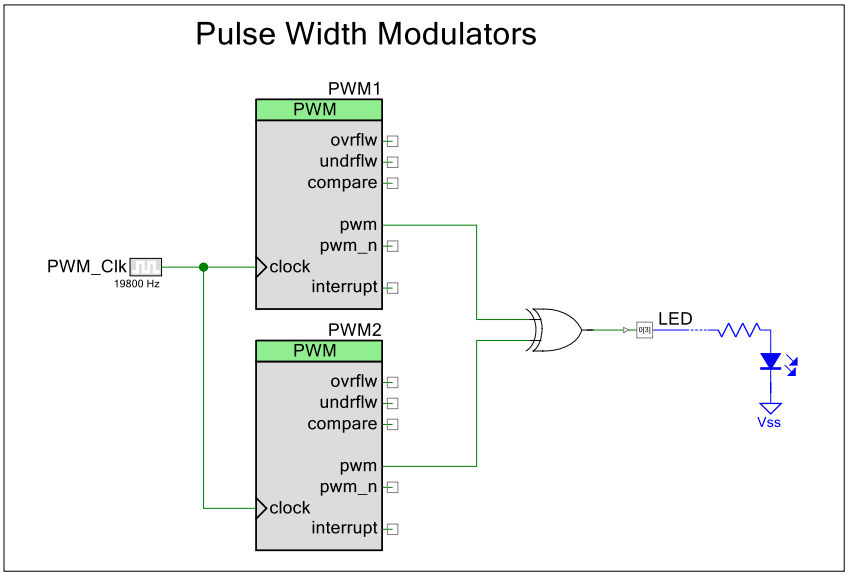
1. Open the *CE220290_TCPWM_Breathing* code example in PSoC Creator.
2. Build the project (**Build** > **Build CE220290_TCPWM_Breathing**).
3. Connect the CY8CKIT-062-BLE PSoC 6 BLE Pioneer Kit to your computer using the USB cable provided.
4. Program the PSoC 6 MCU (**Debug** > **Program**). See the kit guide for details on programming the kit.

The red LED (LED5) shows breathing effect.

Design and Implementation

Figure 1 shows the PSoC Creator schematics of this code example. This code example uses the two TCPWMs, XOR, Pin, and Clock Components.

Figure 1. TopDesign Schematic

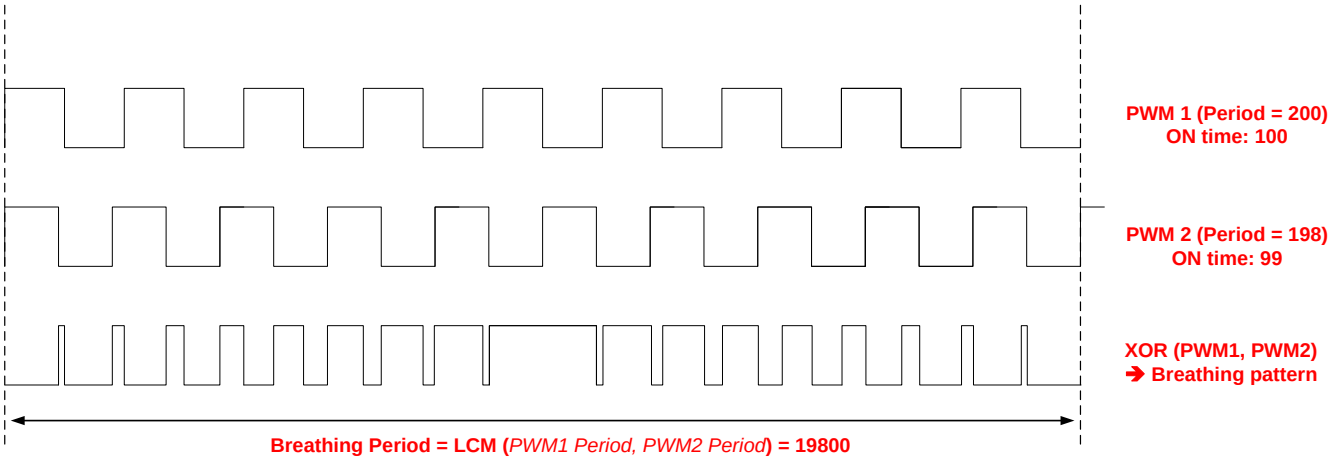


When the intensity of an LED is gradually varied from zero to maximum and then from maximum to zero cyclically, a breathing LED effect is generated. This effect is analogous to the human breathing pattern – inhale (zero to maximum) and exhale (maximum to zero).

Using the flexibility of the PSoC 6 MCU, the waveform required to generate the breathing effect is implemented entirely in hardware. In this example, two PWMs are used; one with a period 200 (10.1 ms) and another with a period of 198 (10 ms), both having a duty cycle of 50%. The outputs of these two PMWs are fed to an XOR gate to generate the breathing LED output, as shown in Figure 2. The breathing period of the LED is given by the least common multiple (LCM) of the two periods.

Firmware starts the PWMs and Clock components. After that, the CPU is put to Sleep mode as it is not required for any other operation and to save power.

Figure 2. Generating Breathing PWM Output Using PWMs and XOR



Components and Settings

Table 1 lists the PSoC Creator Components used in this example, how they are used in the design, and the non-default settings required so they function as intended.

Table 1. List of PSoC Creator Components

Component	Instance Name	Purpose	Non-default Settings
PWM (TCPWM)	PWM1	Generate square wave and bring out the signal to GPIO	**Period 0**: 199 **Compare 0**: 100
	PWM2		**Period 0**: 197 **Compare 0**: 99
Digital Output Pin	LED	Drive the PWM signal to LED	-
Clock	PWM_Clk	Drive the PWM at 19.8kHz	**Frequency**: 19800 Hz
Xor	xor_1	XOR the PWM signals	-

For information on the hardware resources used by a Component, see the Component datasheet.

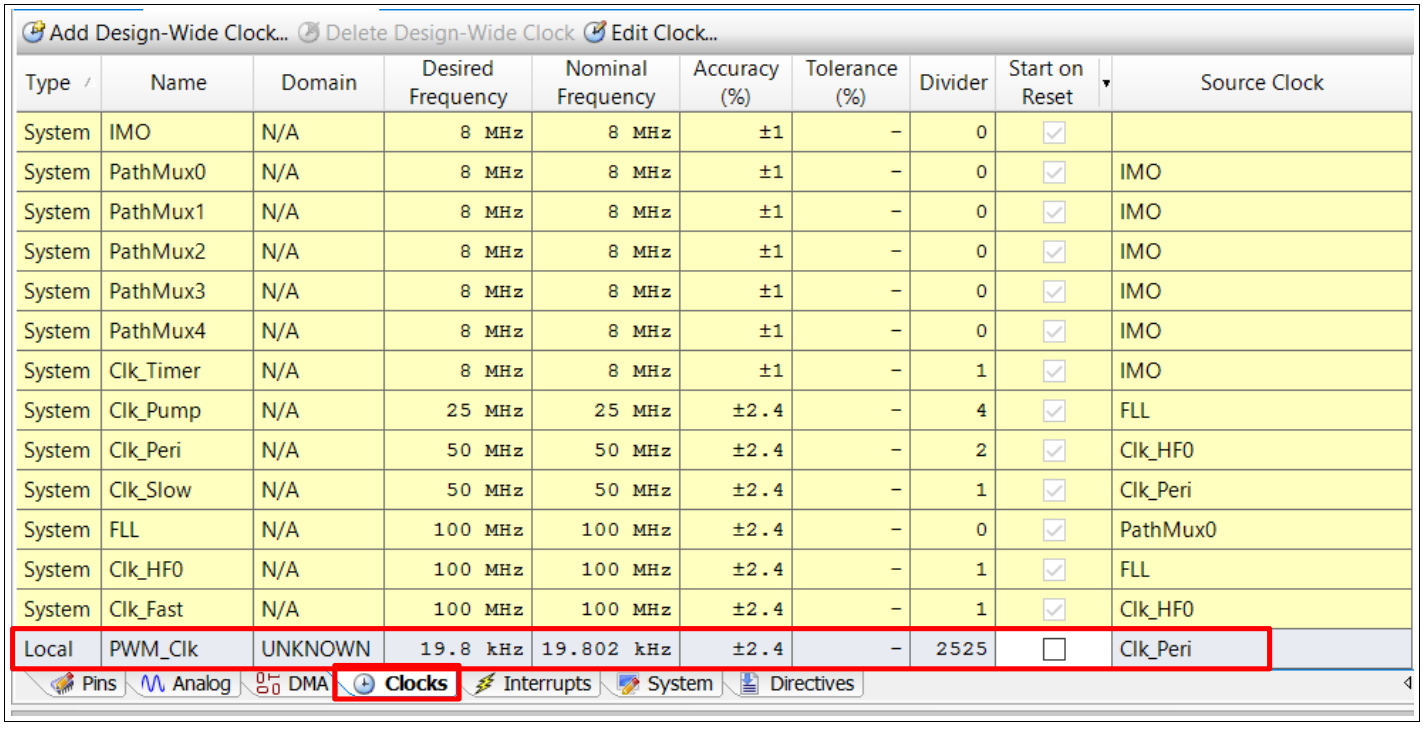
Table 2 shows the pin assignment for the project done through the **Pins** tab in the **Design Wide Resources** window.

Table 2. Pin Names and Location

Pin Name	Location
LED	P0[3]

Figure 3 shows the clock settings for the project configured through the **Clocks** tab in the **Design Wide Resources** window.

Figure 3. CYDWR Clocks Tab Settings



Type	Name	Domain	Desired Frequency	Nominal Frequency	Accuracy (%)	Tolerance (%)	Divider	Start on Reset	Source Clock
System	IMO	N/A	8 MHz	8 MHz	±1	-	0	☑	
System	PathMux0	N/A	8 MHz	8 MHz	±1	-	0	☑	IMO
System	PathMux1	N/A	8 MHz	8 MHz	±1	-	0	☑	IMO
System	PathMux2	N/A	8 MHz	8 MHz	±1	-	0	☑	IMO
System	PathMux3	N/A	8 MHz	8 MHz	±1	-	0	☑	IMO
System	PathMux4	N/A	8 MHz	8 MHz	±1	-	0	☑	IMO
System	Clk_Timer	N/A	8 MHz	8 MHz	±1	-	1	☑	IMO
System	Clk_Pump	N/A	25 MHz	25 MHz	±2.4	-	4	☑	FLL
System	Clk_Peri	N/A	50 MHz	50 MHz	±2.4	-	2	☑	Clk_HF0
System	Clk_Slow	N/A	50 MHz	50 MHz	±2.4	-	1	☑	Clk_Peri
System	FLL	N/A	100 MHz	100 MHz	±2.4	-	0	☑	PathMux0
System	Clk_HF0	N/A	100 MHz	100 MHz	±2.4	-	1	☑	FLL
System	Clk_Fast	N/A	100 MHz	100 MHz	±2.4	-	1	☑	Clk_HF0
Local	PWM_Clk	UNKNOWN	19.8 kHz	19.802 kHz	±2.4	-	2525	☐	Clk_Peri

Reusing This Example

This code example is designed to run on CY8CKIT-062-BLE PSoC 6 BLE Pioneer Kit. To port the design to a different PSoC 6 MCU device and/or kit, change the target device in Device Selector, and update the pin assignments in the Design Wide Resources Pins settings as needed. For single-core PSoC 6 MCU devices, port the code from *main_cm4.c* to *main.c* file.

Related Documents

Application Notes	
AN210781 – Getting Started with PSoC 6 MCU with Bluetooth Low Energy (BLE) Connectivity	Describes PSoC 63 with Bluetooth Low Energy (BLE) Connectivity and how to build your first PSoC Creator project
PSoC Creator Component Datasheets	
PWM	Supports fixed-function PWM implementation
Pins	Supports connection of hardware resources to physical pins
Clock	Supports local clock generation
Device Documentation	
PSoC 6 MCU: PSoC 63 with BLE Datasheet	PSoC 6 MCU: PSoC 63 with BLE Architecture Technical Reference Manual
Development Kit (DVK) Documentation	
CY8CKIT-062-BLE PSoC 6 BLE Pioneer Kit	

Document History

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Revision	ECN	Orig. of Change	Submission Date	Description of Change
*A	5845464	SRDS	08/17/2017	Initial public release
*B	5991534	SRDS	12/21/2017	Updated template and minor text changes. Updated project to PSoC Creator 4.2 Beta.

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