

CE320 : The Ten-Minute Paper: In Class practice

Date: Wednesday, 02/20/2019

Student Name: _____

Please write down

1. Six Push instructions and explain the operations

Assembly	Meaning	Operation
PSHA	<u>PuSH</u> Accumulator A onto Stack	$SP=SP-1$, then copy A to Mem[SP]
PSHB	<u>PuSH</u> Accumulator B onto Stack	$SP=SP-1$, then copy B to Mem[SP]
PSHC	<u>PuSH</u> CCR onto Stack	$SP=SP-1$, copy CCR to Mem[SP]
PSHD	<u>PuSH</u> D register onto Stack	$SP=SP-2$, Mem[SP: SP+1] =D
PSHX	<u>PuSH</u> X register onto Stack	$SP=SP-2$, Mem[SP: SP+1] =X
PSHY	<u>PuSH</u> Y register onto Stack	$SP=SP-2$, Mem[SP:SP+1] =Y

2. Six Pull instructions and explain the operations

Assembly	Meaning	Operation
PULA	<u>PUL</u> I Accumulator A from Stack	A=Mem[SP] then $SP=SP+1$
PULB	<u>PUL</u> I Accumulator B from Stack	B=Mem[SP] then $SP=SP+1$
PULC	<u>PUL</u> I CCR from Stack	CCR=Mem[SP] then $SP=SP+1$
PULD	<u>PUL</u> I D register from Stack	D=Mem[SP: SP+1] then $SP=SP+2$
PULX	<u>PUL</u> I X register from Stack	X=Mem[SP: SP+1] then $SP=SP+2$
PULY	<u>PUL</u> I Y register from Stack	Y=Mem[SP: SP+1] then $SP=SP+2$

3. How does the HCS12 remember where in the main program to resume when it hits a RTS?
- JSR(or BSR) instruction **pushes the address of the instruction immediately following the sub routine call instruction on the top of the stack**
 - **RTS instruction pulls the return address from the stack and loads it into the PC.**