## 21CS51: AUTOMATA THEORY AND COMPILER DESIGN ASSIGNMENT-2

1. Obtain a grammar to generate the following languages

Answer a. L= {an+2bm | n > 0 and m > n}

strêngs that can be generated can be represented as

$$N=0$$
  $N=1$   $N=2$ 

This is in the form of  $a^nb^n|n\geq 1$ . One a followed zero or more b's. The production can be written as

when the language  $a^nb^n \mid n \ge 1$  is generated from variable A & zero or more b's are generated from variable B. Now we need to write the productions for A & B, which can be written as:

$$A \rightarrow aAb|ab$$
  
 $B \rightarrow bB|E$ 

$$A \rightarrow aAb|ab$$

4 tuple system! 
$$V = \{S, A, B\}$$
  $P = \{S \rightarrow aAB\}$   
 $T = \{a,b\}$   $P = \{S \rightarrow aAB\}$   
 $A \rightarrow aAb|ab\}$   
 $B \rightarrow bB|E$ 

The language tells that for every a, there needs to be two b's. This is achieved by adding one or more b at the end.

$$S \rightarrow E$$
 $S \rightarrow aSbb$ 

Henal grammar can be welten as

$$S \rightarrow \epsilon |aSbb|$$

$$G = (V,T,P,S) \Rightarrow V = \{S\}$$

$$T = \{a,b\}$$

$$P = \{S \rightarrow \epsilon |aSbb\}$$

$$S = \{S\}$$

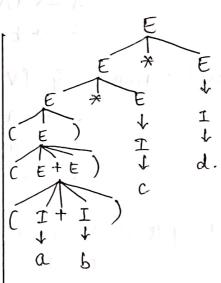
2. Given the following grammar

$$E \rightarrow I \mid E + E \mid E^* E \mid (E)$$

$$I \rightarrow a \mid b \mid c \mid d$$

Obtain the derivation tree for the strings (a+b)\*c\*d & a+b\*c and parse tree for each derivation.

Hower 
$$(afb)^*c^*d$$
  
Leftmost delivation:  $E \rightarrow E^*E$ 



A,B

$$A \rightarrow OA2$$

$$B \rightarrow 1B$$

## Resulting productions

$$G = (V_1 T_1 P_1 S) \Rightarrow V = \{S, A, B\}$$

$$T = \{0, 1, 2\}$$

$$S = \{S\}$$

$$P = S \rightarrow BAAB|AAB|BAB|BAA|AAB|AB|BA|BB|AA|B|A$$
  
 $A \rightarrow 0A&|02$   
 $A \rightarrow &A0|20$   
 $B \rightarrow AB|B|A$   
 $B \rightarrow 1B|1$ 

4. Design a PDA to accept the language  $L = \{a^nb^{dn} \mid n \ge 1\}$ sucr Languages accepted:  $L = \{abb, aabbbb....\}$ .

Transitions.

$$\delta(90, a, E) = (90, a)$$
  
 $\delta(90, a, a) = (90, aa)$   
 $\delta(90, b, a) = (91, aa)$   
 $\delta(91, b, a) = (91, a)$   
 $\delta(91, b, a) = (91, a)$   
 $\delta(91, b, a) = \delta \rightarrow \text{leads fo } E \Rightarrow (92, E)$   
 $\delta(93, E, E) = (93, E)$ .  
 $a, E|a$   
 $a, a|aa$ 

To accept string: abb.  $\delta(90, a, E) \vdash (90, a)$   $\delta(90, a, b) \vdash (99, a)$   $\delta(91, b, a) \vdash (92, E)$   $\delta(92, E, E) \vdash (93, E)$ Accept string

To reject

Thing: ab.  $\delta(90, a, E) \models (90, a)$   $\delta(90, b, a) \vdash (99, a)$ There are no more b's to pop a.

Therefore we reject this string

5. Design a turing machine to accept the following language  $L = \{0^n \pm^n \mid n \geq 1\}.$ 

Step1:- In state 90, replace 0 by X. Change the state to 9, Move to right  $S(90,0) = (9,X,R) \rightarrow XXXQYYII$ 

Stepa:- Replace 0 by 0 or Y by Y and do not change the state- Move to right  $S(9_1,0) = (9_1,0,R)$   $S(9_1,Y) = (9_1,Y,R) \implies XXXOYY 11$   $19_1$ 

steps:-18 input is 1, replace 1 by Y, change state to  $q_2$ . Move to left  $\delta(q_1,1)=(q_2,Y,L) \Rightarrow XXXOYYY1$ 

Stepq: Replace Y by 0, 0 by 0 & move pointer to left  $\delta(9a, Y) = (9a, Y, L)$   $\delta(9a, Y) = (9a, 0, L) \Rightarrow XXXOYYY1$ 

Step 5:- Replace X by X. Change state to qo, Move to right  $\delta(92, X) = (90, X, R) \rightarrow XXXOYYY1$ 90.

Repeating-the steps from 1 to 5, the resulting expression is

XXXXYYYY

step 6:- 18 the scanned symbol is y'after x, then there are no 0's left. 18 there are no 0's. there are no 4's. Change the state to 9.3. Replace Y by Y & move postner to regent

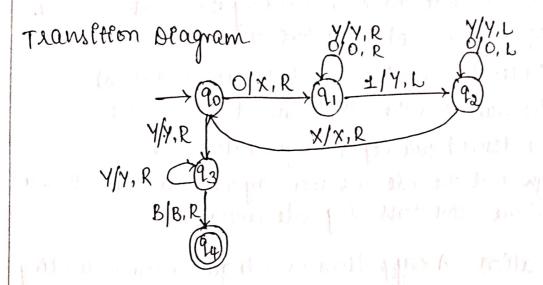
 $6(90,Y) = (93,Y,R) \Rightarrow XXXXYYYYY$ 

18 there are any 1's, It can be replaced by using 1-5 steps.  $\delta(93.Y)=(9.3,Y,R)$ 

XXXXYYYYB  $1_{9.3}$ B Endicates a blank state which is the end of the string  $\delta(9.3, B) = (9.4, B, R) \rightarrow XXXXYYYYBB$   $1_{9.4}$ 

 $M = (0, \Sigma, \Upsilon, \delta, 90, B, F) \Rightarrow 0 = \{90, 91, 92, 93, 94\}$   $\Sigma = \{0, 1\}$   $\Upsilon = \{0, 1, X, Y, B\}$   $90 > \{90\}$   $8 = \{B\}$   $F = \{94\}.$ 

ď	Tape Symbols (7)				
statis	0	1	X	Y	В
90	(9,, x, R)	4	. 1. 1. 1. 1. 1	(93,Y,R)	
91	(91, O, R)	(92, Y, L)	7	(91, Y, L)	
92	(9a,0,L)	redrict t	(90, X, R)	(9a, 4, L)	
9.3		1 - 1 - 1 - 1	1	(93,4,R)	(94,B,R)
94	1/11 - 1-1	- 1 - 0 (	y) weith of	a dja	1



6. Descuss varlous éssues en the design of vole generator.

Answer > Input to the code Generator: The Enput to the code generator is the Entermediate representation of the source program produced by the front-end, along with Enformation in the symbol table that is used to determine the address.

The many choices for the IR include 3 address representations, which include postfix includes, bytecodes etc.

→ The target Program: The instruction set architecture of the target machine has a significance/impact on the difficulty of a good code generator that produces high quality machine code. The most common architectures are RISC (Reduced Instruction Set Computer), CISC (Complex Instruction Set (Architecture) Computer), and stack based

RISC machine hasmany registers, there-address instructions, slimple addressing modes he simple instruction set. In construct CISC has been registers, two address instructions, variety of addressing modes he fushwetton side effects.

In a stack-based machine, operations are done by pushing operands onto a stack a then performing the operations on the operands at the top ob the stack.

-> Instruction Selection: The code generator must map the IR program into a code sequence that can be executed by the target machine. The complexity is determined by factors such as

\* The level of IR (Intumediate Representation).

\* The nature of the instruction-set architecture

\* The destred quality of the generated code

18 the IR is high level, the code generator may translate each IR into a sequence of machine instructions using code templates.

→ Regester Allocation: A key problem in code generation is deciding what values to hold in what registers. Registers are the Eastest computational units on target machines.

The use of registers is often develded ento a subproblems

\* Righter allocation, during which we select the set divariables that will reside in registers at each point in the program

\* Register assignment, during which we plak the specific register that a vailable who reside in.

Hending an optimal assignment of registers to variables is difficult even with single-register machine. Mathematically, the problem is NP-compute.

4. Défine shift-reduce parser explaîn êts action à conflict by taking an example

Answer shift-reduce parser is a form of bottom up paising in which a stack holds grammar symbols and an input symbol is held by the buffer to be parsed. The paise tree is constructed from leaf to the root

ACTION: The ACTION function takes arguments as state i and a terminal a (or \$, the input end marker)

The value of ACTION [i; a] can have 4 forms

-> Sheft - Shefts the Enput to stack

- -> Reduce Reduce the portion to non-terminal symbol
- -> Accept Accepts the input & finishes parsing
- -> Error Olscovers the error Estakes cornective actions

CONFLICT: - The CONFLICT in shift-reduce passing occurs when the parser can't decide whether to shift or reduce (OR) it occurs in a state that requests both shift is reduce action.

Consider the example:  $S \rightarrow TL$   $T \rightarrow \text{ entitions}$   $L \rightarrow L, \text{ edited}.$ 

Input buffer	Pauling Action
ent ed, ed; \$	Parsing Action
id, Id; \$	Reduce T→int
ld, ld; \$	shift
, id; \$	Reduce L→ Pd
	shift
id; \$	suft
; \$ 1	Reduce S-TL
\$	Accept.
	<pre>fnt fd, fd; \$   id, fd; \$   id, fd; \$   id; \$   , id; \$   , id; \$   id; \$   id; \$</pre>

- 8. a. Construct DAG for the expression at b\*(a+b)+c+d b. Give SDD of simple calculator
- Answera. DAG-Directed Acyclic Graph!—It is a directed graph that does not have any cycles. In DAG, each node represents an operation or a variable and the edges represent the dependencies

DAG for atb\*(atb) + c+d

3 different Acyclic Graphs can be drawn

b. Production rules L→E n E -> EI+T E -> T T -> TXF T -> F/  $F \rightarrow (E)$ Annotated Parse tree 1979 pollungi generalistica E-Val=15 T. Val = 15

Production rules

L 
$$\rightarrow$$
 E  $n$ 

E  $\rightarrow$  E<sub>1</sub>+T

E  $\rightarrow$  Cal = E<sub>1</sub>  $\rightarrow$  Cal + T<sub>1</sub>  $\rightarrow$  Cal

T  $\rightarrow$  T<sub>1</sub>\*F

T  $\rightarrow$  T<sub>1</sub>  $\rightarrow$  F

T  $\rightarrow$  CE

F  $\rightarrow$  Cal

F  $\rightarrow$  Cal

C  $\rightarrow$  Cal

C  $\rightarrow$  Cal

F  $\rightarrow$  Cal

C  $\rightarrow$  Cal

C

L. val = 19 2911, NOHAUL E. val = 19 T. val = 4 MYLL - BLYF 1. 11 F. Val = 4 T. val=3 \* F-val=5 diget-urval=4 Fival=3 digit-urval=5 digit-uxval =3

9. Translate the assignment a=bx-c+bx-c into three address code & quadruples

Answer Three Address code

Quadrupus

$$(1, -, c, t_1)$$

10.

Give the L-attributed SDD for simple desk calculator & draw the annotation parse the & dependency graph for the expression 4+6\*2n.

Answer Production rules

$$T \rightarrow F$$

Semantic Rules

Annotated parse free

T-val= 6 tan E-val=4 F-val=an T-val=6 id-val=an Dependency graph

