Task-2

Figure:1. Schematic Diagram of Traffic light of Aurdino in KiCad

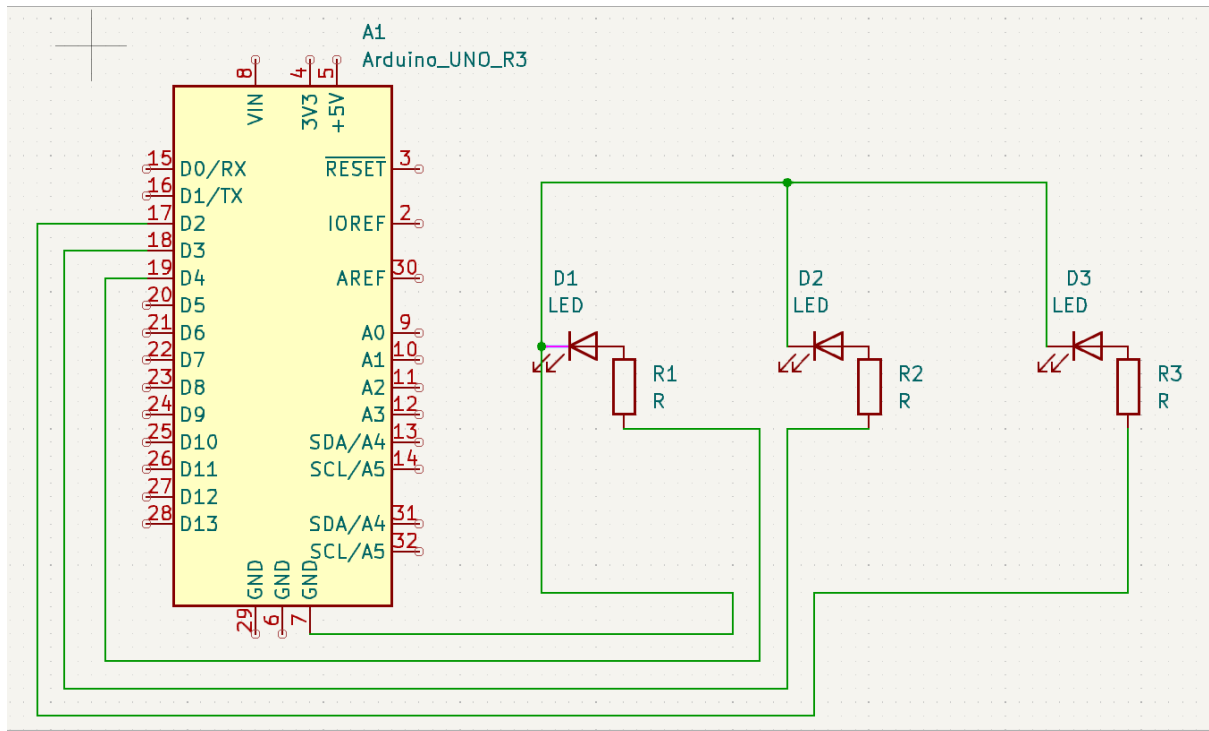**Figure:2.PCB with no DRC error**
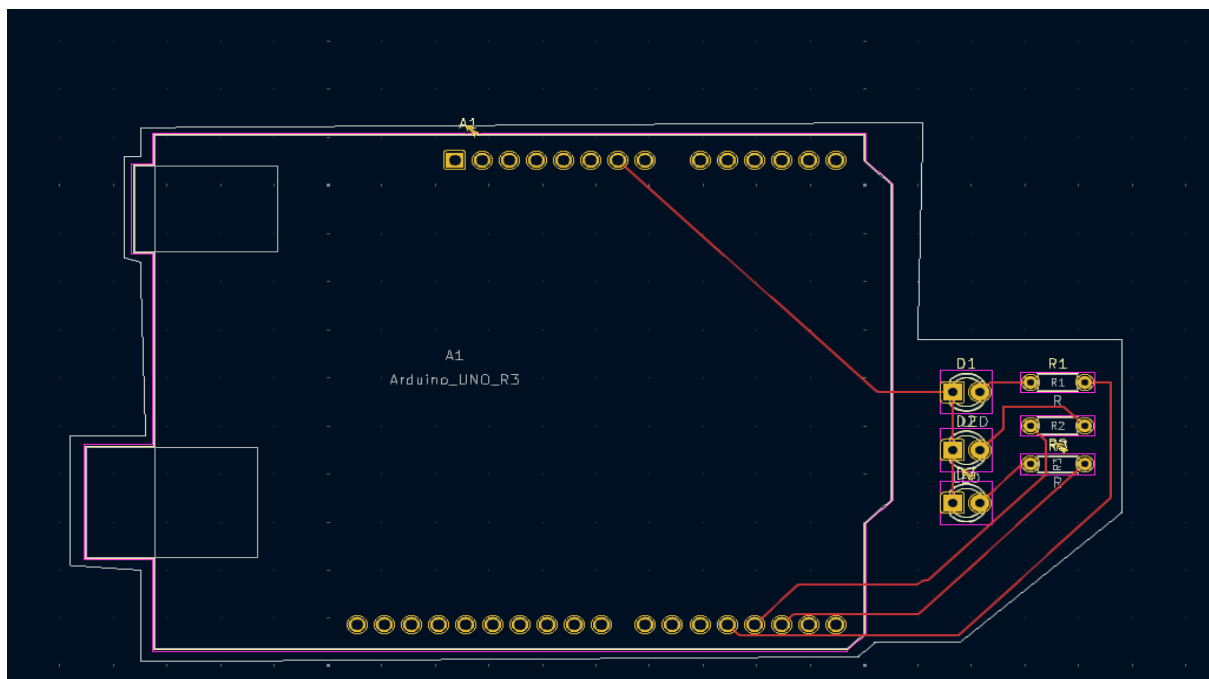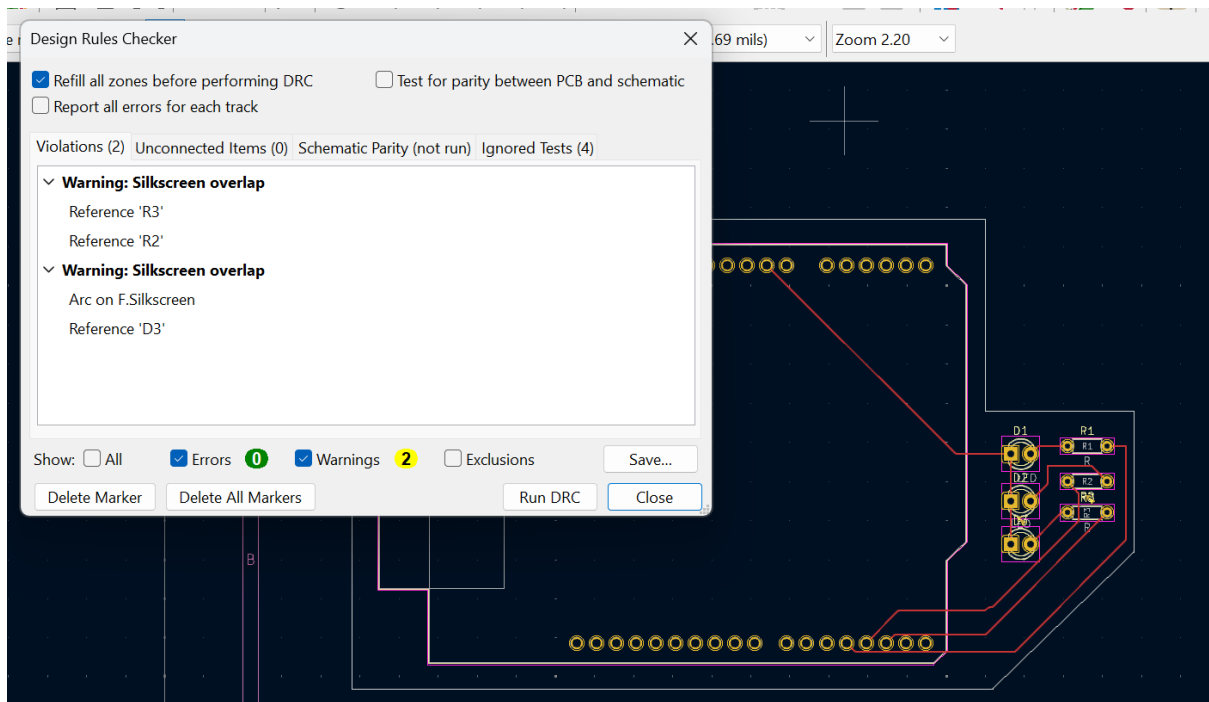
Figure: 3.DRC Check

Figure:.4.3D View of Traffic light PCB