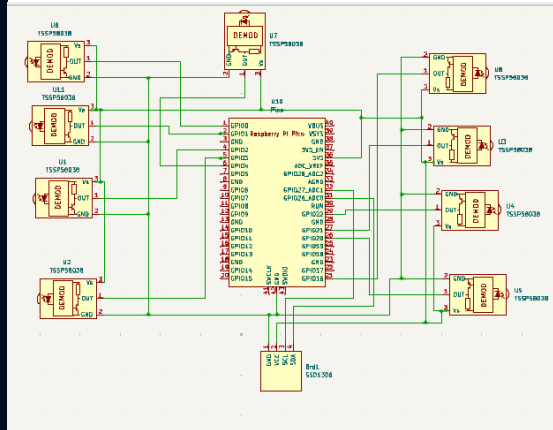
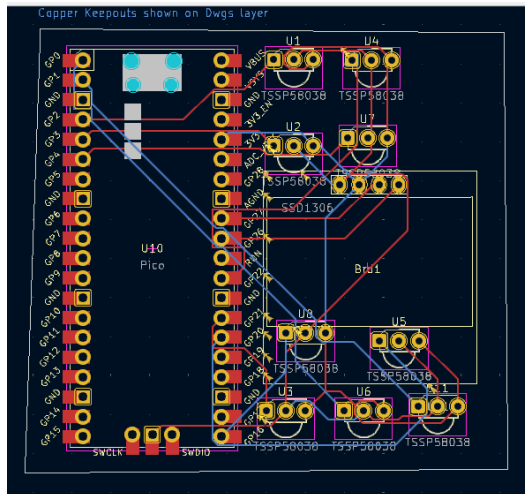
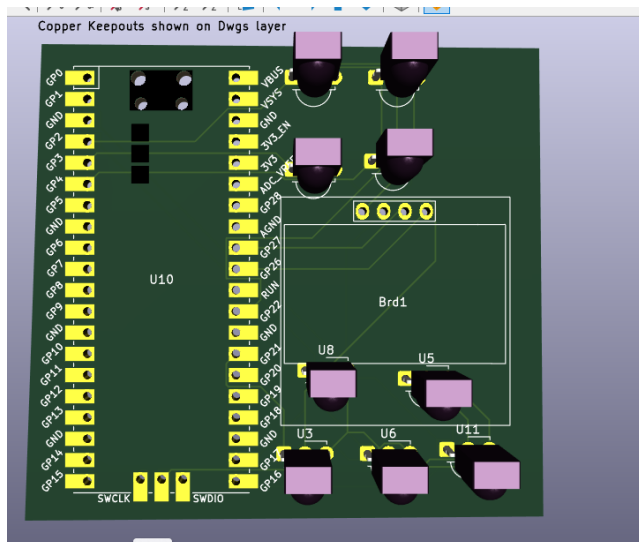


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Smart Lift system PCB simulation using kicad





☒ Refill all zones before performing DRC ☐ Test for parity between PCB and schematic
☐ Report all errors for each track

Violations (0) Unconnected Items (0) Schematic Parity (not run) Ignored Tests (4)

Show: ☐ All ☒ Errors 0 ☐ Warnings 15 ☐ Exclusions

Delete Marker Delete All Markers Run DRC Close