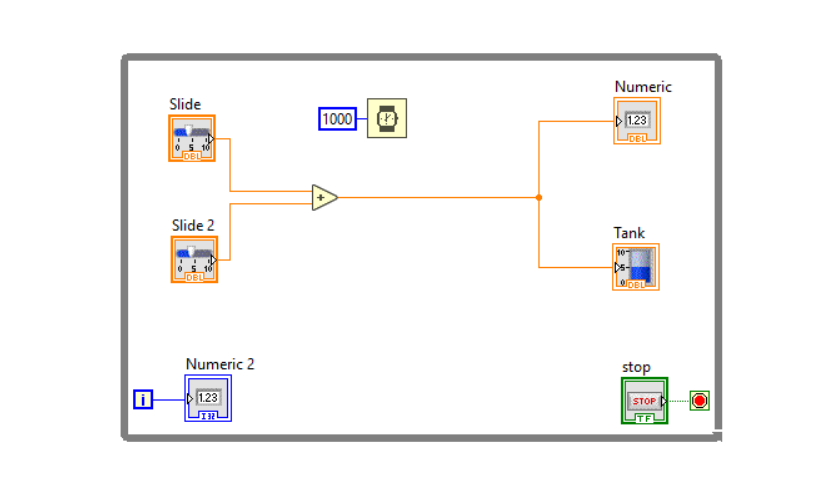
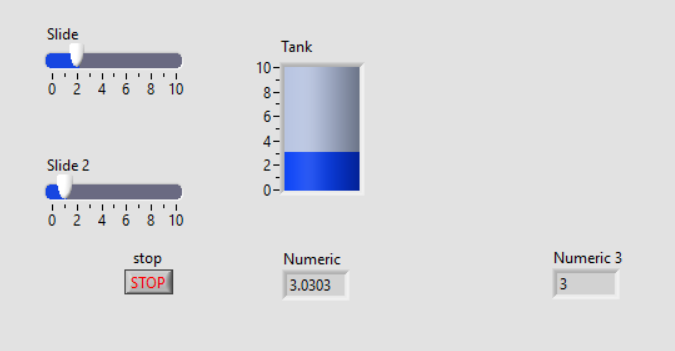
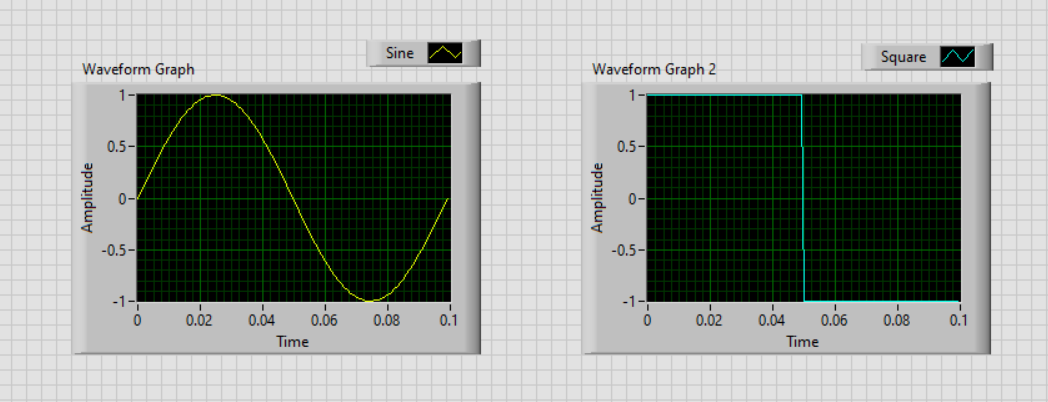
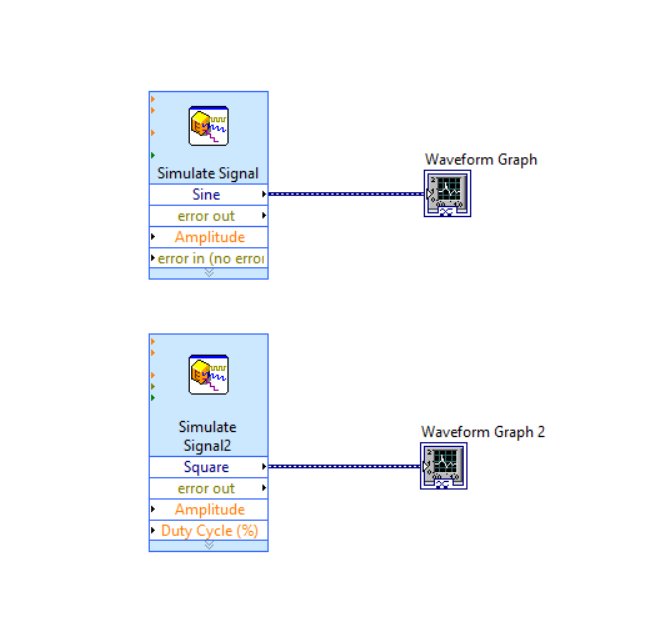
SIMULATION LAB EXPERIMENT-7

Simulation 1 (While loop)



Simulation 2 (signal Generation)





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