

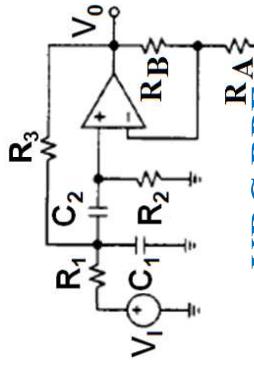
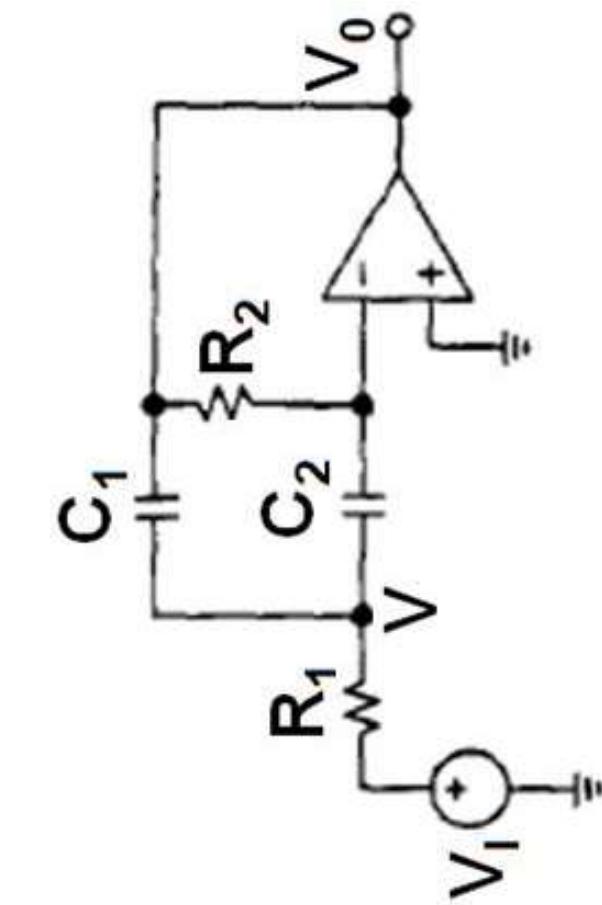
# LIC: LECTURE ACTIVE FILTERS

- Multiple Feedback Filters
  - (i) Concept
  - (ii) Multiple Feedback based Band Pass filter
  - (iii) Multiple Feedback based Low Pass filter
  - (iv) Example

[1] Franco, Sergio. Design with operational amplifiers and analog integrated circuits. Vol. 1988. New York: McGraw-Hill.

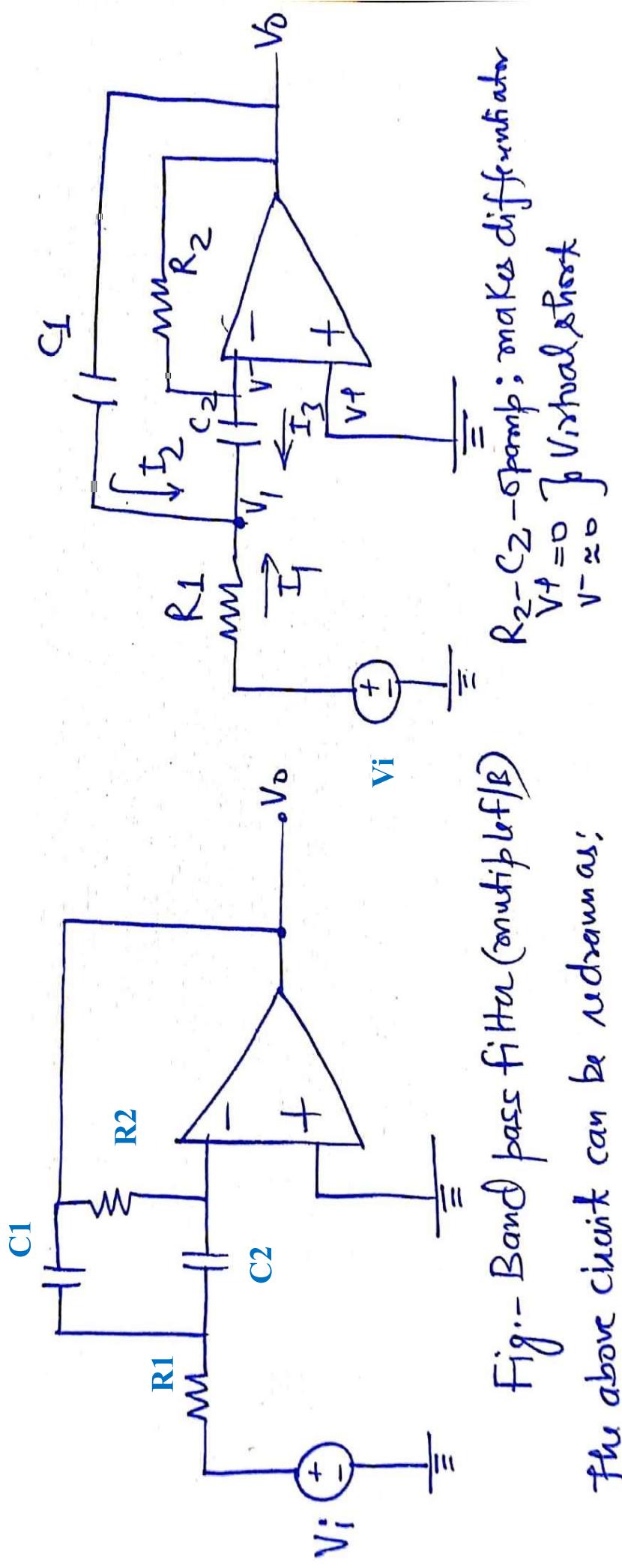
# Multiple Feedback Filters

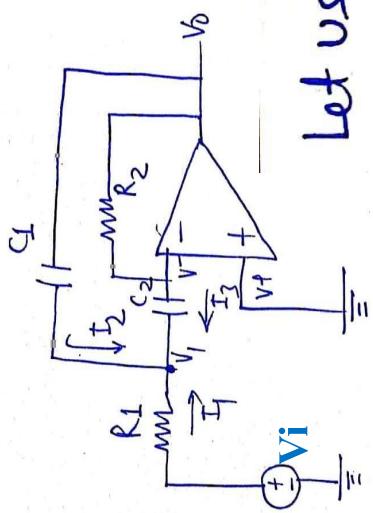
- Multiple Feedback Filters utilizes more than one feedback path.
- Unlike their counterparts, configure the op-amp for a finite gain  $K$ , multiple feedback filters exploit the full open loop gain and are also referred to as infinite gain filters.



KRC BPF

# Multiple Feedback based Band Pass Filter

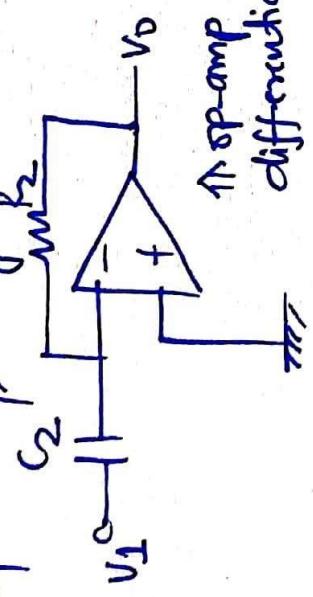




## Multiple Feedback based BPF

Let us analyse  $C_2 - R_2$  op-amp

separately, we get:



KCL at  $V_1$  in fig. 1, we get:

$$I_1 + I_2 + I_3 = 0$$

$$\frac{V_{in} - V_1}{R_1} + \frac{V_o - V_1}{1/sC_1} + \frac{0 - V_2}{1/sC_2} = 0$$

$\uparrow$  op-amp  
differentiator

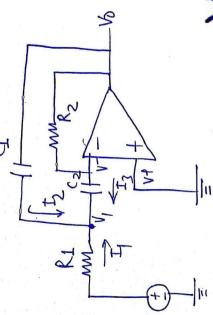
$$\frac{V_{in}}{R_1} + sC_1 V_o - V_1 \left( \frac{1}{R_1} + sC_1 + sC_2 \right) = 0$$

$$\text{from (1), } V_1 = \frac{-V_0}{sR_2 C_2}$$

$$\frac{V_o}{V_1} = \frac{-R_2}{sC_2} = \frac{-R_2}{1/sC_2} = sR_2 C_2$$

$$\boxed{V_o = -sR_2 C_2 V_1} \quad (1)$$

$$\frac{V_{in}}{R_1} + sC_1 V_o + \frac{V_o}{sR_2 C_2} \left[ \frac{1 + sC_1 R_1 + sC_2 R_1}{R_1} \right] = 0$$



# Multiple Feedback based BPF

$$\frac{V_{in}}{R_1} + SC_1V_o + \frac{V_o}{SR_2C_2} \left[ \frac{1 + SC_1R_1 + SC_2R_2}{R_1} \right] = 0$$

$$-\frac{V_{in}}{R_1} = \left[ SC_1 + \frac{1}{SC_2R_2} (1 + SC_1R_1 + SC_2R_2) \right] V_o$$

$$V_{in} = -R_1 \left[ \frac{S^2 R_1 R_2 C_1 C_2 + 1 + SC_1 R_1 + SC_2 R_2}{S R_2 C_2 R_1} \right] V_o$$

$$(1)$$

$$H(s) = \frac{V_o(s)}{V_{in}(s)} = \frac{-SR_2C_2}{1 + SC_1R_1 + SC_2R_1 + S^2C_1C_2R_1R_2}$$

$$(2)$$

Let  $s = j\omega$ , we get:

$$H(j\omega) = \frac{-j\omega R_2 C_2}{1 - \omega^2 R_1 R_2 C_1 C_2 + j\omega R_1 [C_1 + C_2]}$$

$$(3)$$

Comparing with standard transfer func. of B.P.F.

$$H(j\omega) = \frac{H_0 B_P \left( \frac{j\omega / \omega_0 \alpha}{1 - (\omega / \omega_0)^2 + \left( \frac{j\omega / \omega_0 \alpha}{\omega_0} \right)^2} \right)^{-N}}{1 - \left( \frac{\omega / \omega_0}{\omega_0} \right)^2 + \left( \frac{j\omega / \omega_0 \alpha}{\omega_0} \right)^2}$$

Comparing (3) & (4)

$$\omega^2 R_1 R_2 C_1 C_2 = \frac{\omega^2}{\omega_0^2}$$

$$\omega_0 = \frac{1}{\sqrt{R_1 R_2 C_1 C_2}}$$

$$f_0 = \frac{1}{2\pi\sqrt{R_1 R_2 C_1 C_2}}$$

\* Cut-off freq.

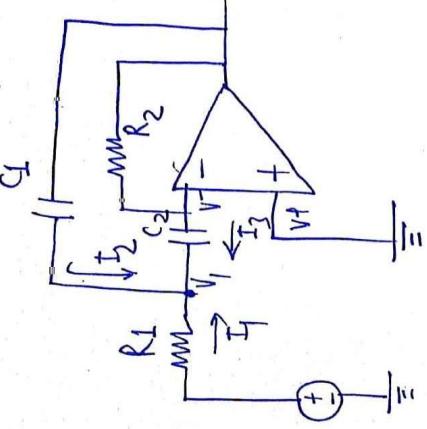
$$(2) j\omega R_1 [C_1 + C_2] = \frac{j\omega}{\omega_0 \alpha}$$

$$Q = \frac{1}{\omega_0 R_1 [C_1 + C_2]}$$

$$\Omega = \frac{\sqrt{R_1 R_2 C_1 C_2}}{R_1 [C_1 + C_2]}$$

$$\Omega = \frac{\sqrt{R_2/R_1}}{\sqrt{\frac{C_1}{C_2}} + \sqrt{\frac{C_2}{C_1}}}$$

$$(3) \text{ similarly: } H_0 B_P = \frac{-R_2/R_1}{1 + C_1/C_2}$$



## Multiple Feedback based BPF

If substitute  $C_1 = C_2 = C$  then

$$W_0 = \frac{1}{\sqrt{R_1 R_2 \cdot C}} *$$

$$Q = 0.5 \sqrt{\frac{R_2}{R_1}} *$$

$$\Omega^2 = (0.5)^2 \frac{R_2}{R_1} \rightarrow R_2 = R_1 \Omega^2 \cdot 4$$

$$W_0^2 = \frac{1}{R_1 R_2 C^2} \rightarrow \frac{1}{W_0^2 C^2 R_1} = R_1 \Omega^2 \cdot 4$$

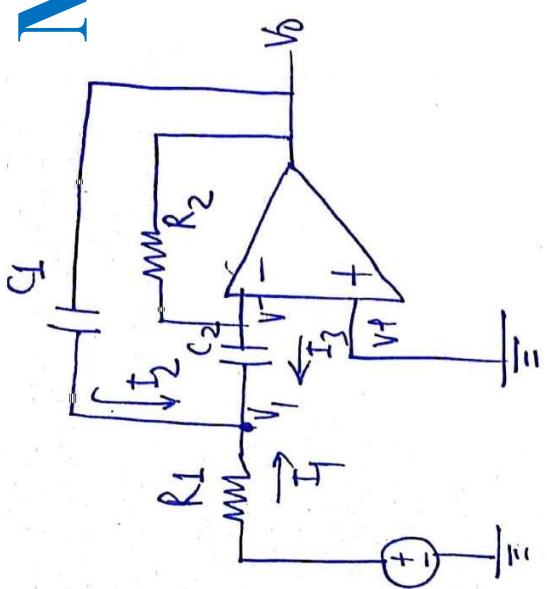
$$\text{So, } R_1 = \frac{1}{2 W_0 C} , \text{ similarly calculate } R_2.$$

$\Rightarrow$  Denoting resonance gain magnitude as  $H_0 = |H_{BPF}|$ , for simplicity we observe that it increases quadratically with  $Q$ . ( $H_0 = -2Q^2$ )

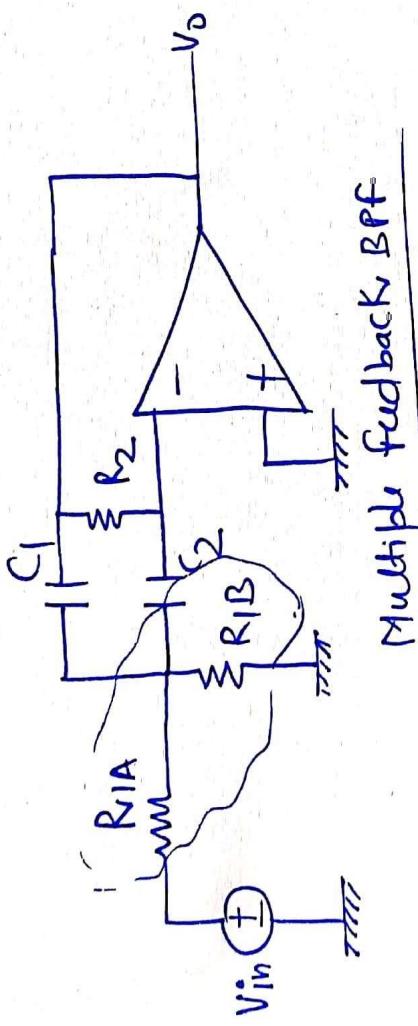
DESIGN EQUATIONS:

$$R_1 = \frac{1}{2 W_0 \Omega \cdot C}; R_2 = \frac{2 Q}{W_0 C}$$

$$R_1 = \frac{1}{2 W_0 Q \cdot C} \leftarrow \text{proof}$$



DESIGN: If we want  $H_0 < 2\Omega^2$ , we must replace  $R_1$  with a voltage divider as shown below:



Multiple feedback BPF

The design equations:

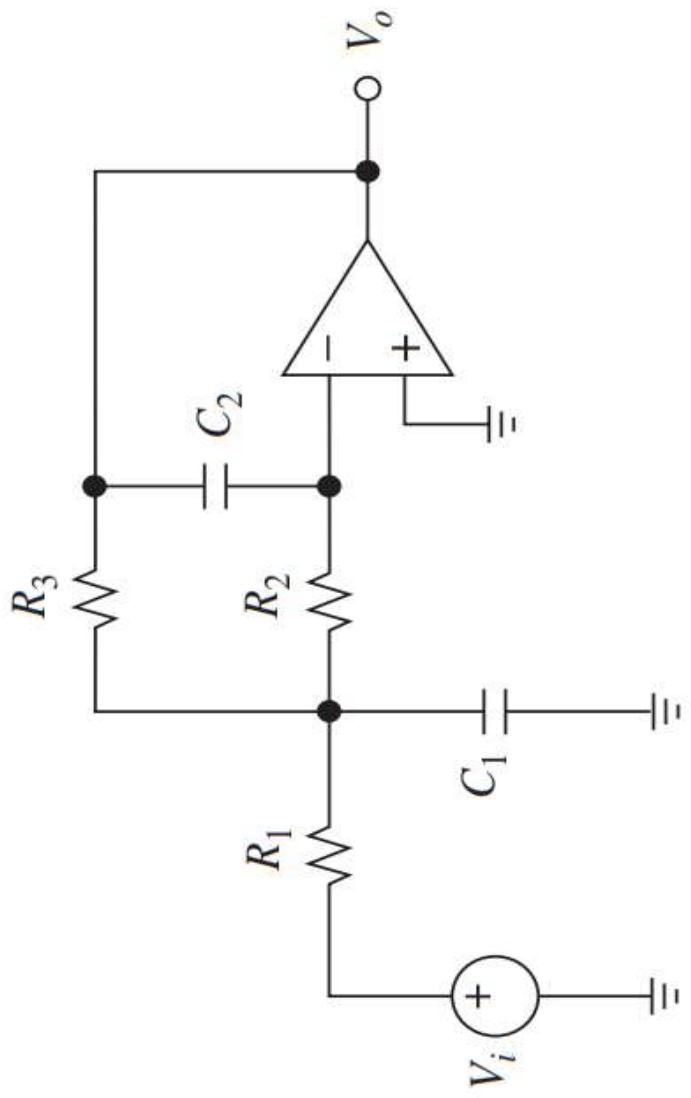
$$R_1 A = \frac{\Omega}{H_0 \omega C}$$

$$R_1 B = \frac{R_1 A}{\left[ \left( \frac{R_2 \Omega^2}{H_0} \right) - 1 \right]}$$

Problem: Design a multiple feedback band-pass filter with  $f_0 = 1 \text{ KHz}$ ,  $H_0 \text{BP} = 20 \text{ dB}$ ,  $\Omega = 1.0$ ,  $C_{\text{in}} = 1 \mu\text{F}$

# Multiple Feedback based Low Pass Filter

- The circuit consists of the low-pass stage  $R_1-C_1$  followed by the integrator stage made up of  $R_2, C_2$ , and the op amp.
- So, we anticipate a low-pass response. Moreover, the presence of positive feedback via  $R_3$  should allow for Q control.



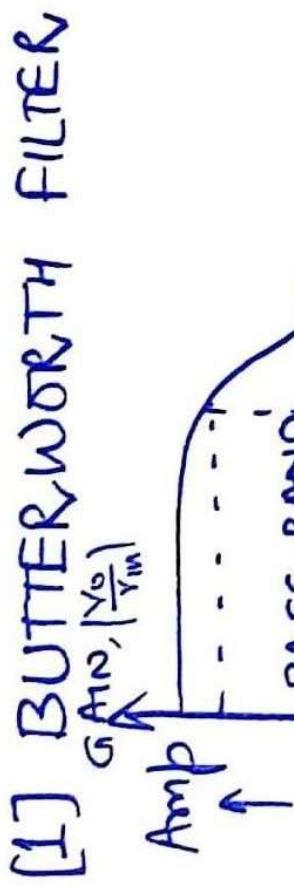
# LIC: LECTURE ACTIVE FILTERS

- FILTER APPROXIMATIONS:

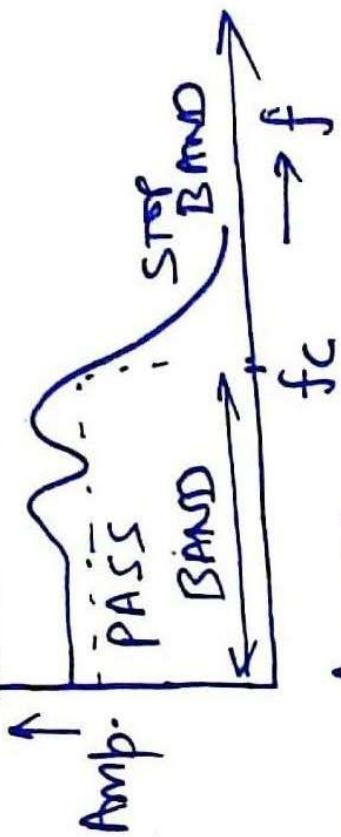
- (i) Introduction to different type of Filters for Approximation
- (ii) Butterworth Response
- (a) Filter Design Approximation
- (b) Design of First Order BW Filter
- (c) Design of Higher Order BW Filters & Examples

# INTRODUCTION: FILTER APPROXIMATIONS

## BUTTERWORTH, CHEBYSHEV, ELIPTICAL & BESSLE



## [2] CHEBYSHEV FILTER APPROXIMATION



- \* Maximally flat approximation

- \* FLAT BAND: flat response  
No ripples

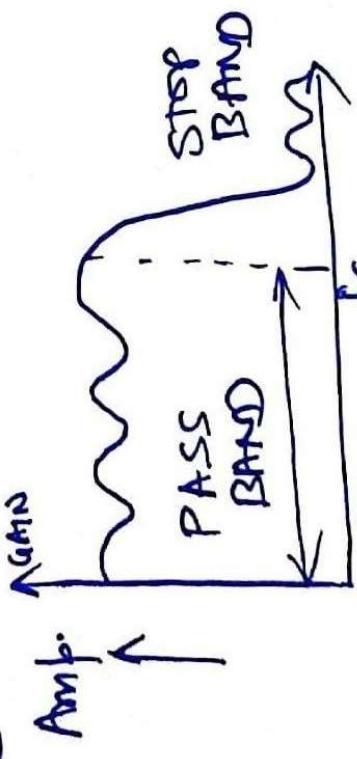
- \* Roll-off rate - 20 dB/decade
- \* Also, no ripple in stop band

- \* No of ripples =  $\frac{n}{2}$
- \* Ripple pass band
- \* Transition very fast

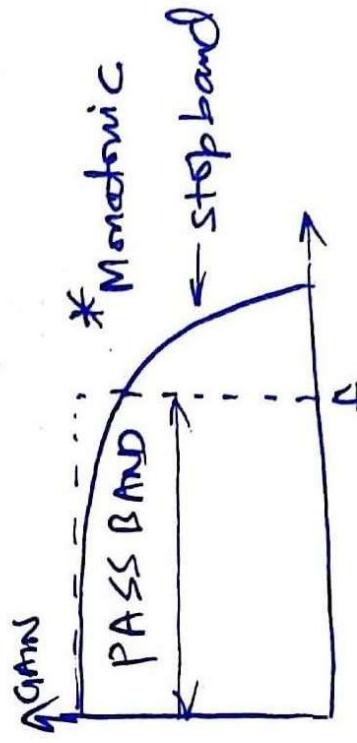
# INTRODUCTION: FILTER APPROXIMATIONS

## BUTTERWORTH, CHEBYSHEV, ELIPTICAL & BESEL

(3) ELLIPTICAL FILTER APPROXIMATION



(4) BESEL FILTER APPROXIMATION



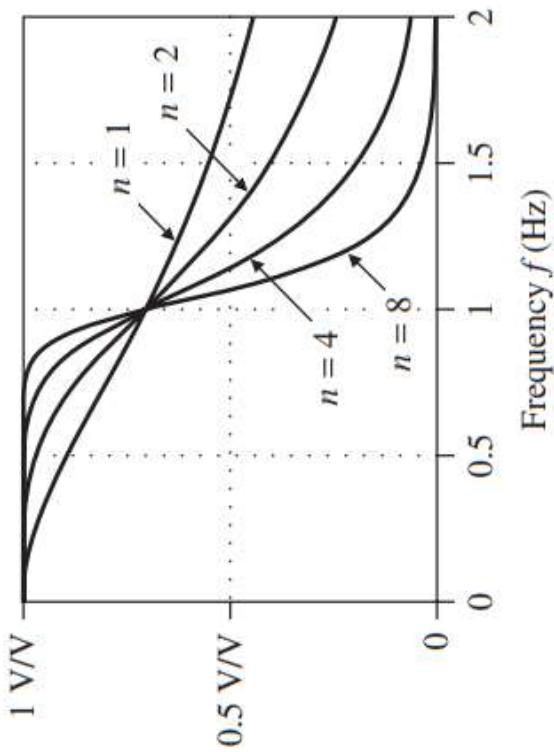
- \* No ripples in pass band & stop band.
- \* Phase - frequency relation linear (slow transition)
- \* Constant Delay (Slow transition)

# Higher order Butterworth Filter design

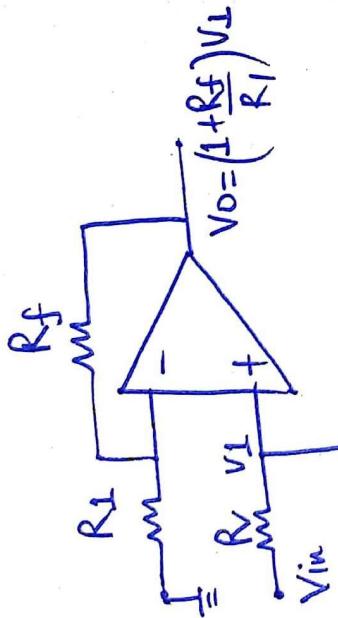
- The gain of the Butterworth approximation is

$$|H(j\omega)| = \frac{1}{\sqrt{1 + \epsilon^2(\omega/\omega_c)^{2n}}}$$

where  $n$  is the order of the filter,  $\omega_c$  is the cutoff frequency, and  $\epsilon$  is a constant that determines the maximum passband variation as  $A_{\max} = A(\omega_c) = 20 \times \log_{10} \sqrt{1 + \epsilon^2} = 10 \log_{10}(1 + \epsilon^2)$ . The first  $2n - 1$  derivatives of  $|H(j\omega)|$  are zero at  $\omega = 0$ , indicating a curve as flat as possible at  $\omega = 0$ . Aptly referred to as *maximally flat*, a Butterworth curve becomes somewhat rounded near  $\omega_c$  and rolls off at an ultimate rate of  $-20n$  dB/dec in the stopband.



# FIRST ORDER LOW PASS BUTTERWORTH FILTER APPROXIMATIONS



As per Butterworth approximation:  
for  $\xi_1 = 1$

$$|H(j\omega)| = \frac{1}{\sqrt{1 + (\omega/\omega_c)^2}}$$

for first order  $N = 1$

$$|H(j\omega)| = \frac{1}{\sqrt{1 + (\omega/\omega_c)^2}}$$

$$V_1 = \frac{X_C}{R + X_C} V_{in}$$

$$\left| \frac{V_0}{V_{in}} \right| = \frac{A_f}{\sqrt{1 + (\omega/\omega_c)^2}}$$

$$\left. \begin{array}{l} A_f = \frac{1 + R_f/R_1}{1 + j\omega/\omega_c} \\ \omega_c = \frac{1}{R_1 C} \end{array} \right\}$$

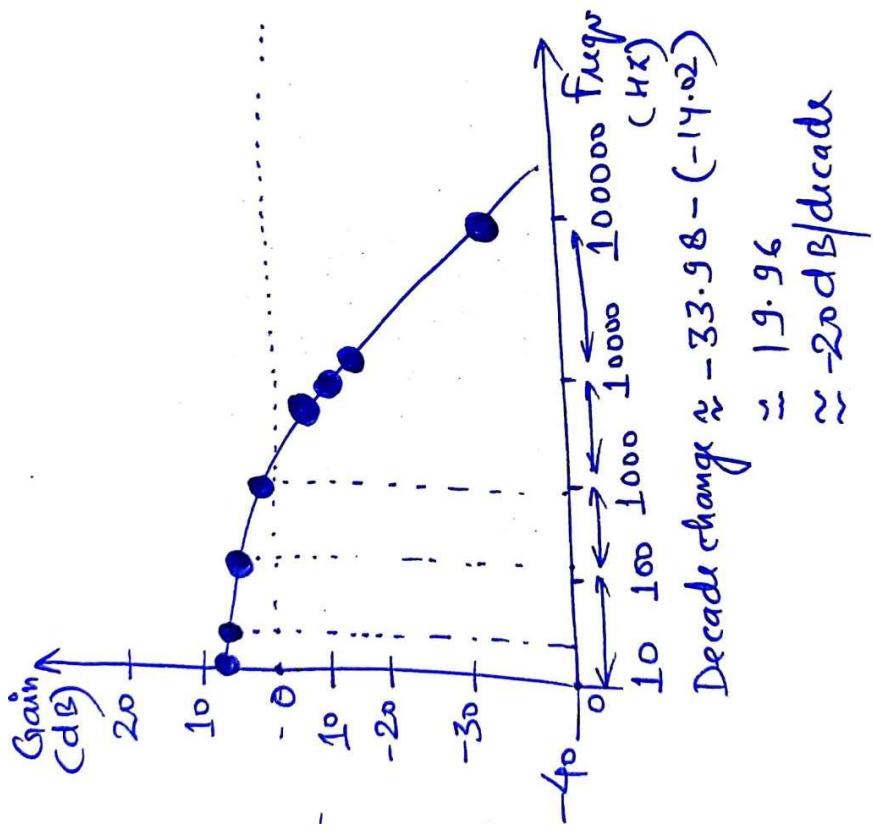
for  $f_c = 1KHz$ ,  $C = 0.01 \mu F$  &  $A_f = 2$   
Design Butterworth low pass filter

Assume -  
 $A_f = 1 + \frac{R_f}{R_1} = 2$        $R_f = R_1 = 10k\Omega$

$$R_1 = \frac{1}{2\pi f_c \cdot C} = 15.9 k\Omega$$

# FIRST ORDER LOW PASS BUTTERWORTH FILTER APPROXIMATIONS

F (Hz)	Gain ( $V_o/V_i$ )	Gain (dB)
10	2	6.02
20	1.99	5.98
200	1.96	5.85
700	1.64	4.29
1000	1.41	3.01
3000	0.63	-3.98
10000	0.2	-14.02
100000	0.02	-33.98



# HIGHER ORDER LOW PASS BUTTERWORTH FILTER APPROXIMATIONS

- General Low pass equation transfer function

$$H_{LP}(j\omega) = \frac{1}{1 - (\omega/\omega_0)^2 + (j\omega/\omega_0)/Q}$$

- $H(j\omega) = H_{oLP} * H_{LP}(j\omega)$

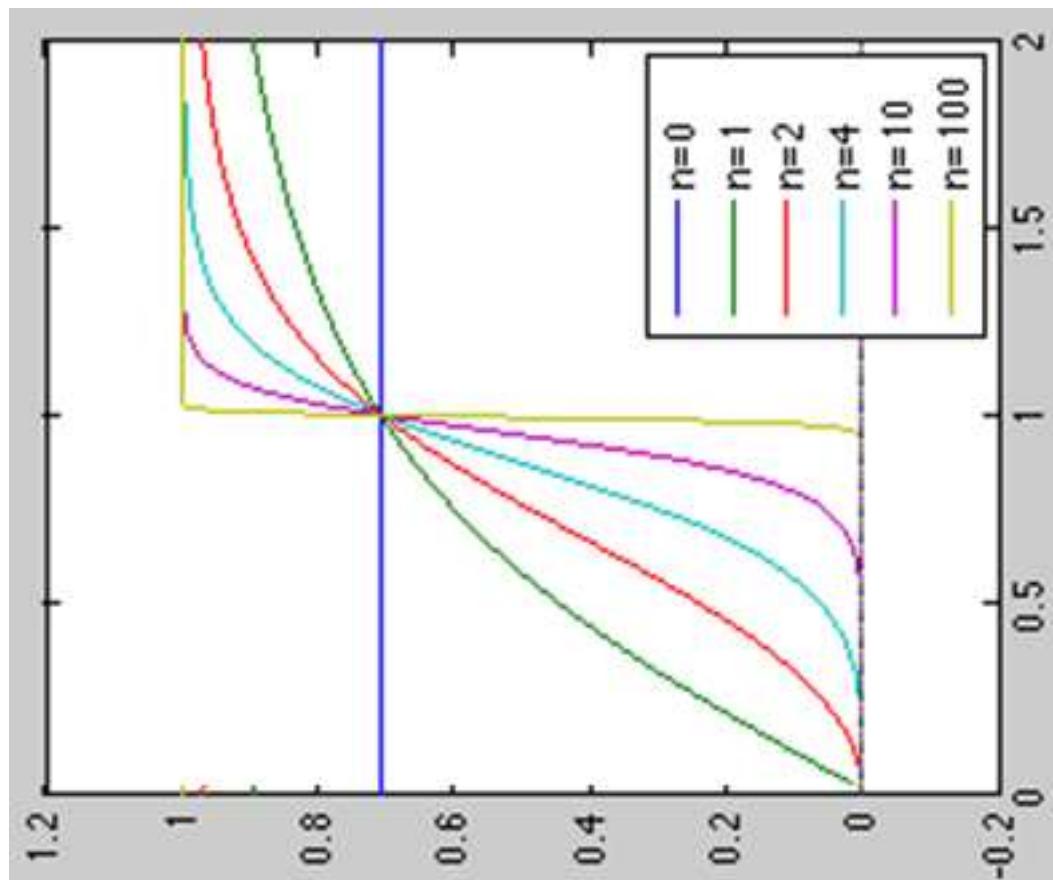
- Butterworth filter  $Q=0.707 = (1/\sqrt{2})$

$$H(j\omega) = \frac{K}{\sqrt{1 + \left(\frac{\omega}{\omega_0}\right)^{2n}}}$$

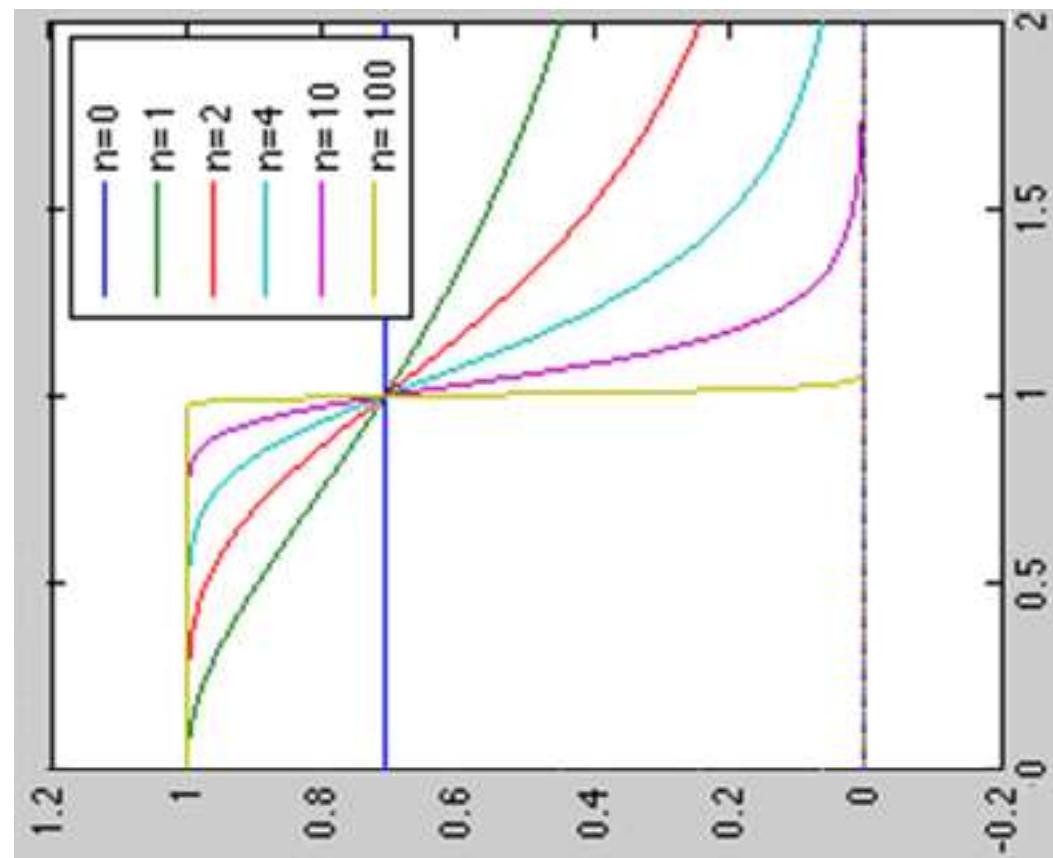
- Find out the amplitude ??

<b>n</b>	<b>Normalised Denominator Polynomials in Factored Form (Each coefficient=3-K)</b>
1	(1+s)
2	(1+1.414s+s <sup>2</sup> )
3	(1+s) (1+s+s <sup>2</sup> )
4	(1+0.765s+s <sup>2</sup> ) (1+1.848s+s <sup>2</sup> )
5	(1+s) (1+0.618s+s <sup>2</sup> ) (1+1.618s+s <sup>2</sup> )
6	(1+0.518s+s <sup>2</sup> ) (1+1.414s+s <sup>2</sup> ) (1+1.932s+s <sup>2</sup> )
7	(1+s) (1+0.445s+s <sup>2</sup> ) (1+1.247s+s <sup>2</sup> ) (1+1.802s+s <sup>2</sup> )
8	(1+0.390s+s <sup>2</sup> ) (1+1.111s+s <sup>2</sup> ) (1+1.663s+s <sup>2</sup> ) (1+1.962s+s <sup>2</sup> )
9	(1+s) (1+0.347s+s <sup>2</sup> ) (1+s+s <sup>2</sup> ) (1+1.532s+s <sup>2</sup> ) (1+1.879s+s <sup>2</sup> ) (1+1.879s+s <sup>2</sup> )
10	(1+0.313s+s <sup>2</sup> ) (1+0.908s+s <sup>2</sup> ) (1+1.414s+s <sup>2</sup> ) (1+1.782s+s <sup>2</sup> ) (1+1.975s+s <sup>2</sup> ) (1+1.975s+s <sup>2</sup> )

**Butterworth HPF**



**Butterworth LPF**



## Design a 2<sup>nd</sup> order Butterworth LPF with $f_c = 1\text{kHz}$

$$\frac{V_0}{V_I} = \frac{K}{(sCR)^2 + sCR(3-K) + 1}$$

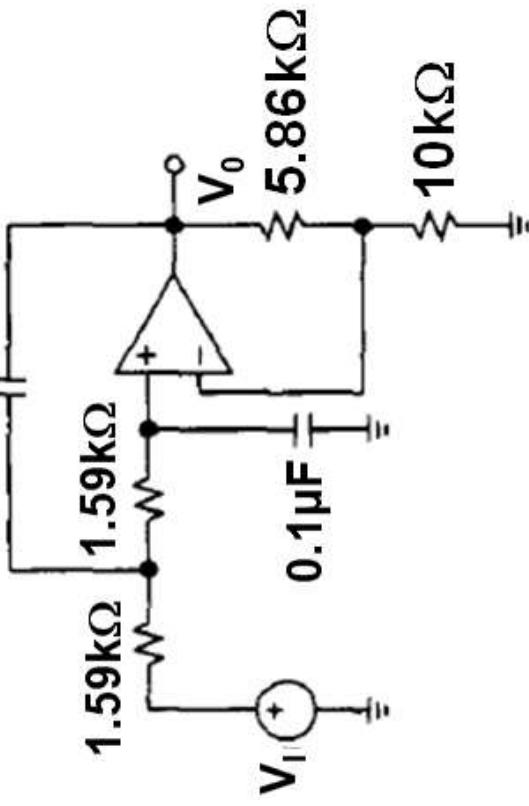
$$H(s) = \frac{K}{1 + R_1 R_2 C_1 C_2 s^2 + [(1-K)R_1 C_1 + R_1 C_2 + R_2 C_2]s}$$

$$\text{Let } C = 0.1\mu\text{F} \rightarrow \omega_0 = \frac{1}{CR} = \frac{1}{2\pi \times 1 \times 10^{-3} \times 0.1 \times 10^{-6}} = 1.59\text{k}\Omega$$

$$(1+1.414s+s^2) \rightarrow 3 - K = 1.414 \rightarrow K = 1.586$$

$0.1\mu\text{F}$

$$\Rightarrow \frac{V_0}{V_I} = \frac{1.586}{(sCR)^2 + 1.414sCR + 1}$$



$$K = 1.586 = 1 + \frac{R_B}{R_A} \Rightarrow R_B = R_A(K-1)$$

$$\begin{aligned} \text{Let } R_A &= 10\text{k}\Omega \\ \Rightarrow R_B &= 5.86\text{k}\Omega \end{aligned}$$



## Design a 4<sup>th</sup> order Butterworth LPF with $f_c = 1\text{kHz}$

$$\frac{V_0}{V_I} = \frac{K}{(sCR)^2 + sCR(3 - K) + 1}$$

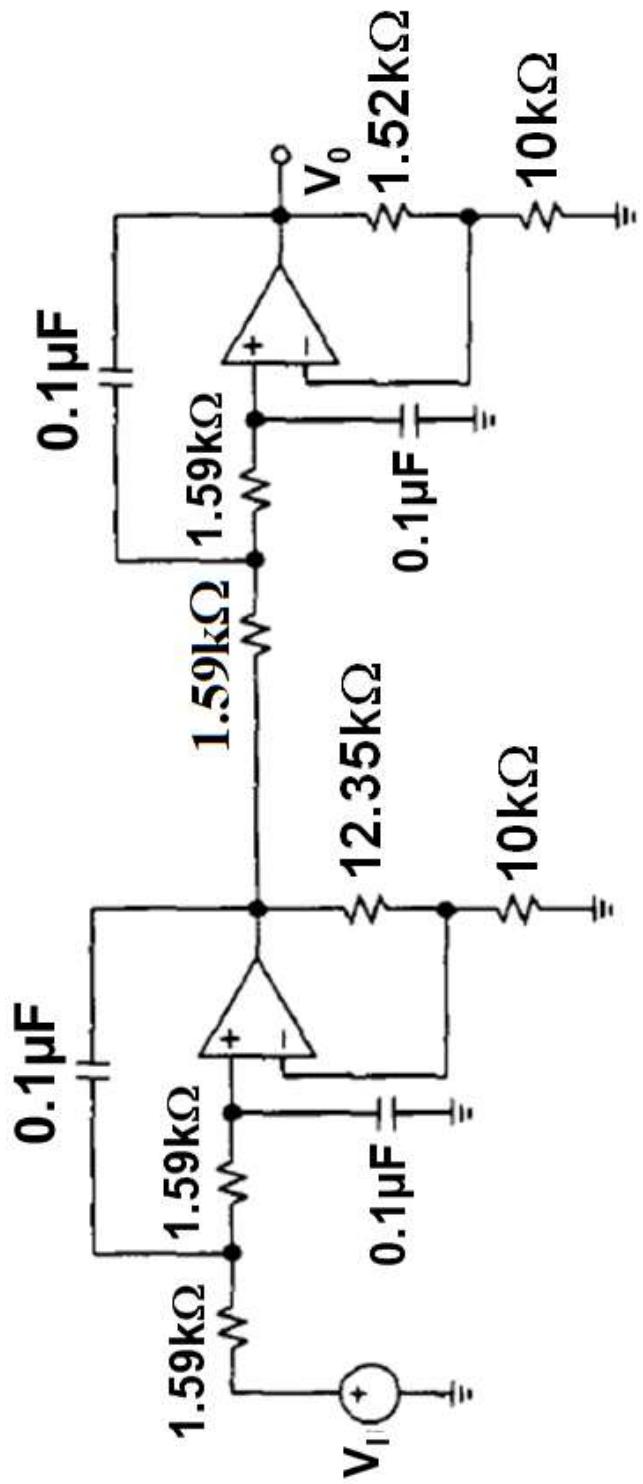
$$\begin{aligned} (1+0.765s+s^2)(1+1.848s+s^2) &\rightarrow 3 - K_1 = 0.765 \rightarrow K_1 = 2.235 \\ (1+0.765s+s^2)(1+1.848s+s^2) &\rightarrow 3 - K_2 = 1.848 \rightarrow K_2 = 1.152 \end{aligned}$$

$$\text{Let } C = 0.1\mu\text{F} \rightarrow \omega_0 = \frac{1}{CR} \Rightarrow R = \frac{1}{2\pi \times 1 \times 10^3 \times 0.1 \times 10^{-6}} = 1.59\text{k}\Omega$$

$$\begin{aligned} K_1 = 2.235 &= 1 + \frac{R_B}{R_A} \Rightarrow R_B = 1.235R_A & \text{Let } R_{A1} = 10\text{k}\Omega \\ &\Rightarrow R_{B1} = 12.35\text{k}\Omega \end{aligned}$$

$$\begin{aligned} K_2 = 1.152 &= 1 + \frac{R_B}{R_A} \Rightarrow R_B = 0.152R_A & \text{Let } R_{A2} = 10\text{k}\Omega \\ &\Rightarrow R_{B2} = 1.52\text{k}\Omega \end{aligned}$$

**Resulting gain K =  $K_1 \times K_2 = 2.575$**



# LIC: LECTURE

## Static and Dynamic Op Amp Limitations

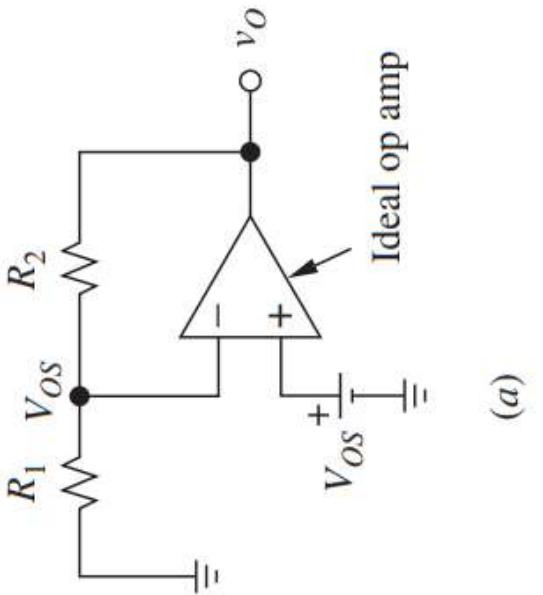
- INPUT OFFSET VOLTAGE:

- (i) Error Caused by  $V_{OS}$
- (ii) Effective CMRR: Impact of  $V_{OS}$
- (iii) Power Supply Rejection Ratio: Impact of  $V_{OS}$
- (iv) Examples

# POWER SUPPLY REJECTION RATIO: PSRR

- If we change one of the op amp supply voltages  $V_S$  by a given amount  $\Delta V_S$  the operating points of the internal transistors will be altered, generally causing a small change in  $V_o$ . By analogy with the CMRR, we model this phenomenon with a change in the input offset voltage.
- Which we express in terms of the power-supply rejection ratio (PSRR) as  $(1/\bar{PSRR}) \times \Delta V_S$

$$\frac{1}{\bar{PSRR}} = \frac{\partial V_{OS}}{\partial V_S}$$

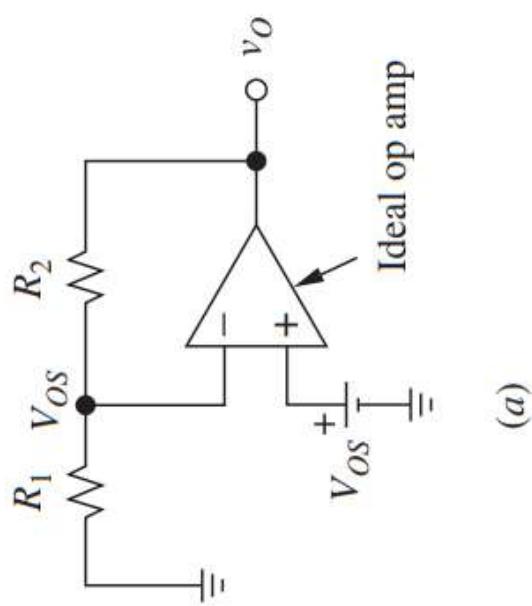


- The parameter  $1/\bar{PSRR}$  represents the change in  $V_{OS}$  brought about by a 1-V change in  $V_S$ , and is expressed in microvolts per volt. Like the CMRR, the PSRR deteriorates with frequency

# POWER SUPPLY REJECTION RATIO: PSRR

Some data sheets give separate PSRR ratings, one for changes in  $V_{CC}$  and the other for changes in  $V_{EE}$ . Others specify the PSRR for  $V_{CC}$  and  $V_{EE}$  changing symmetrically. The PSRR<sub>dB</sub> ratings of most op amps fall in the range of 80 dB to 120 dB. The devices of superior matching usually offer the highest PSRRs.

The 1/PSRR ratings for the 741C, which are given for symmetric supply changes, are 30  $\mu\text{V}/\text{V}$  typical, 150  $\mu\text{V}/\text{V}$  maximum. This means that changing, for instance, the supply voltages from  $\pm 15\text{ V}$  to  $\pm 12\text{ V}$  yields  $\Delta V_{OS} = (1/\text{PSRR}) \Delta V_S = (30 \mu\text{V})(15 - 12) = \pm 90 \mu\text{V}$  typical,  $\pm 450 \mu\text{V}$  maximum. The OP77 op amp has  $1/\text{PSRR} = 0.7 \mu\text{V}/\text{V}$  typical, 3  $\mu\text{V}/\text{V}$  maximum.

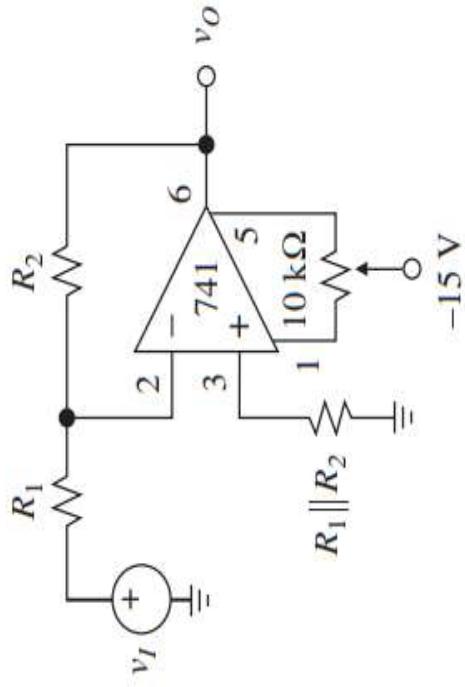


**EXAMPLE** A 741 op amp is connected as in Fig. 5.13a with  $R_1 = 100 \Omega$  and  $R_2 = 100 \text{k}\Omega$ . Predict the typical as well as the maximum ripple at the output for a power-supply ripple of 0.1 V (peak-to-peak) at 120 Hz.

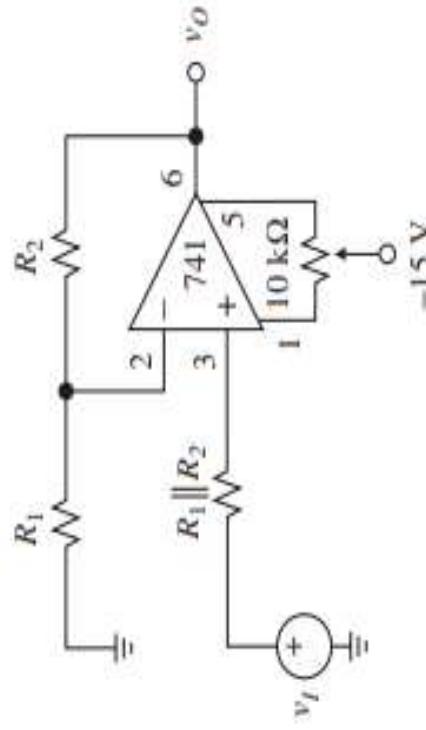
(a)

# INPUT OFFSET ERROR AND COMPENSATION TECHNIQUES

- Compensation for input offset voltage is required when we want to make it absolutely sure that output voltage is zero for a zero input voltage.
- 741-opamp provides offset compensation pins to nullify the input offset voltage [Pin 1 & 5]



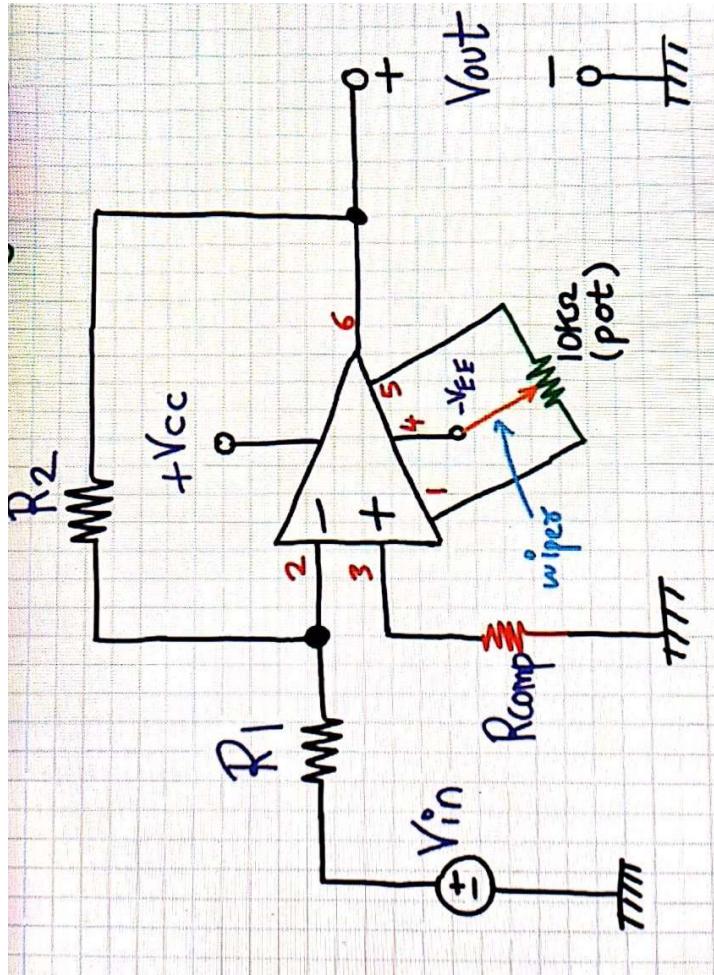
(a)



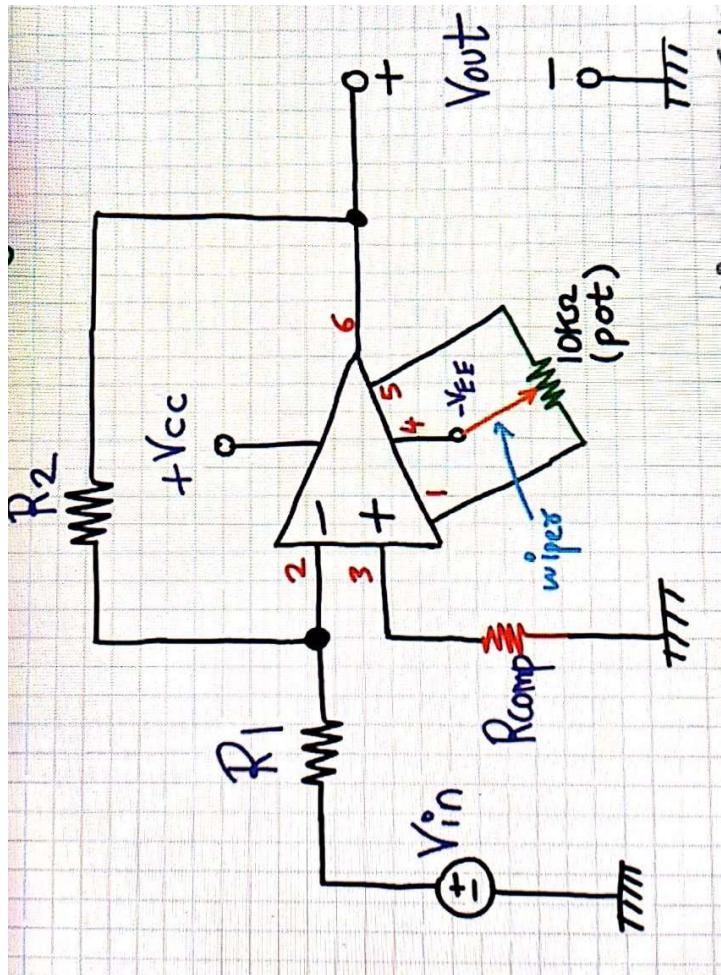
(b)

# INPUT OFFSET ERROR AND COMPENSATION TECHNIQUES

- Here, a  $10k\Omega$  potentiometer is placed across the offset null pins 1 & 5 and the wiper (variable terminal of pot) is connected to pin no. 4 (i.e. negative supply pin 4).
- The position of the wiper is adjusted to nullify the output offset voltage / output offset error.



# INPUT OFFSET ERROR AND COMPENSATION TECHNIQUES



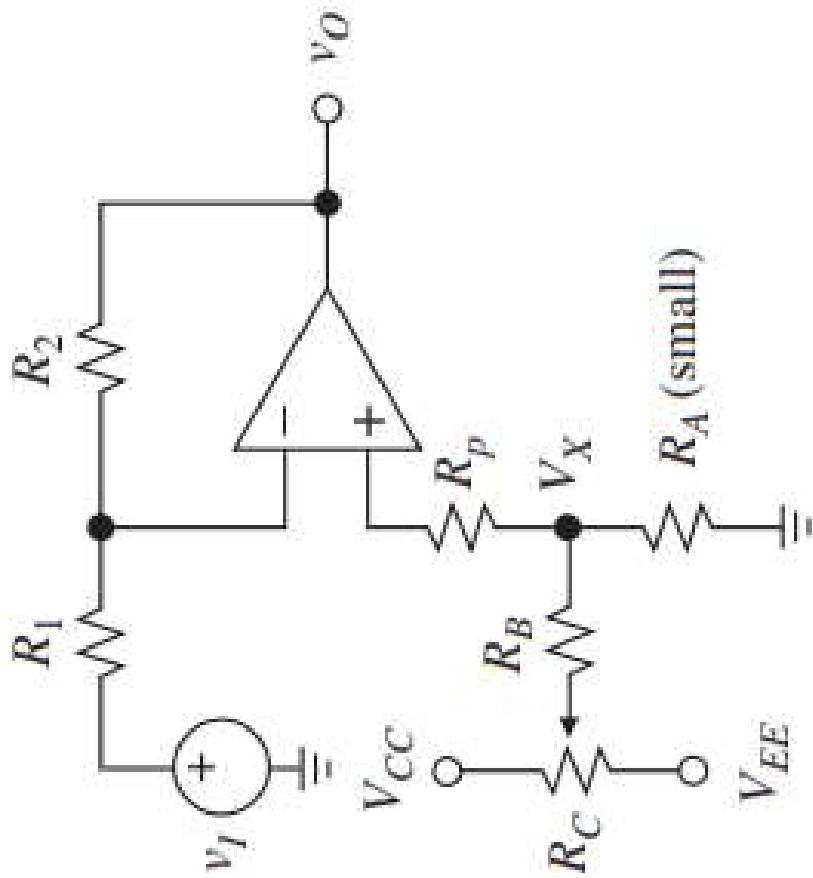
$$v_o = A_s v_I + E_O$$

$$E_O = \left(1 + \frac{R_2}{R_1}\right) [V_{OS} - (R_1 \parallel R_2) I_{OS}] = \frac{1}{\beta} E_I$$

Where  $A_s = -R_2/R_1$  for the inverting amplifier, and  $A_s = 1 + R_2/R_1$  for the noninverting one. We call  $A_s$  the signal gain to distinguish it from the dc noise gain, which is  $1/\beta = 1 + R_2/R_1$  for both circuits. Moreover,  $E_I = V_{OS} - (R_1 \parallel R_2) I_{OS}$  is the total offset error referred to the input, and  $E_O$  the total offset error referred to the output.

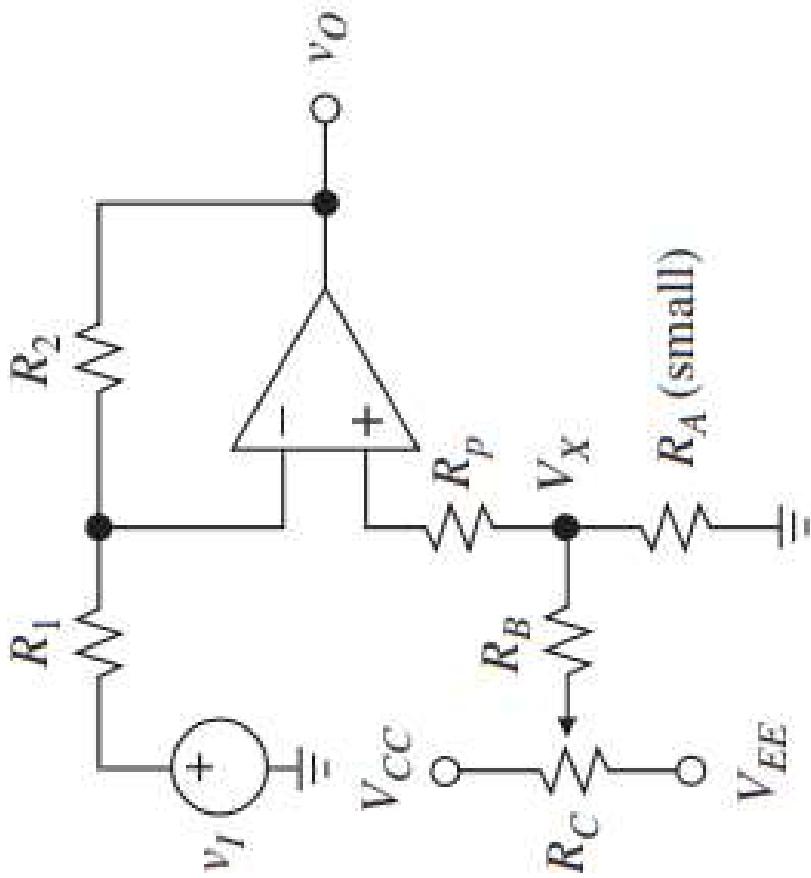
# INPUT OFFSET ERROR AND COMPENSATION TECHNIQUES: EXTERNAL NULLING

- External nulling is based on the injection of an adjustable voltage or current into the circuit to compensate for its offset error. This scheme does not introduce any additional imbalances in the input stage, so there is no degradation in drift, CMRR, or PSRR.
- The most convenient point of injection of the correcting signal depends on the particular circuit. For inverting-type configurations like the amplifier, we simply lift  $R_p$  off ground and return it to an adjustable voltage  $V_X$ .



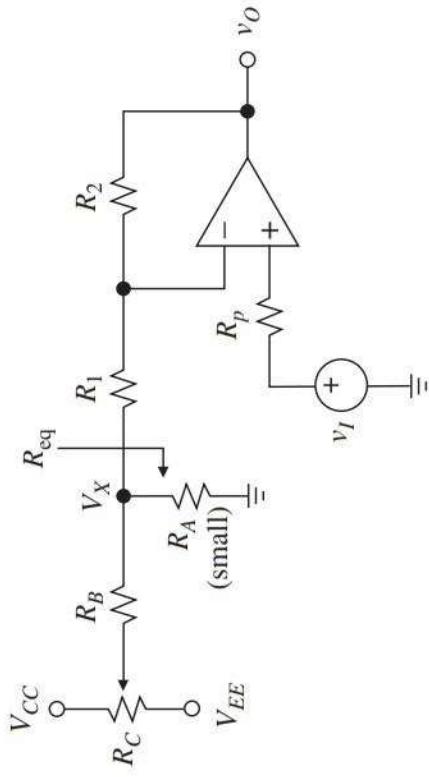
# INPUT OFFSET ERROR AND COMPENSATION TECHNIQUES: EXTERNAL NULLING

- we now have an apparent input error of  $E_I + V_X$ , and we can always adjust  $V_X$  to neutralize  $E_I$
- $V_X$  is obtained from a dual reference source, such as the supply voltages if they are adequately regulated and filtered.
- In the circuits shown, we impose  $R_B \gg R_C$  to avoid excessive loading at the wiper, and  $R_A \ll R_p$  to avoid perturbing the existing resistance levels.



# INPUT OFFSET ERROR AND COMPENSATION TECHNIQUES: EXTERNAL NULLING

In principle, the foregoing scheme can be applied to any circuit that comes with a dc return to ground. In the circuit of Fig. ,  $R_1$  has been lifted off ground and returned to the adjustable voltage  $V_X$ . To avoid upsetting the signal gain, we must impose  $R_{\text{req}} \ll R_1$ , where  $R_{\text{req}}$  is the equivalent resistance of the nulling network as seen by  $R_1$  (for  $R_A \ll R_B$  we have  $R_{\text{req}} \approx R_A$ .) Alternatively, we must decrease  $R_1$  to the value  $R_1 - R_{\text{req}}$ .



**FIGURE**  
External offset-error nulling for the noninverting amplifier.

# LIC: LECTURE

## Static and Dynamic Op Amp Limitations

- DYNAMIC OP-AMP LIMITATIONS: INTRODUCTION
  - (i) Open Loop Response
  - (ii) Single Pole Open-loop Gain
  - (iii) Transient Response
    - (a) Rise Time
    - (b) Slew Rate- Definition and Limitations

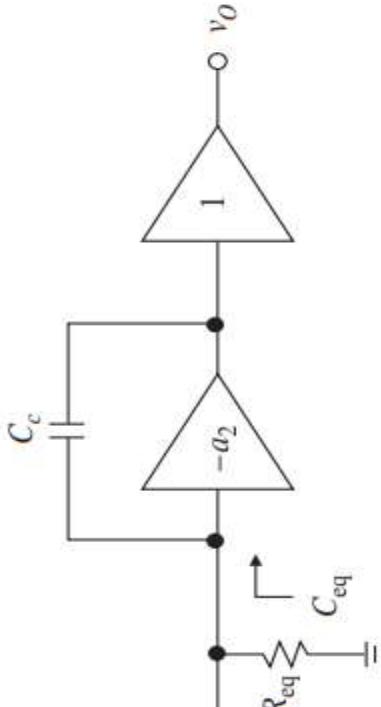
# DYNAMIC OP-AMP LIMITATIONS

- Till now, we have assumed op-amps with extremely high open-loop gains, regardless of frequency.
- A practical op amp provides high gain only from dc up to a given frequency, beyond which gain decreases with frequency and the **output is also delayed with respect to the input**.
- These limitations have a profound impact on the closed-loop characteristics of a circuit: they **affect both its frequency and transient responses**, and also its input and output impedances.

# DYNAMIC OP-AMP LIMITATIONS

## OPEN LOOP RESPONSE

- The most common open-loop response is the dominant-pole response.
- As, its frequency profile is primarily controlled by a single pole.
- At low frequencies, where  $C_c$  acts as an open circuit:

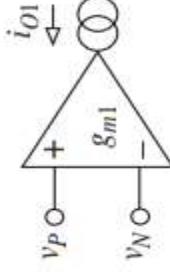


Simplified Op-Amp Block diagram

- The low-frequency gain, called the dc gain and denoted as  $a_0$ ,

$$a_0 = g_{m1} R_{eq} a_2$$

- $g_{m1}$ =transconductance gain of the first stage  
 $-a_2$ =voltage gain of the second stage (inverting stage)
- $R_{eq}$ ,  $C_{eq}$ = Net equivalent resistance and capacitance between the node common to the first and second stage, and ground.



$$V_0 = 1 \times -a_2 \times (-R_{eq} \cdot i_{o1})$$

$$V_0 = g_{m1} R_{eq} a_2 \times (V_p - V_n)$$

# DYNAMIC OP-AMP LIMITATIONS

## OPEN LOOP RESPONSE

- Increasing the operating frequency will bring the impedance of  $C_{eq}$  into the circuit, causing gain to roll off with frequency because of the LPF action provided by  $R_{eq}$  and  $C_{eq}$ .
- Gain starts to roll off at the frequency  $f_b$  that makes:



This frequency ( $f_b$ ), is called as dominant-pole frequency.

$gm1$ =transconductance gain of the first stage  
 $-a2$ =voltage gain of the second stage (inverting stage)  
 **$R_{eq}$ ,  $C_{eq}$** = Net equivalent resistance and capacitance between the node common to the first and second stage, and ground.

# DYNAMIC OP-AMP LIMITATIONS

## Single Pole Open Loop gain

- Assume that the open-loop gain  $a(s)$  possesses just a single pole; this, both to facilitate our mathematical manipulation and to help us develop a basic feel for the effect of the gain roll-off on the closed-loop parameters. Gain can be written as:

where  $j$  is the imaginary unit ( $j^2 = -1$ ), and  $f_b = \omega_b/(2\pi)$  is the *open-loop -3-dB frequency*, also called the *open-loop bandwidth*. We calculate gain magnitude and phase as

$$a(s) = \frac{a_0}{1 + s/w_b}$$

Where,  $S$  is the complex frequency  
 $a_0$  = Open loop DC gain  
 $w_b$  = s-plane pole location

$$|a(jf)| = \text{mag } a(jf) = \frac{a_0}{\sqrt{1 + (f/f_b)^2}}$$

$$\angle a(jf) = \text{ph } a(jf) = -\tan^{-1}(f/f_b)$$

or

In terms of frequency:

$$a(if) = \frac{a_0}{1 + if/f_b}$$

# DYNAMIC OP-AMP LIMITATIONS

## Single Pole Open Loop gain

- The gain is high and approximately constant only from dc up to  $f_b$ . Past  $f_b$  it rolls off at the rate of  $-20$  dB/dec, until it drops to  $0$  dB (or  $1$  V/V) at  $f=f_t$ .
- This frequency  $f_t$**  is called the **unity-gain frequency**, or also the transition frequency.
- Because it marks the transition from amplification (positive decibels) to attenuation (negative decibels).

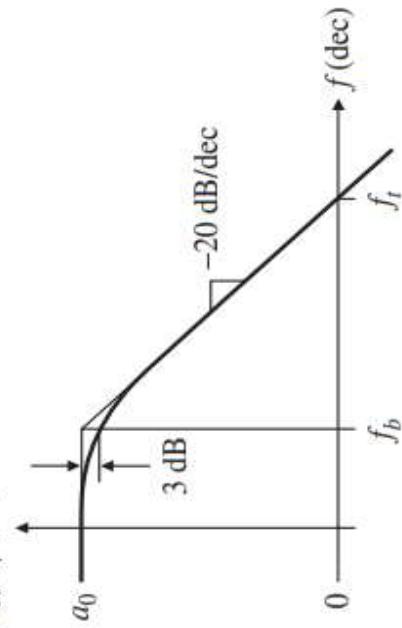
$$1 = \frac{a_0}{\sqrt{1 + (f_t/f_b)^2}} \quad \text{and } f > f_b$$

**So,**  $a_0$     **We get,**  $f_t = a_0 f_b$

- Over the frequency region  $f > f_b$  the op amp behaves as an integrator, and that its gain-bandwidth product, defined as  $\text{GBP} = |a(f)| \times f$ , is constant.

$$\text{So, } \text{GBP} = f_t \quad \text{For } f > f_b$$

$$|a(f)| \text{ (dB)}$$



**Fig. Single-pole open-loop gain**

- For this reason, op amps with dominant-pole compensation are also referred to as constant-GDP op amps.
- Increasing (or decreasing)  $f$  by a given amount in the region of integrator behavior will decrease (or increase)  $|a|$  by the same amount.**

# TRANSIENT RESPONSE

- That is, the response to an input step as a function of time.
- This response, like its frequency domain counterpart, varies with the amount of feedback applied.

**Calculation of Rise Time  $t_R$ :** Small signal B.W. of voltage follower is  $f_t$ . The freq. response can be written as:

$$A(jt) = \frac{1}{1 + jf/f_t}$$

Where,  $S=-2\pi f t$  (pole)

- Subjecting the voltage follower of Fig. to an input voltage step of sufficiently small amplitude  $V_m$  will result in the well-known exponential response
- The time  $t_R$  it takes for ‘ $V_0$ ’ to swing from 10% to 90% of ‘ $V_m$ ’ is called the rise time.
- It provides an indication of how rapid the exponential swing is. So,

$$t_R = \tau (\ln 0.9 - \ln 0.1), \text{ or } t_R = \frac{0.35}{f_t}$$

- This provides a link between the frequency-domain parameter  $f_t$  and the time domain parameter  $t_R$ .

$$V_0(t) = V_m(1 - e^{-t/\tau})$$

$$\text{where, } \tau = \frac{1}{2\pi f_t}$$

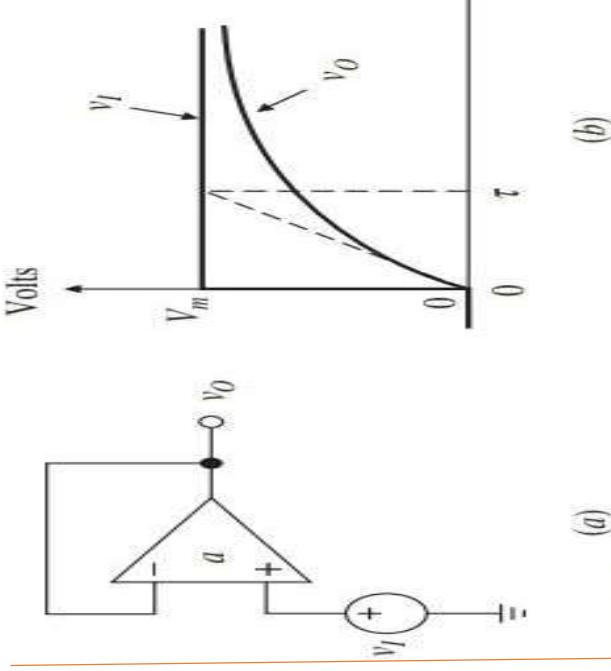
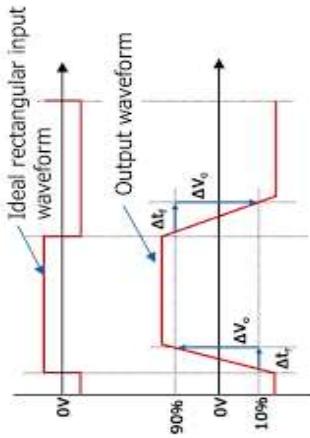


FIGURE  
Voltage follower and its small-signal step response.

# TRANSIENT RESPONSE

**Calculation of Slew Rate (SR=ΔV/Δt): Max. rate of change of an op-amps output per unit time (change of voltage or current per unit time)**



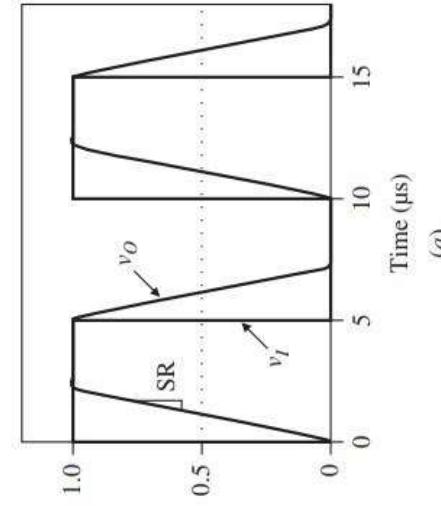
**SLEW RATE LIMITATIONS:** The rate at which  $V_o$  changes with time is highest at the beginning of the exponential transition.

$$V_o(t) = V_m(1 - e^{-t/\tau}) \quad \frac{dV_o}{dt} = \frac{V_m}{\tau} \quad (\text{at } t=0)$$

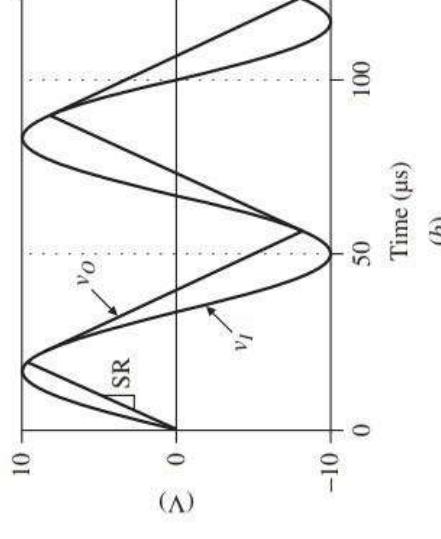
- If we increase  $V_m$ , the rate at which the output sows will have to increase accordingly in order to complete the 10%-to-90% transition within the time  $t_R$ .

- In practice it is observed that above a certain step amplitude the output slope saturates at a constant value called the slew rate (SR). The output waveform, rather than an exponential curve, is now a ramp.

- $\mathbf{SR}$  is a nonlinear large-signal parameter, while  $t_R$  is a linear small signal parameter.



(a)



(b)

FIGURE 1  
Slew-rate limited responses of the 741 follower (a) a pulse and (b) a sinusoid.

- The output-step magnitude corresponding to the onset of slew-rate limiting is such that  $V_{om(\text{crit})}/\tau = SR$ .

$$V_{om(\text{crit})} = \frac{SR}{2\pi f_t}$$