FEEDBACK AMPLIFIERS

Defending on the polarity of the signal being fed back into a Circuit, one may have negative or fasitive feedback.

* Negative feedback results in decreased Voltage gain, for which number of Circuit features are improved.

* Poritire fieldback drives a Circuit into ossallation as in Various types of Oscillator Circuits

* A typical fudback Connection is shown in Fig ():

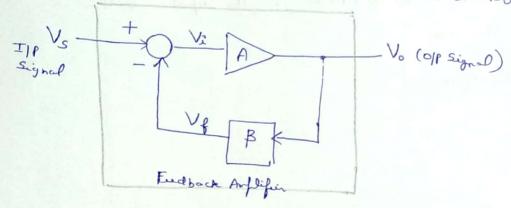


Fig (): Simple block diagram of feedback amflifuir

the ilp rignal Vs is afflied to a mixer network when it is Combuned with a feedback sugnal Vj.

+ The difference of these signals 'Vi is then the infut Woltage to the amplifie.

(A faction of the off amplifier Output Vo is Connected to the feedback network (B), which frevides a reduced faction of the output as fieldback signal to the infut mixer network.

* If the fudback signal is of Offwrite fularity to the infut rignal,

I Although negative fudback results in reduced ormall Voltage gain, a number of advantages are thre. They are:

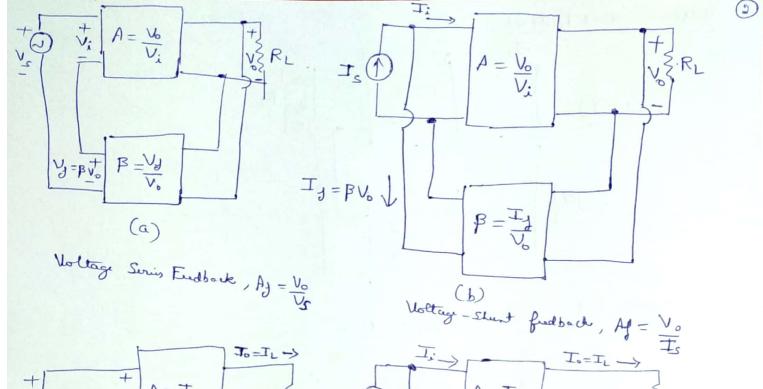
- 1. Higher infect infectance
- 2. Better Stabilized Voltage gain
- 3. Improved frequency response
- te. Lower ofp infedera
- 5. Reduced Noise
- 6. More linear operation

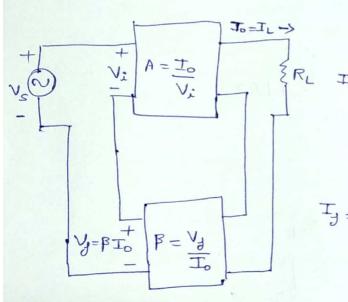
=> Fudback Connection Types

- Here are It basic ways of Connecting the fudback signal.

 Both Wortage and Coursent be fed back to the input either in series or forallel. They are:
 - 1. Woltage series F.B (fudback)
 - d. Woltage-short FB
 - 3. Current series FB
 - d. Current short FB
 - as infut to the fredback network.
 - If 'Current' refers to taffing off some outfut Current through the FB N/w.
 If Series' refers to Connecting the FB seginal in series while the i/P signal Voltage.

 Shoul' refers to Connecting the FB seginal in shoul (parallel) with an infect Current Sources
- I Servis FB Connections tend to increase the infut presistence, whereas short FB Connections tend to decrease the infut presistence.
 - t Willage FB tends to decrease the O/P infedance, aller as current FB tends to increase the O/P infedance.
- 4 Typically, higher infect and lower output infectances are desired for most Cascade amplifiers. Both of there are provided using the Voltage series fulback Cornection.





Ty = BIV (a)

(c) Current - Series find back Ay = To

Current-short fieldbock, $A_{J} = \frac{\pm o}{I_{S}}$.

of the gain without fudback, A, is that of the amplifun stage.

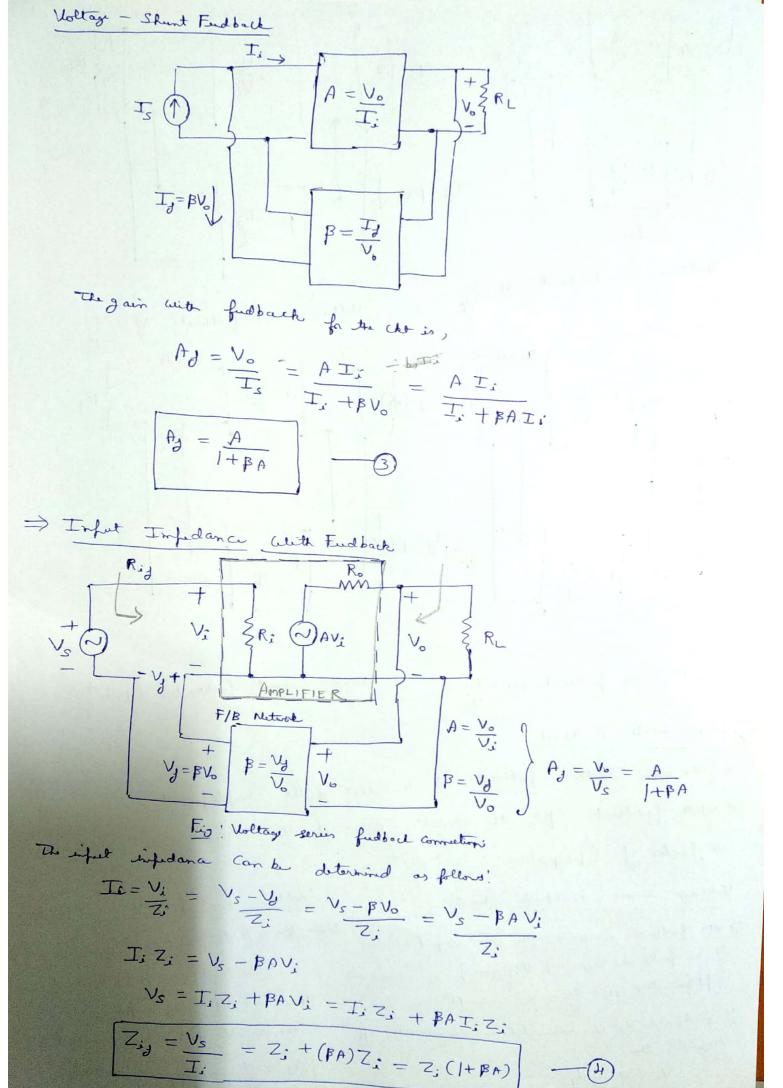
of cuts feedback 'B', the ornale gain of the circuit is reduced by

Voltage Series Endback (Fig. a)

of Uty fedback in series with input eignal. If no fudback (Vy =0), My gain of anflifer stage $A = \frac{V_0}{V_s} = \frac{V_0}{V_s}$ y feedback V' is connected in serie with iff Riginal, then Vi = Vs - V

in the state of th Since Vo = AVi = A (Vs-Vy) = AVs-AVy Vo = AVS - A (BVO) Then (I+FA)Vo = AVS So the arnall Woltage gain with FB,

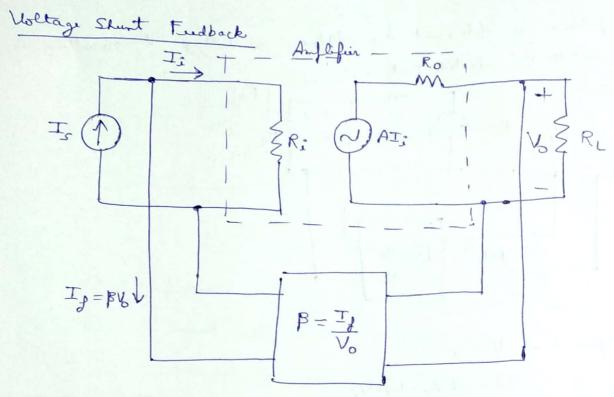
Scanned with CamScanner



The infut impedance with series feedback in seen to be the Volume of 3.

the infut impedance without feedback multiplied by the factor (I+PA),
and is afflied to both Nollay-series (Fig. and Current-series (Fig.)

Configurations.



Ev: Woltage Short fulback Connection

The infut infedera Can be determined to ke,

$$Z_{ij} = \frac{V_{i}}{T_{s}} = \frac{V_{s}}{T_{i} + T_{j}} = \frac{V_{s}}{T_{i} + \beta V_{o}}$$

$$\frac{1}{T_{s}} = \frac{V_{s}}{T_{s}} + \frac{1}{T_{s}} = \frac{V_{s}}{T_{s} + \beta V_{o}}$$

$$\frac{1}{T_{s}} = \frac{V_{s}}{T_{s}} + \frac{1}{T_{s}} = \frac{V_{s}}{T_{s}}$$

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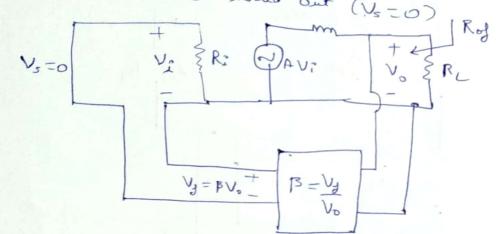
This reduced is fut infedence affilies to the Woltage-series Connection (fis @) and the Woltage-short Connection of Tio @.

Output Impedance With Fredback

For Woltage feedback, the output impedance is decreases, whereas Current fudback increes the output impedance.

Voltage-serier Fudbock: (Fig 14.3)

The Op infedence is determined by afflying a Woltage 'V', resulting in a Conrect I, With Vs shorted out (Vs=0) | Roj

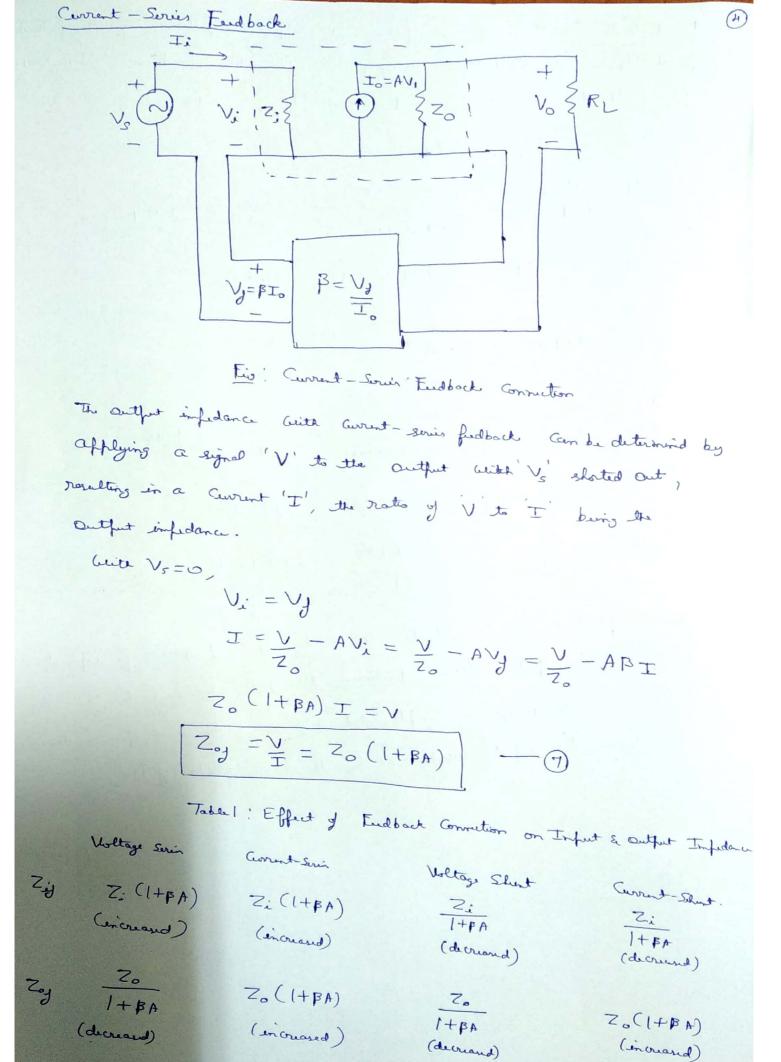


The Wollage V is then,

For
$$V_s = 0$$
, $V_i = -V_d$

Sother
$$V = IZ_0 - AV_y = IZ_0 - A(\beta V)$$
 $\Rightarrow V \neq BAV$

Eq" (shows that weith Voltage-series fudback, to output infedance is reduced from that without fuelback by the Jocta (ItBA).



Ex:1: Determine Woltage gain, input and output impedance certite fudback for Woltage - series fudback larving A = -100, Ri = 10 keV and Ro = 20 keV for fudback of (a) B = -0.1 and (b) B = -0.5.

Ent levery set D, Q and C, au calculate

$$\frac{Q}{1+\beta} = \frac{A}{1+\beta} = \frac{-100}{1+(-0.1)(-100)} = \frac{-100}{11} = -9.09$$

$$Z_{ij} = Z_{i}(1+\beta) = -100$$

$$Z_{ej} = \frac{Z_{o}}{1+PA} = \frac{20\times10^{3}}{11} = 1.82 \text{ k}$$

$$\frac{b}{1+b} = \frac{A}{1+b} = \frac{-100}{1+(-0.5)(-100)} = -\frac{100}{51} = -1.96$$

$$Z_{0}J = \frac{Z_{0}}{1+\beta A} = \frac{20\times13}{51} = 392.16 \text{ }\Omega$$

This example (Pat-a) demonstrates the trade-off of gain for infrared infect and outfut resistance. Reducing the gain by a factor of 11 (from 100 to 9.09) is Conflemented by a reduced outfut resistance and increased infect resistance by the same factor of 11.