

Project Report: Random Number Generator

Sudarshan Shivashankar

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1 Introduction

The Random Number Generator (RNG) is a circuit that generates random numbers using electronic components. In this project, we will design and implement an RNG using a breadboard, a Seven-Segment Display, a 7474 Flip-Flop, a 7447 Decoder, a 7684 Clock, a 555 IC, resistors ($1k\Omega$, $1M\Omega$), capacitors ($100nF$, $10nF$), and jump wires. This report provides a detailed description of the components used, the circuit design, a block diagram of the system, and an output image.

2 Components

The following components are required for this project:

1. Breadboard: Provides a platform for connecting and prototyping electronic circuits.
2. Seven-Segment Display: Displays the generated random number in decimal format.
3. 7474 Flip-Flop: A dual positive-edge-triggered D-type flip-flop used for storing and controlling the random number generation.
4. 7447 Decoder: Converts the binary output from the flip-flop to the appropriate signals for the Seven-Segment Display.
5. 7684 Clock: Generates clock pulses to trigger the flip-flop.
6. 555 IC: An integrated circuit used as an astable multivibrator to generate clock pulses.
7. Resistors: A $1k\Omega$ resistor and a $1M\Omega$ resistor are used to set the timing and voltage levels in the circuit.
8. Capacitors: A $100nF$ capacitor and a $10nF$ capacitor are used for timing and stability purposes.
9. Jump Wires: Used to establish connections between the components on the breadboard.

3 Description of the Circuit

The circuit consists of the following key components:

1. 555 IC: Configured as an astable multivibrator to generate clock pulses. The timing is determined by the values of the resistors and capacitors connected to the IC.
2. 7684 Clock: Receives the clock pulses from the 555 IC and provides them to the flip-flop.
3. 7474 Flip-Flop: Connected to the clock, it stores and controls the random number generation process.
4. 7447 Decoder: Takes the binary output from the flip-flop and converts it to the appropriate signals for the Seven-Segment Display.
5. Seven-Segment Display: Displays the generated random number in decimal format.

The circuit is designed as follows:

1. Connect the power supply to the breadboard.
2. Place the 555 IC on the breadboard and connect the resistors and capacitors to the appropriate pins to configure it as an astable multivibrator.
3. Connect the clock output of the 555 IC to the clock input of the 7684 Clock.
4. Connect the power and ground pins of the flip-flop and decoder to the power supply and ground respectively.
5. Connect the clock output of the 7684 Clock to the clock input of the flip-flop.
6. Connect the binary outputs of the flip-flop to the inputs of the decoder.
7. Connect the output pins of the decoder to the appropriate input pins of the Seven-Segment Display.
8. Connect the power and ground pins of the Seven-Segment Display to the power supply and ground respectively.

4 Working of the Circuit

Description of the Random Number Generator Circuit using Shift Registers:

- The random number generator circuit is designed to generate random numbers using shift registers, providing a versatile and reliable solution for applications that require randomness. The circuit incorporates various components, including a breadboard, seven-segment display, decoder, flip-flops, XOR gate, 555 timer IC, resistors, capacitors, and jumper wires.
- At the core of the circuit is the shift register, constructed using two D flip-flops (7474 ICs) and an XOR gate (7486 IC). The shift register operates based on the clock signal generated by the 555 timer IC. The clock signal serves as the synchronization mechanism, ensuring the precise shifting of data within the shift register.
- The clock signal generated by the 555 timer circuit is connected to the CLOCK input of the D flip-flops, enabling the sequential shifting of data through the flip-flops. This shifting action creates a cascading effect, allowing the circuit to generate a sequence of random bits.
- The output of each D flip-flop is connected to the Decoder IC (7447 IC), which converts the binary input into a corresponding output that can be displayed on the seven-segment display. The connections between the flip-flops and the decoder are carefully established to ensure the proper mapping of binary values to the display segments.
- The seven-segment display, a common anode type, is connected to the Decoder IC (7447 IC) following the prescribed pin connections. This display arrangement allows the generated random numbers to be visually represented in a human-readable format.
- Power and ground connections are established for all components to ensure proper operation and signal integrity. The circuit requires a stable power supply, and the voltage specifications of each component should be respected to prevent damage and ensure accurate performance.
- The random number generator circuit offers various applications, such as simulations, gaming, cryptography, testing, art, research, and decision-making. By following the circuit diagram and carefully assembling the components, users can create a functional and reliable random number generator capable of providing high-quality random numbers for their specific needs.

5 Output Image

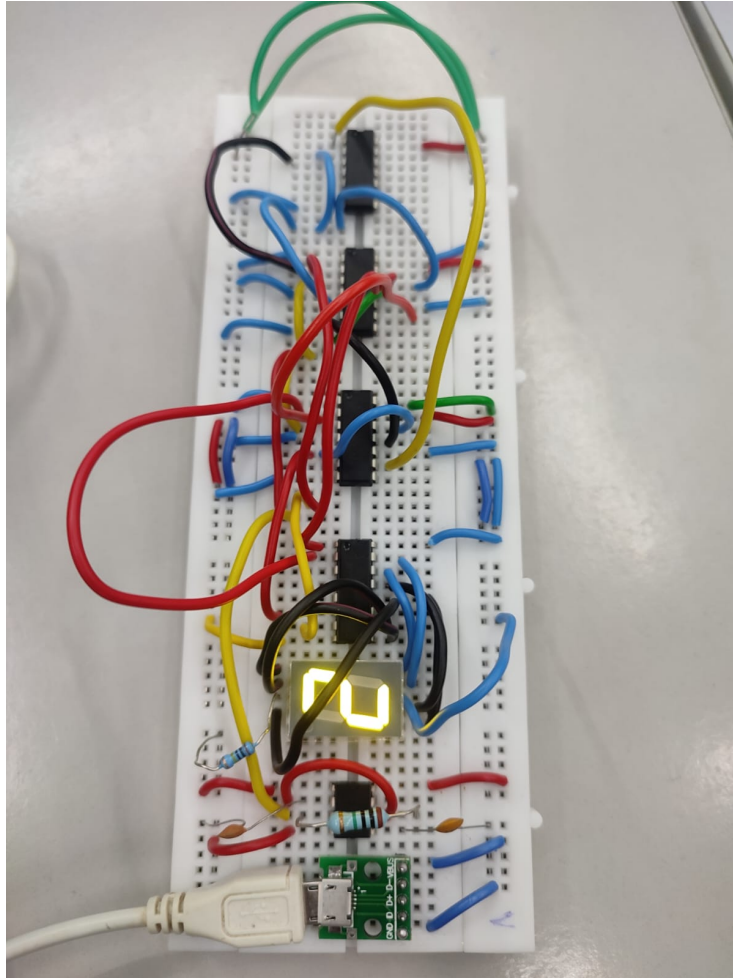


Figure 1: Completed circuit with the Seven-Segment Display displaying a random number.

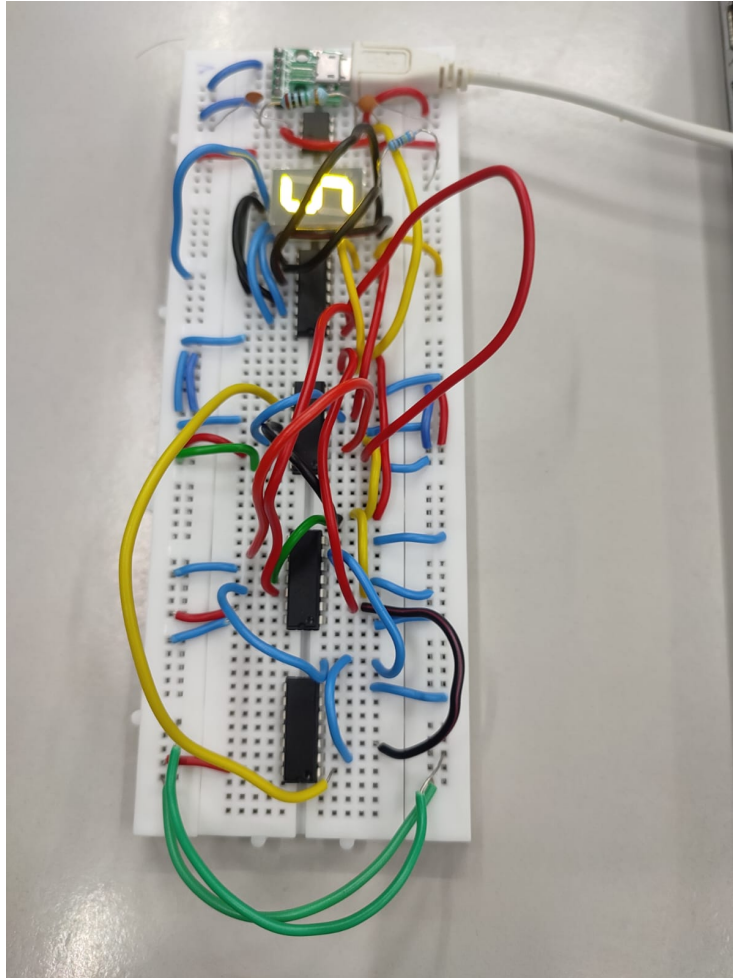


Figure 2: Completed circuit with the Seven-Segment Display displaying a random number.

Block Diagram

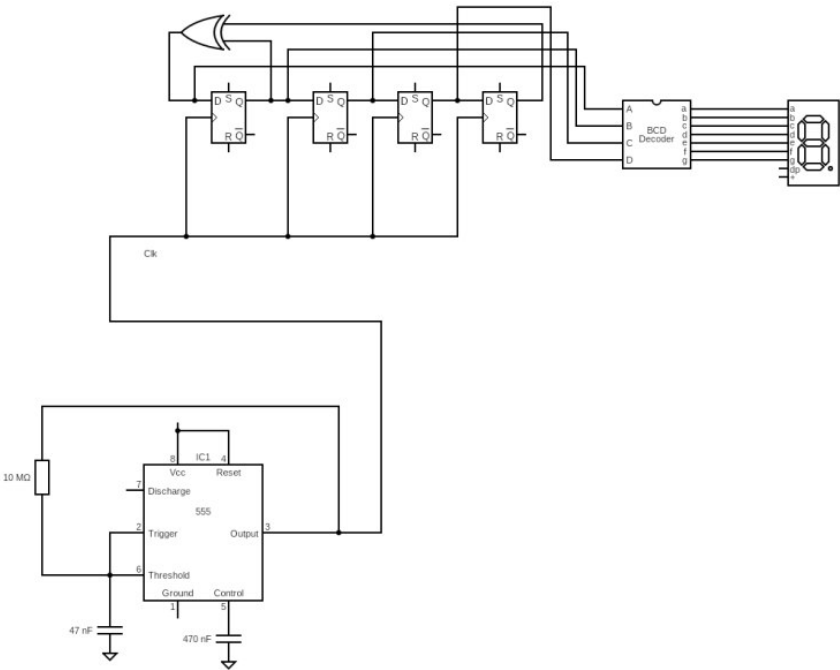


Figure 4: Block Diagram

Figure 3: Block diagram of the Random Number Generator circuit.