

野火_XC7A35T/100T/200T核心板_原理图_V1.0

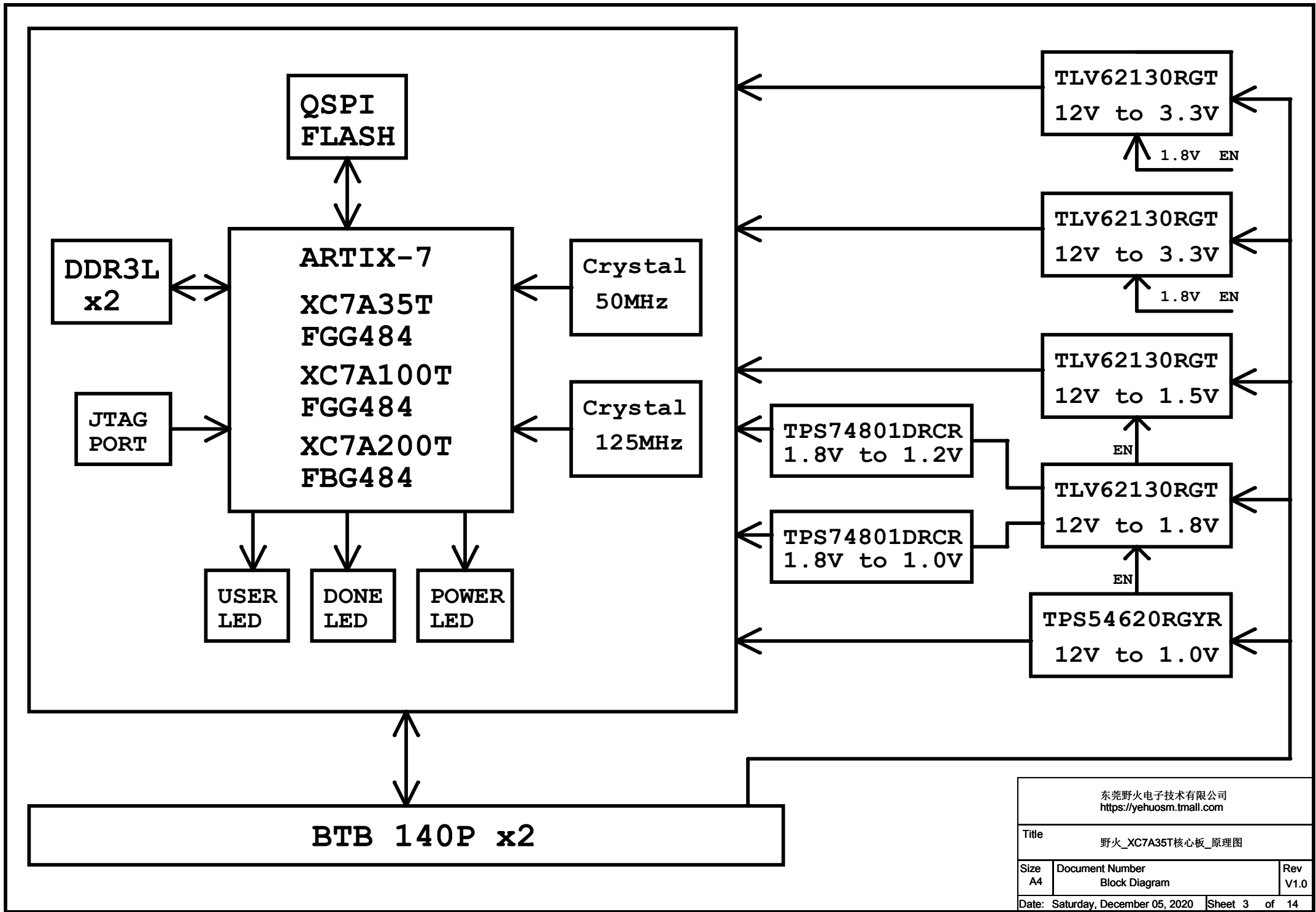
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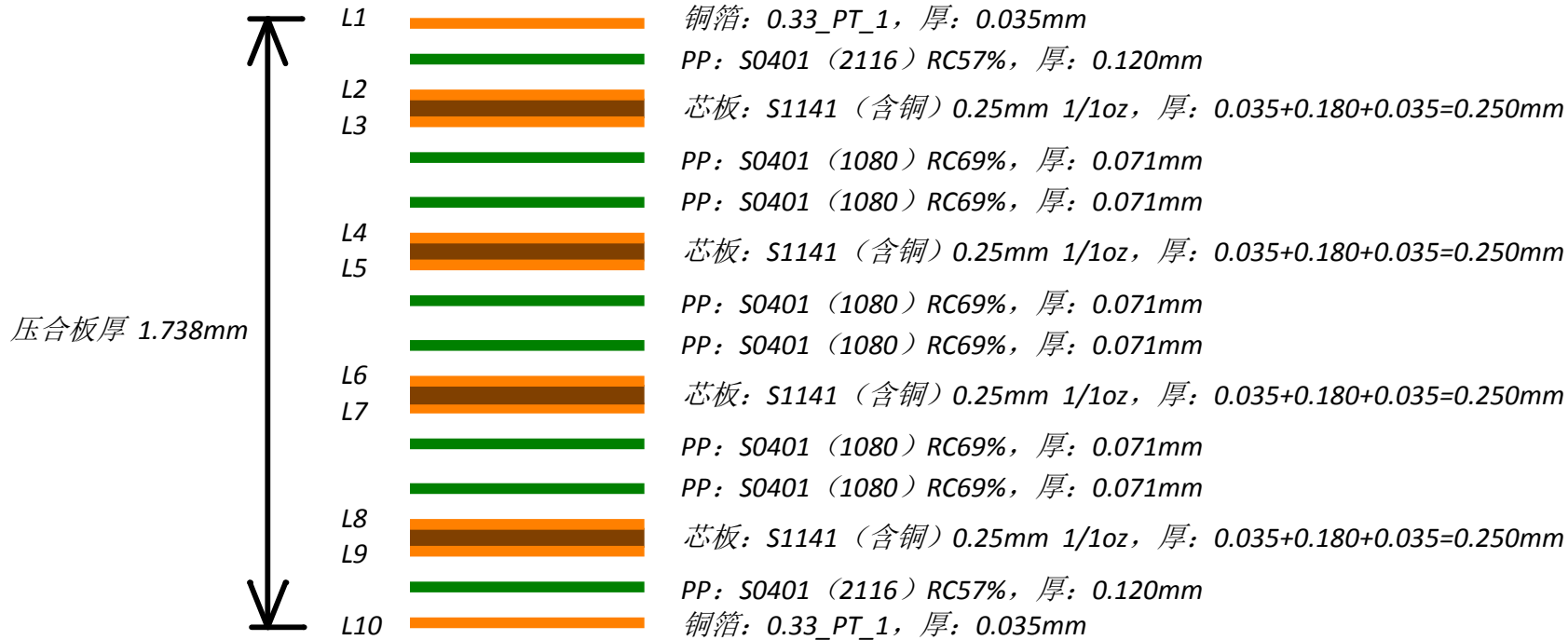
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历史版本

版本号	日期	设计	描述
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V1.0	2021-03-05	xgh	整理资料发布，稳定版

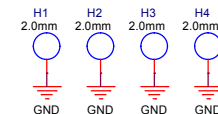
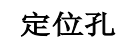
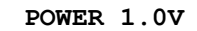
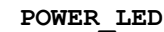
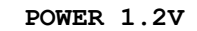
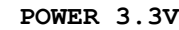
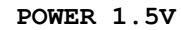
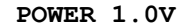


ebf_xc7a35t/100t/200t core board



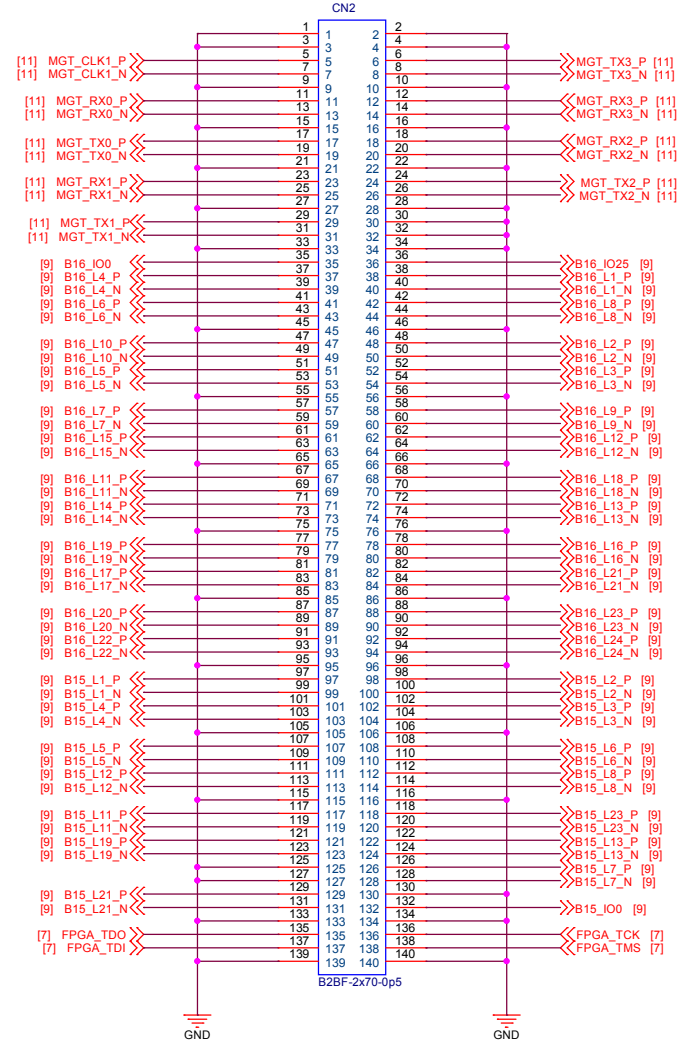
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POWER 5V~12V

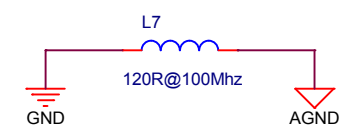
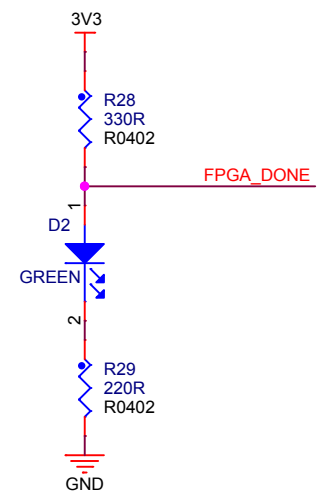
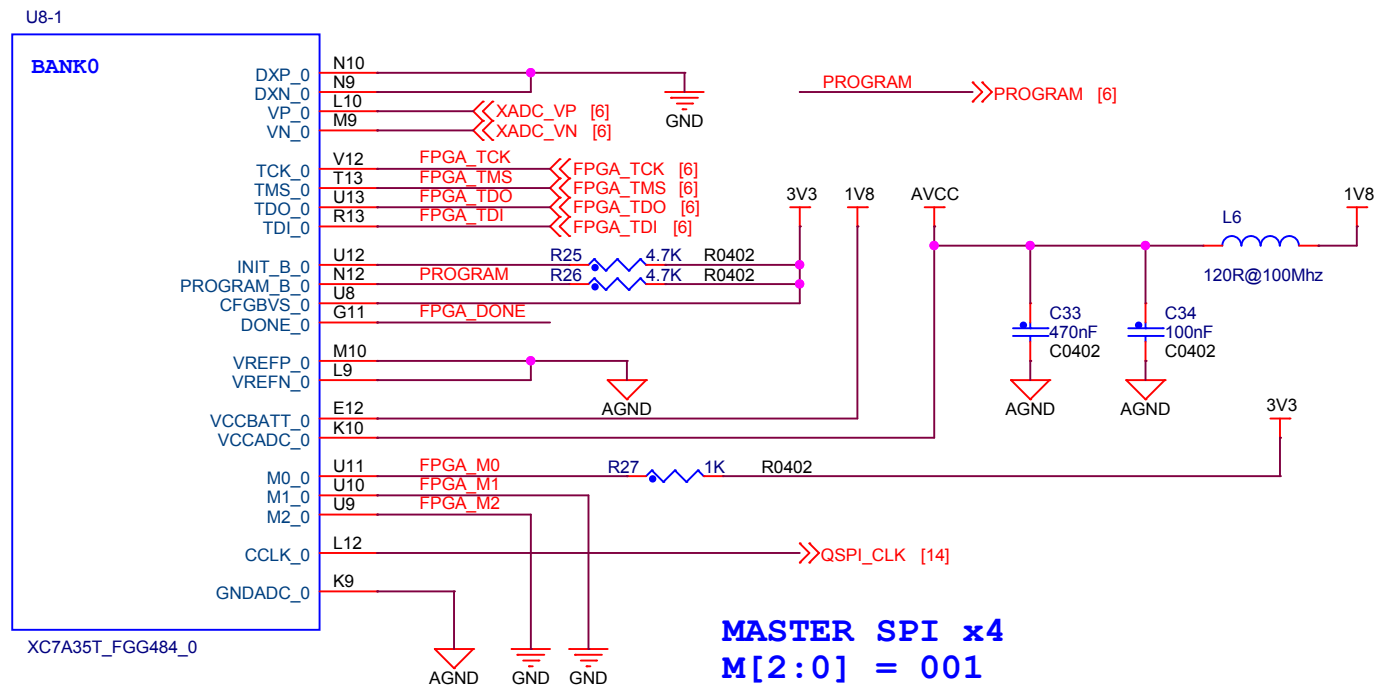


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BTB连接器

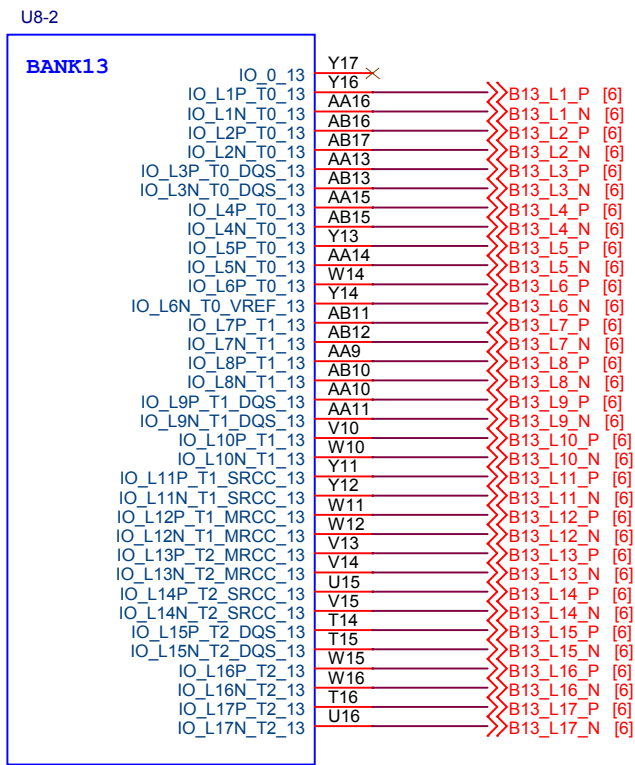


BANK0



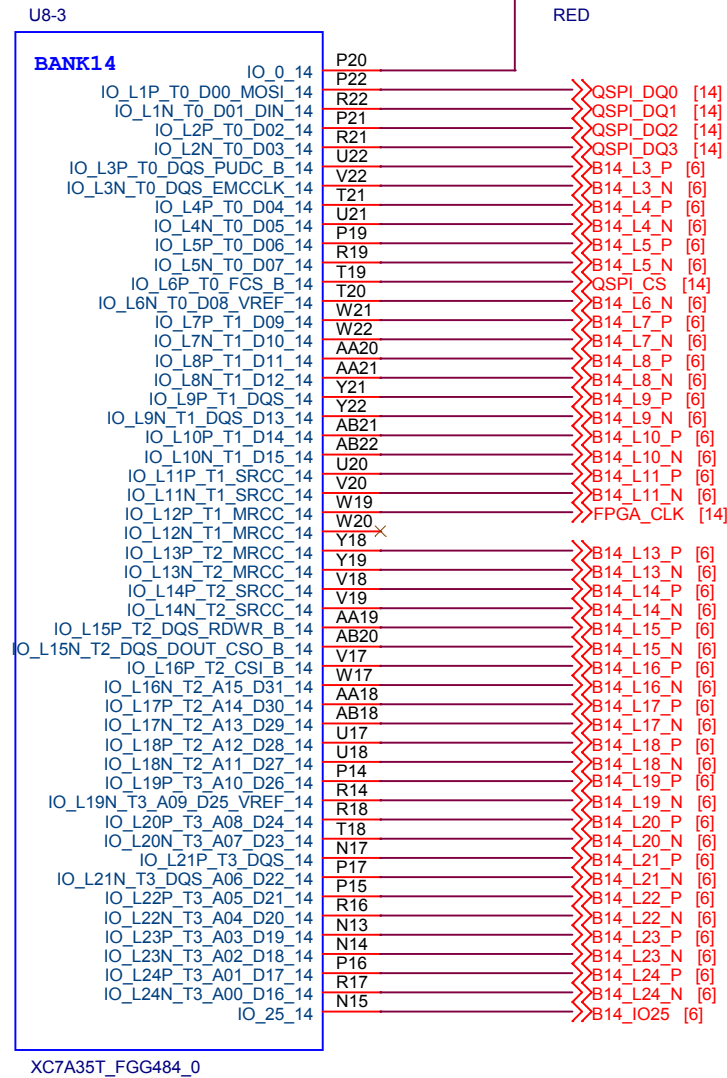
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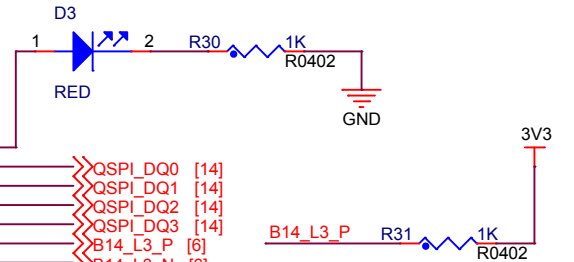
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XC7A35T无BANK13
XC7A100T/XC7A200T有BANK13



XC7A35T_FGG484_0

USER_LED



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BANK15/16

U8-4

BANK15

IO_0_15	J16	>>B15_IO0 [6]
IO_L1P_T0_AD0P_15	H13	>>B15_L1_P [6]
IO_L1N_T0_AD0N_15	G13	>>B15_L1_N [6]
IO_L2P_T0_AD8P_15	G15	>>B15_L2_P [6]
IO_L2N_T0_AD8N_15	G16	>>B15_L2_N [6]
IO_L3P_T0_DQS_AD1P_15	J14	>>B15_L3_P [6]
IO_L3N_T0_DQS_AD1N_15	H14	>>B15_L3_N [6]
IO_L4P_T0_15	G17	>>B15_L4_P [6]
IO_L4N_T0_15	G18	>>B15_L4_N [6]
IO_L5P_T0_AD9P_15	J15	>>B15_L5_P [6]
IO_L5N_T0_AD9N_15	H15	>>B15_L5_N [6]
IO_L6P_T0_15	H17	>>B15_L6_P [6]
IO_L6N_T0_VREF_15	H18	>>B15_L6_N [6]
IO_L7P_T1_AD2P_15	J22	>>B15_L7_P [6]
IO_L7N_T1_AD2N_15	H22	>>B15_L7_N [6]
IO_L8P_T1_AD10P_15	H20	>>B15_L8_P [6]
IO_L8N_T1_AD10N_15	G20	>>B15_L8_N [6]
IO_L9P_T1_DQS_AD3P_15	K21	>>B15_L9_P [6]
IO_L9N_T1_DQS_AD3N_15	K22	>>B15_L9_N [6]
IO_L10P_T1_AD11P_15	M21	>>B15_L10_P [6]
IO_L10N_T1_AD11N_15	L21	>>B15_L10_N [6]
IO_L11P_T1_SRCC_15	J20	>>B15_L11_P [6]
IO_L11N_T1_SRCC_15	J21	>>B15_L11_N [6]
IO_L12P_T1_MRCC_15	J19	>>B15_L12_P [6]
IO_L12N_T1_MRCC_15	H19	>>B15_L12_N [6]
IO_L13P_T2_MRCC_15	K18	>>B15_L13_P [6]
IO_L13N_T2_MRCC_15	K19	>>B15_L13_N [6]
IO_L14P_T2_SRCC_15	L19	>>B15_L14_P [6]
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IO_L15N_T2_DQS_ADV_B_15	M22	>>B15_L15_N [6]
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IO_L24P_T3_RS1_15	M15	>>B15_L24_P [6]
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BANK16

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IO_L3P_T0_DQS_16	C14	>>B16_L3_P [6]
IO_L3N_T0_DQS_16	C15	>>B16_L3_N [6]
IO_L4P_T0_16	E13	>>B16_L4_P [6]
IO_L4N_T0_16	E14	>>B16_L4_N [6]
IO_L5P_T0_16	E16	>>B16_L5_P [6]
IO_L5N_T0_16	D16	>>B16_L5_N [6]
IO_L6P_T0_16	D14	>>B16_L6_P [6]
IO_L6N_T0_VREF_16	D15	>>B16_L6_N [6]
IO_L7P_T1_16	B15	>>B16_L7_P [6]
IO_L7N_T1_16	B16	>>B16_L7_N [6]
IO_L8P_T1_16	C13	>>B16_L8_P [6]
IO_L8N_T1_16	B13	>>B16_L8_N [6]
IO_L9P_T1_DQS_16	A15	>>B16_L9_P [6]
IO_L9N_T1_DQS_16	A16	>>B16_L9_N [6]
IO_L10P_T1_16	A13	>>B16_L10_P [6]
IO_L10N_T1_16	A14	>>B16_L10_N [6]
IO_L11P_T1_SRCC_16	B17	>>B16_L11_P [6]
IO_L11N_T1_SRCC_16	B18	>>B16_L11_N [6]
IO_L12P_T1_MRCC_16	D17	>>B16_L12_P [6]
IO_L12N_T1_MRCC_16	C17	>>B16_L12_N [6]
IO_L13P_T2_MRCC_16	C18	>>B16_L13_P [6]
IO_L13N_T2_MRCC_16	C19	>>B16_L13_N [6]
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IO_L17N_T2_16	A19	>>B16_L17_N [6]
IO_L18P_T2_16	F19	>>B16_L18_P [6]
IO_L18N_T2_16	F20	>>B16_L18_N [6]
IO_L19P_T3_16	D20	>>B16_L19_P [6]
IO_L19N_T3_VREF_16	C20	>>B16_L19_N [6]
IO_L20P_T3_16	C22	>>B16_L20_P [6]
IO_L20N_T3_16	B22	>>B16_L20_N [6]
IO_L21P_T3_DQS_16	B21	>>B16_L21_P [6]
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IO_L23N_T3_16	D21	>>B16_L23_N [6]
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IO_L24N_T3_16	G22	>>B16_L24_N [6]
IO_25_16	F21	>>B16_IO25 [6]

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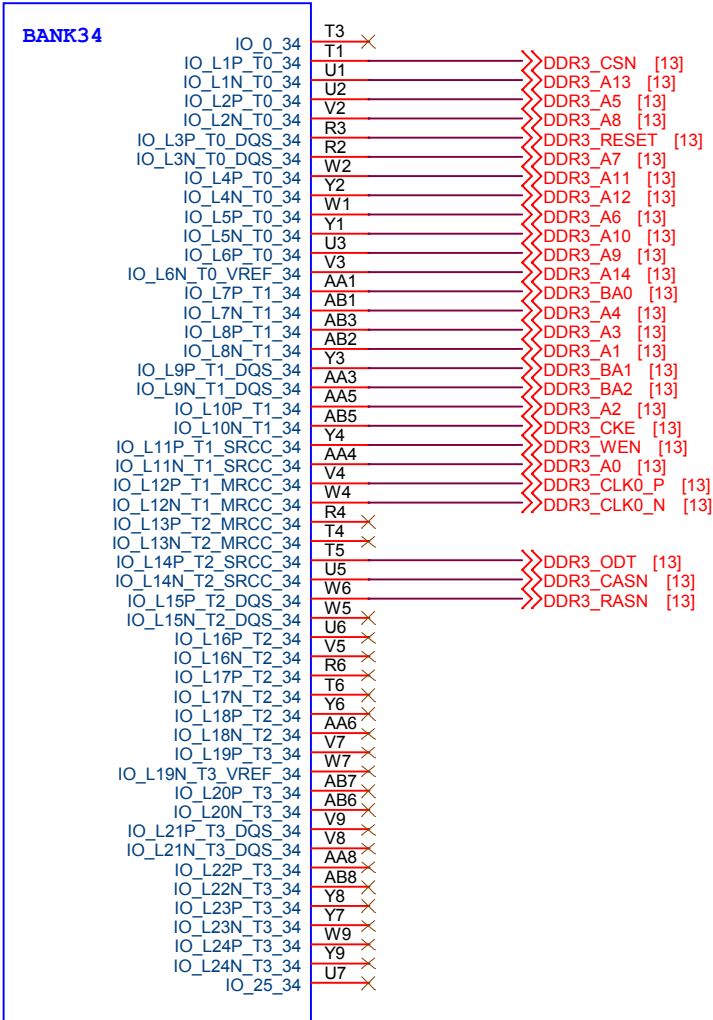
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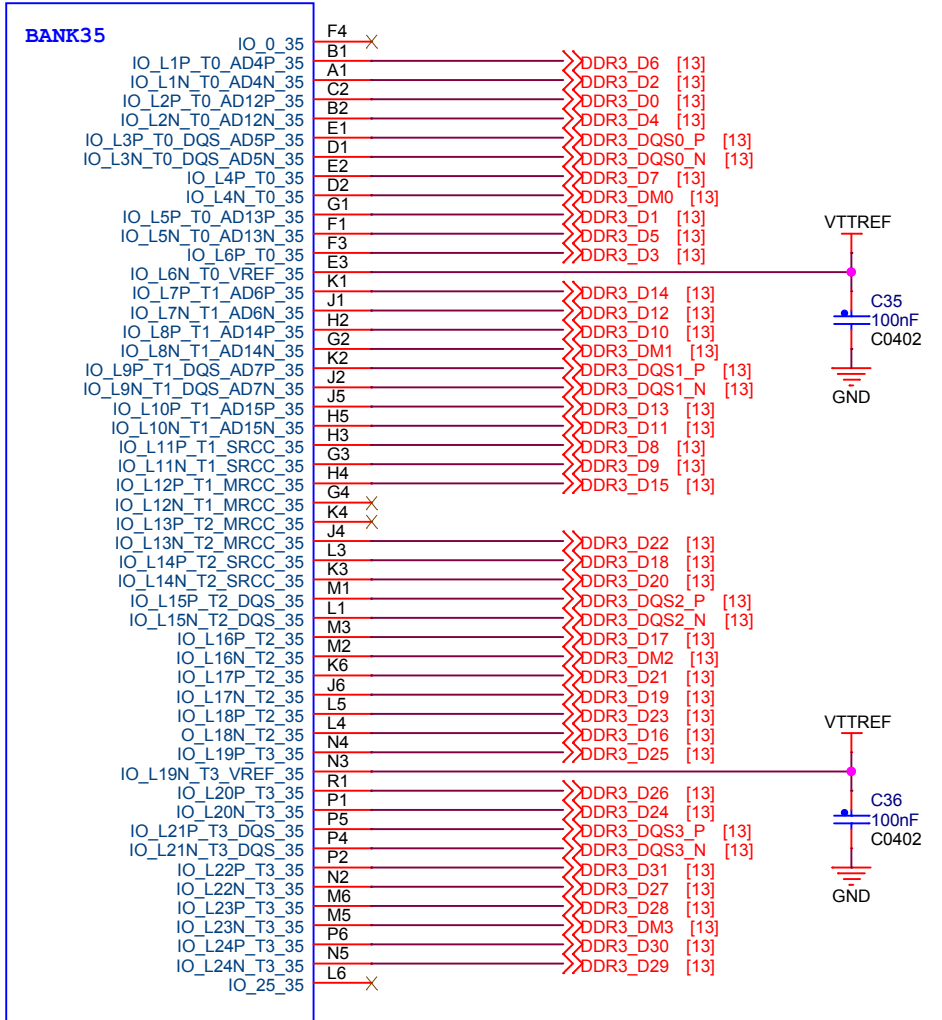
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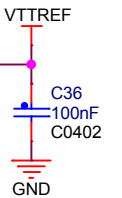
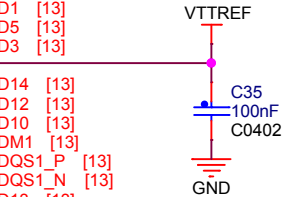


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U8-7

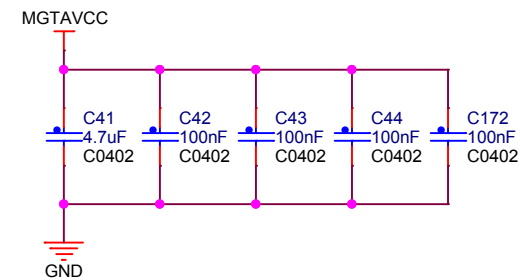
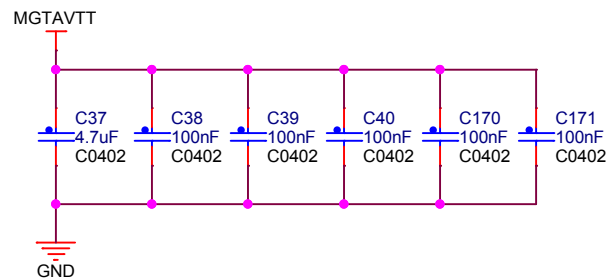
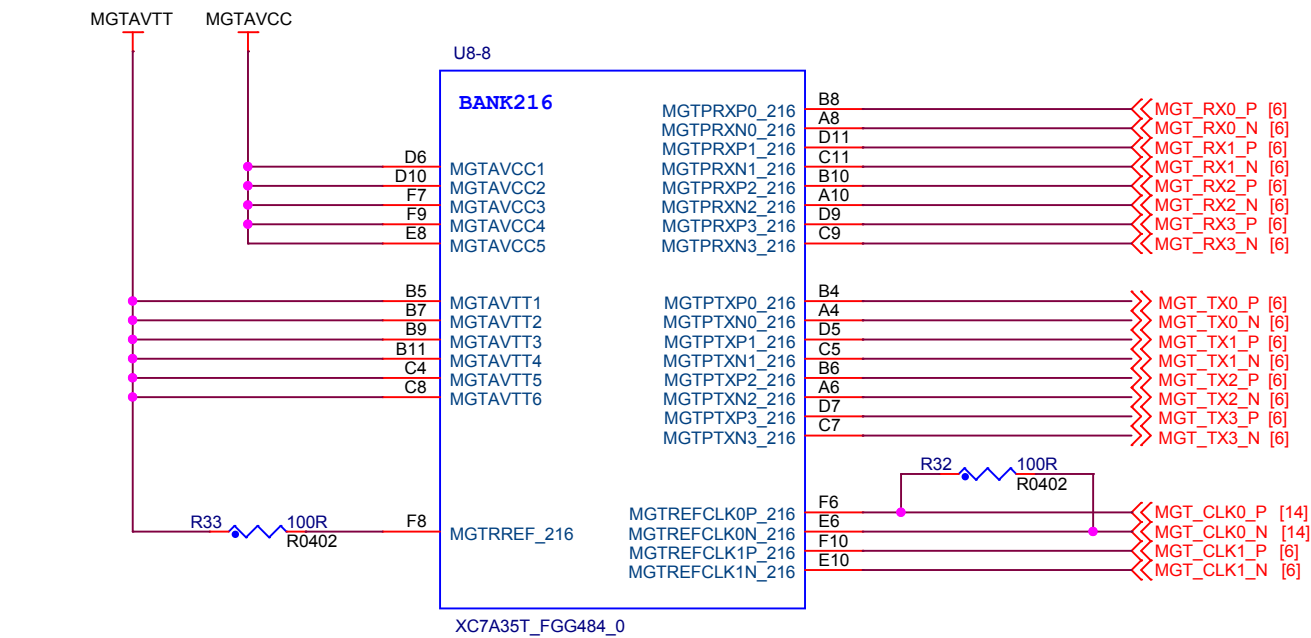


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BANK216



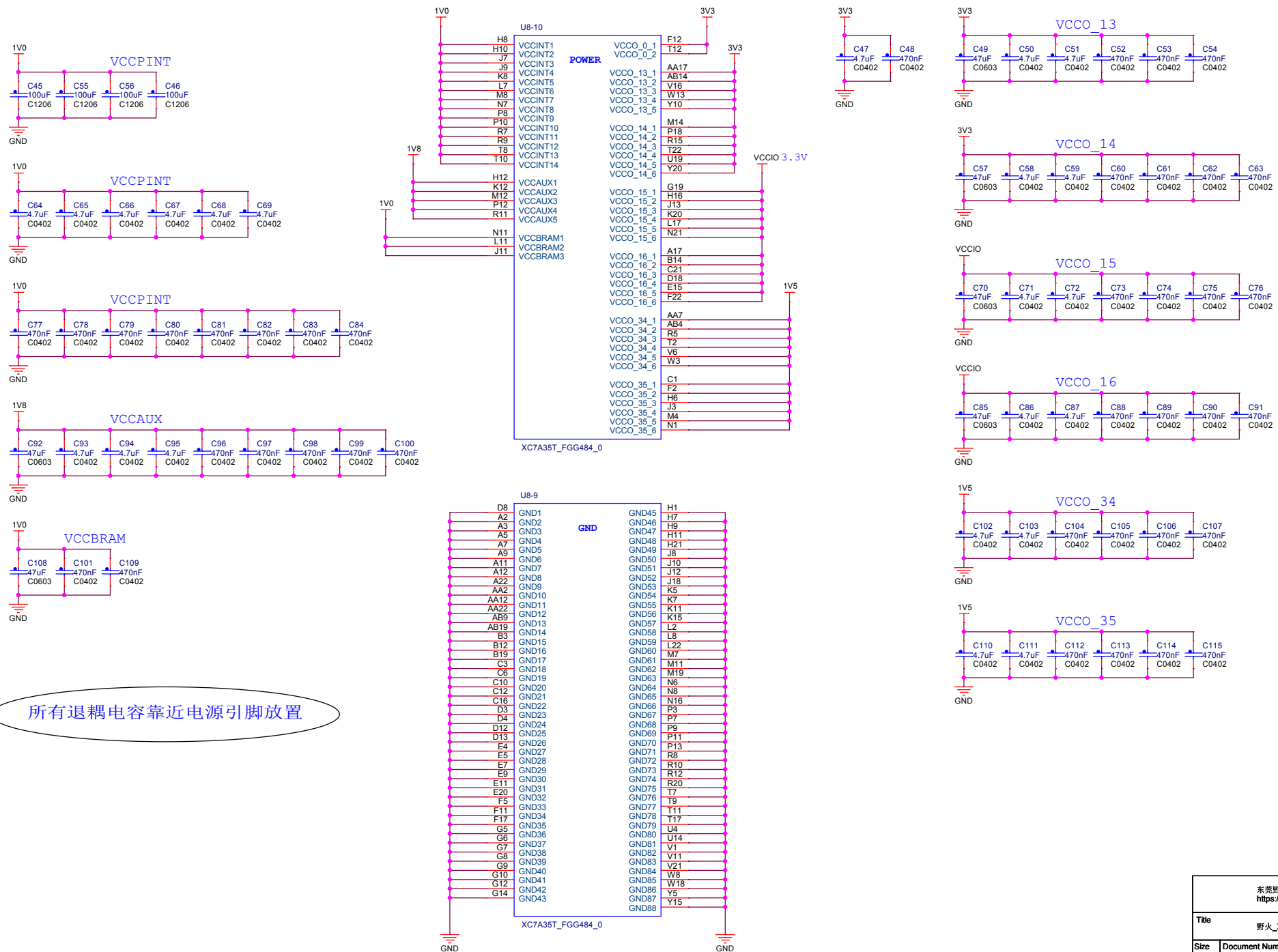
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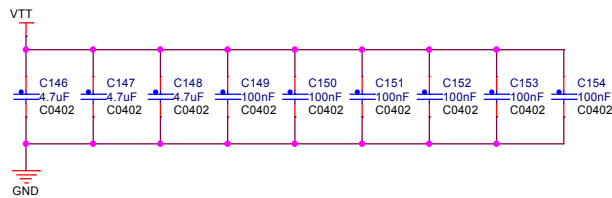
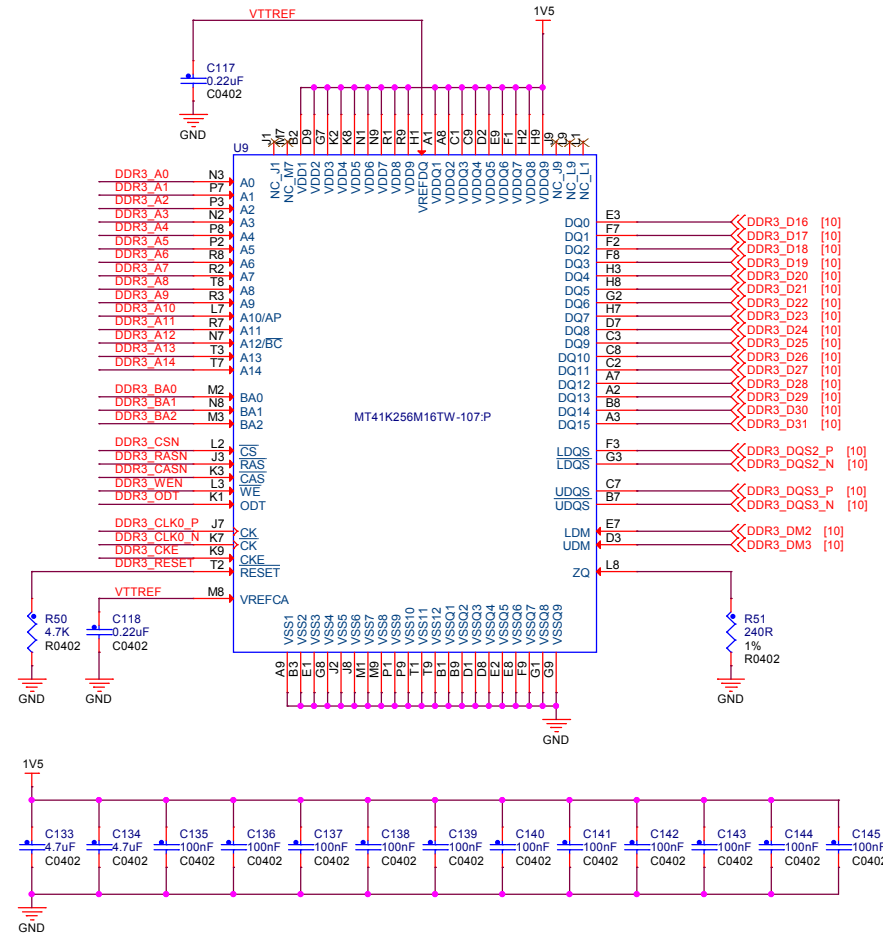
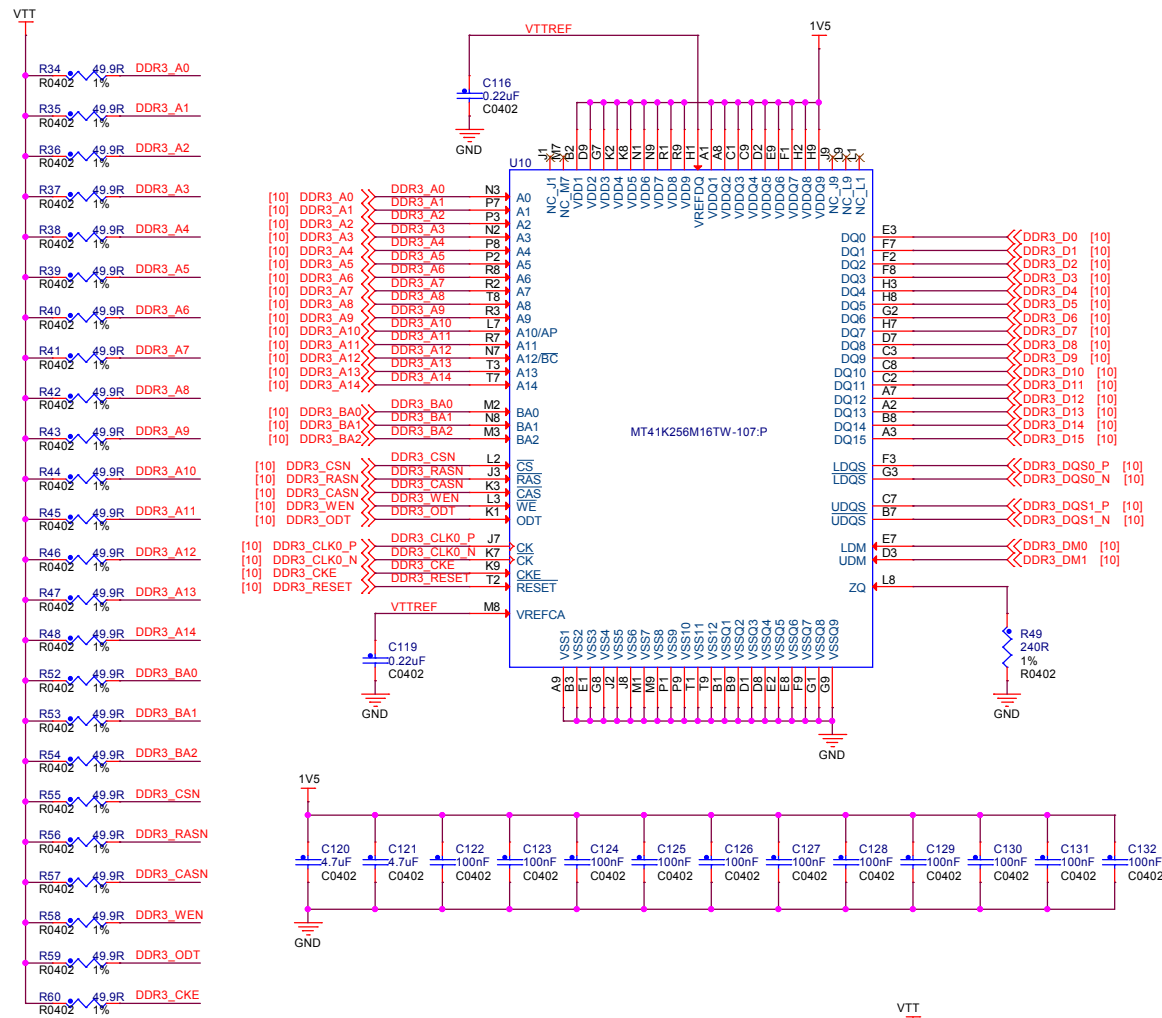
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IO_POWER/CORE_POWER



所有退耦电容靠近电源引脚放置

DDR3L

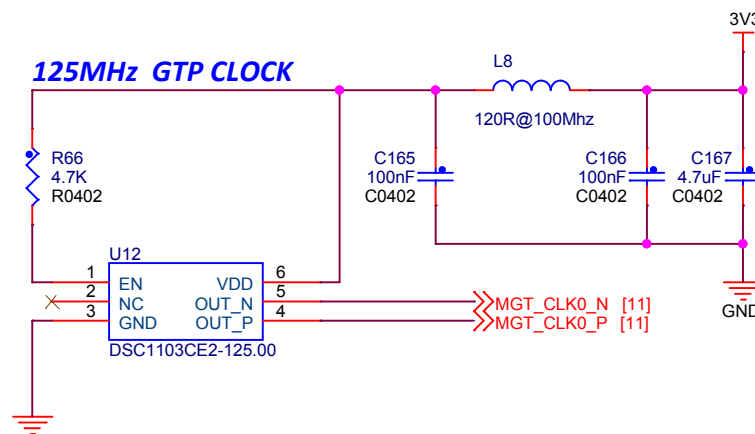
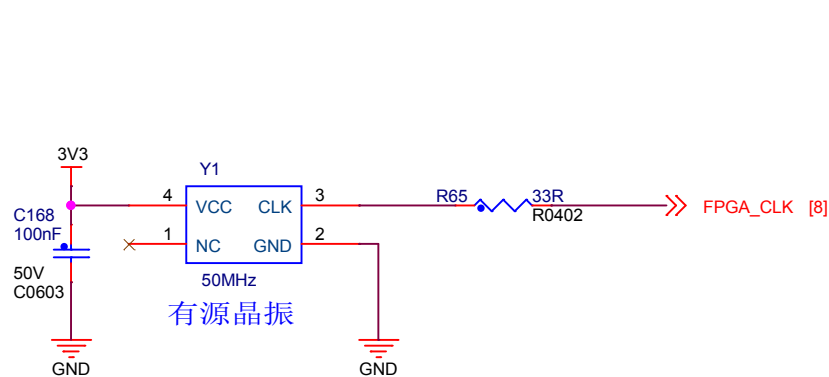


CLK终端电阻:R64靠近DDR3放置



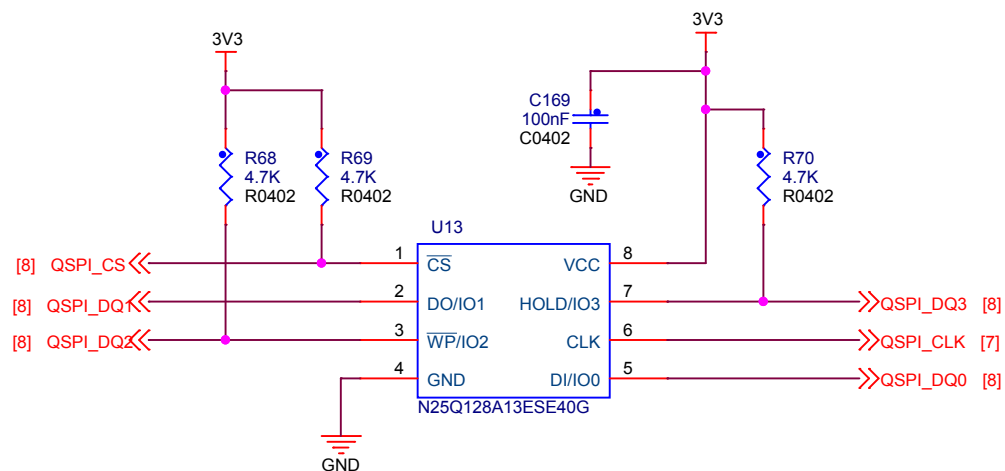
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CLOCK



SPI_FLASH

容量: 16M字节



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