

# 野火\_XC7A35T/100T/200T核心板\_原理图\_V1.0

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Title 野火\_XC7A35T核心板\_原理图

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# 历史版本

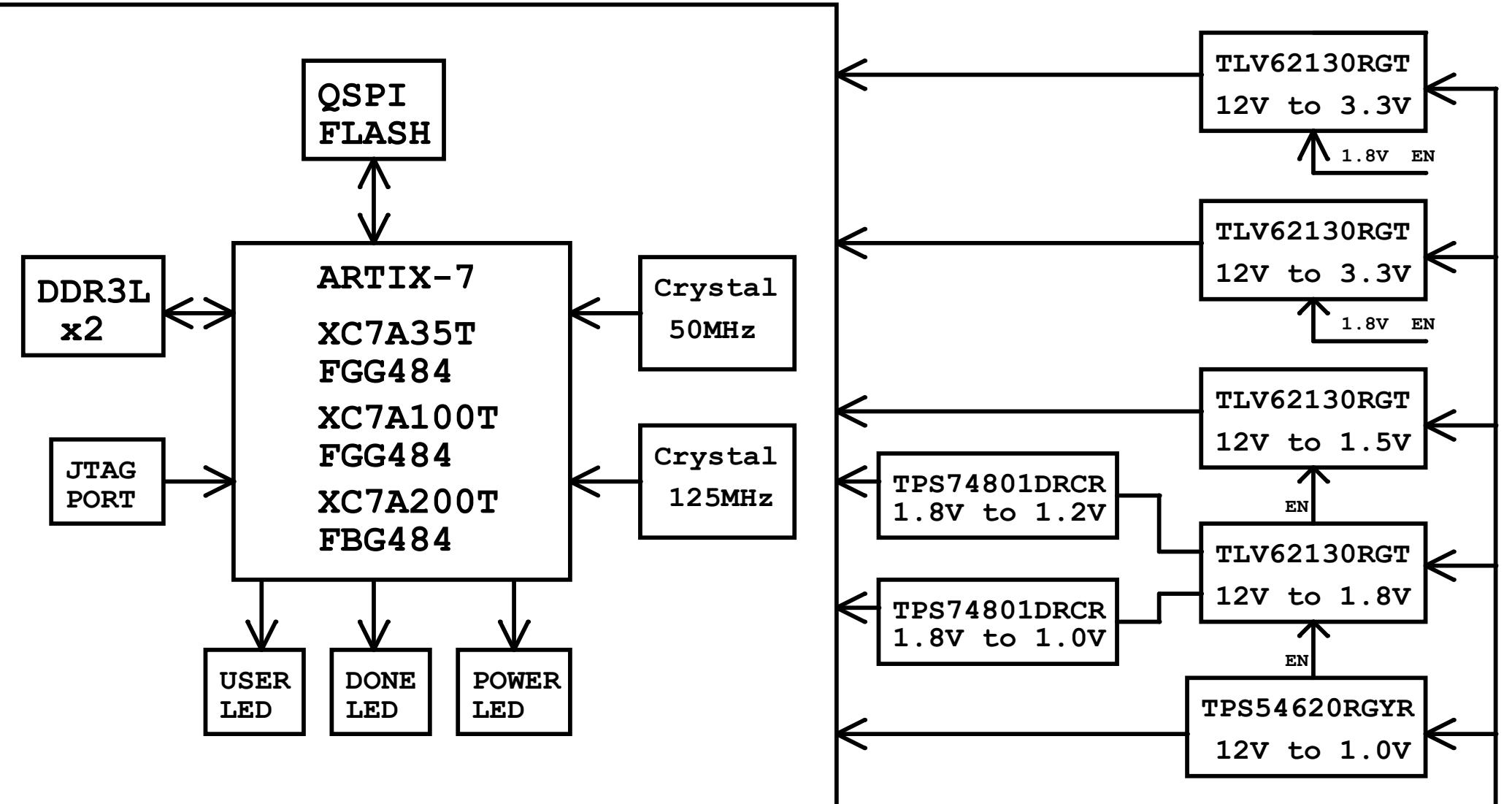
版本号	日期	设计	描述
V1.0	2020-09-29	xgh	初始版本
V1.0	2021-03-05	xgh	整理资料发布，稳定版

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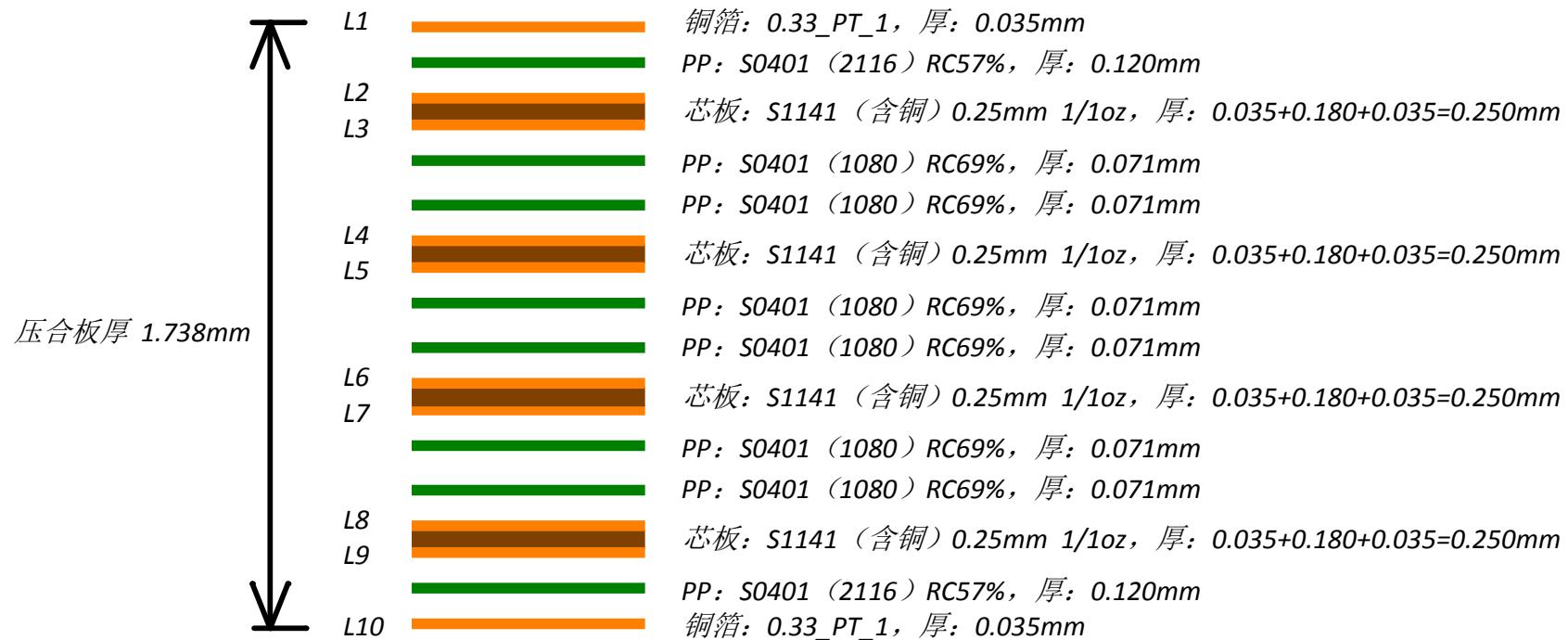
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Title: Wildfire\_XC7A35T核心板\_原理图

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# ***ebf\_xc7a35t/100t/200t core board***

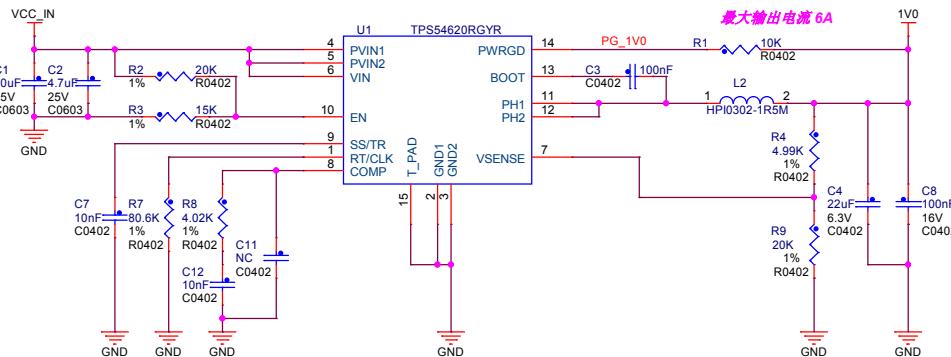
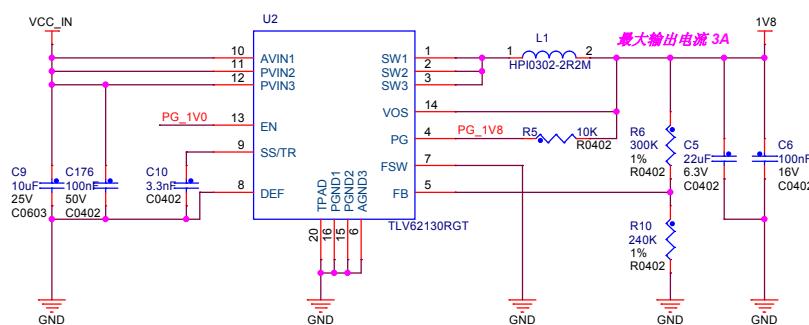
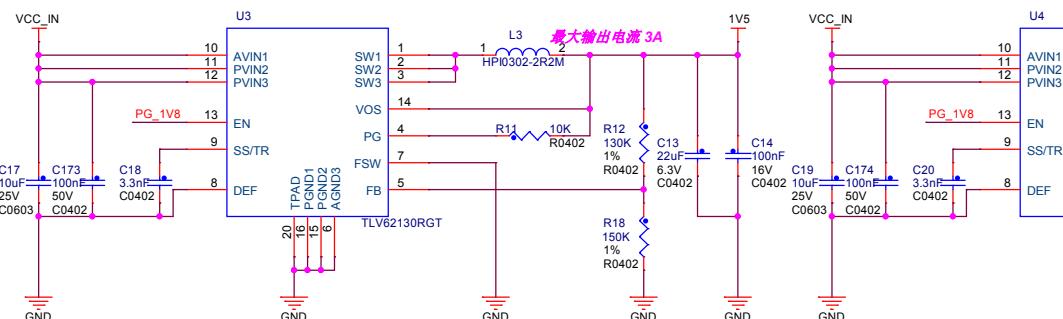
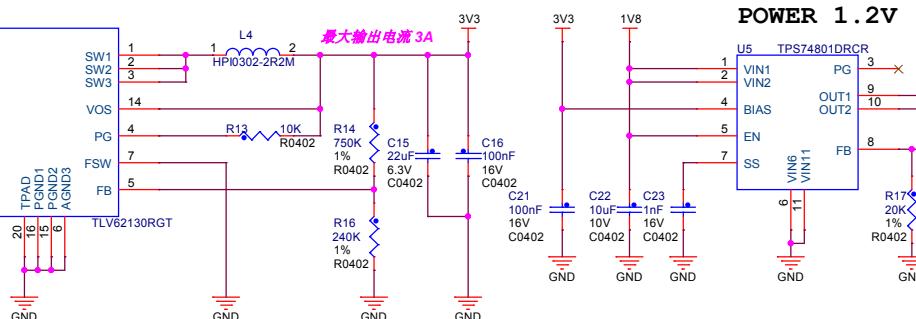


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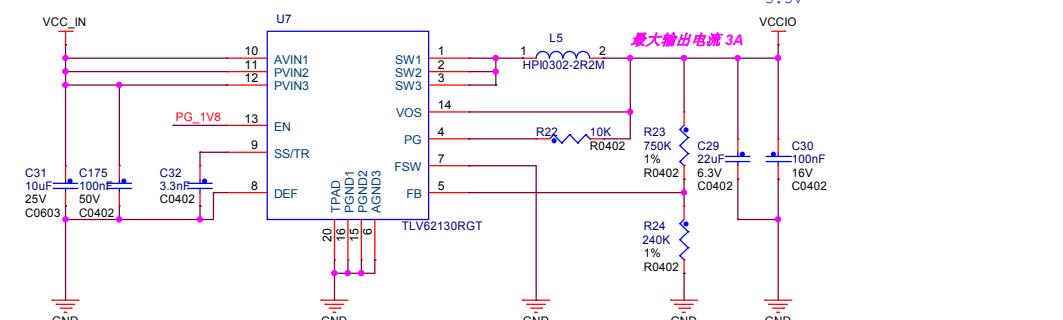
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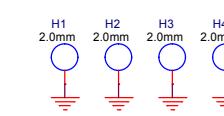
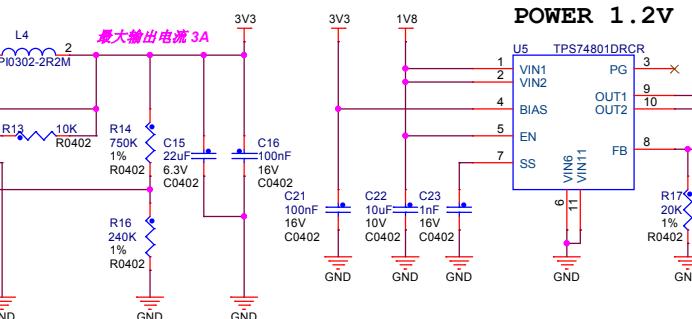
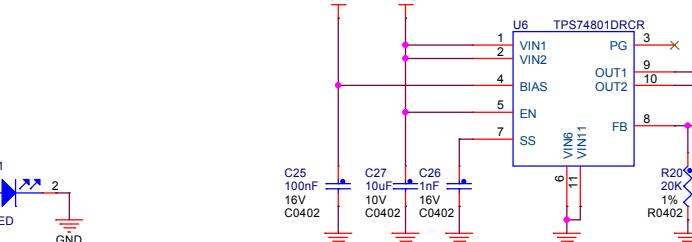
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**POWER 5V~12V****POWER 1.0V****POWER 1.8V****POWER 1.5V****POWER 3.3V**

**1.0V->1.8V->1.5V&3.3V&VCCIO**

**POWER 3.3V****POWER\_LED**

**定位孔**

**POWER 1.2V****POWER 1.0V**

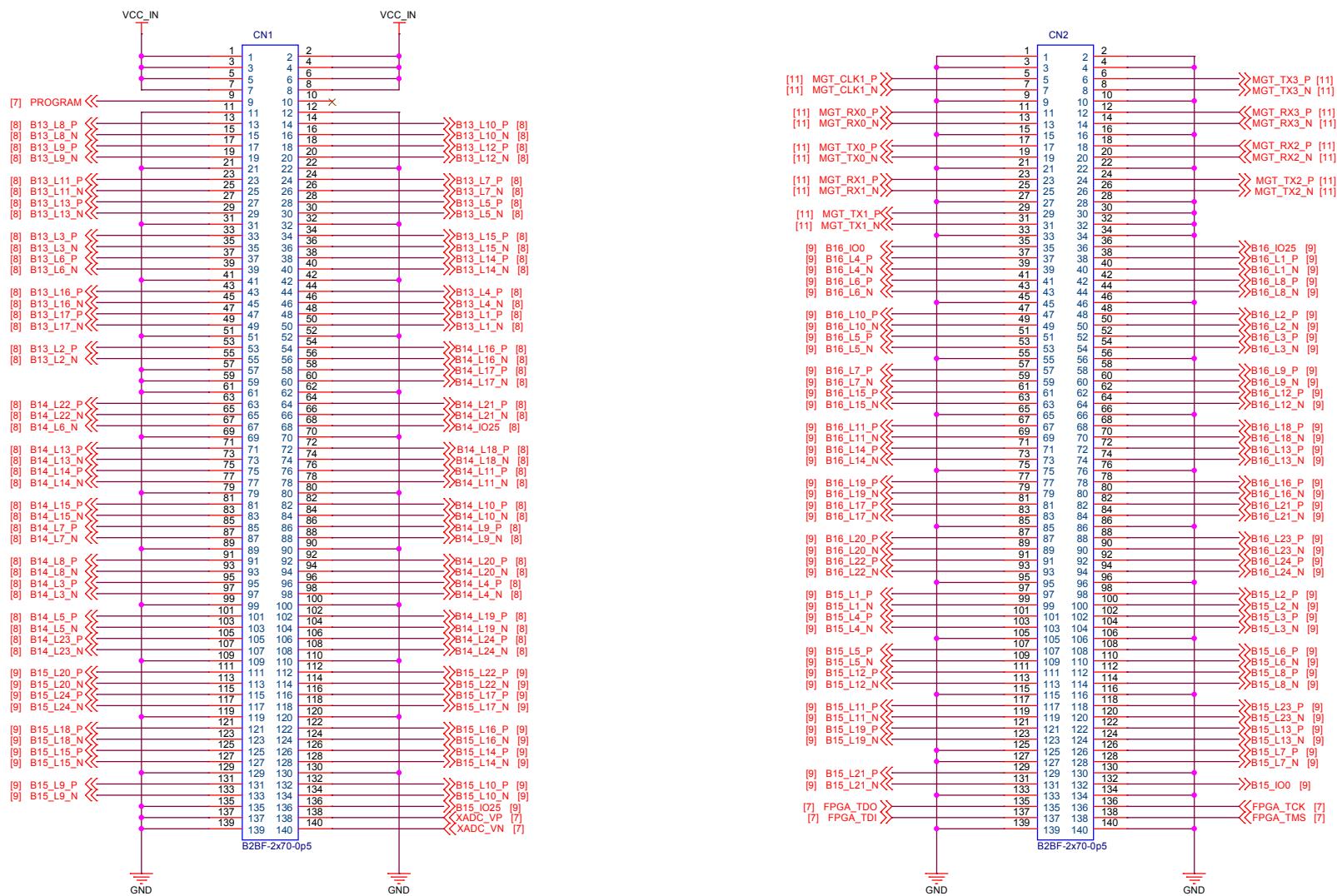
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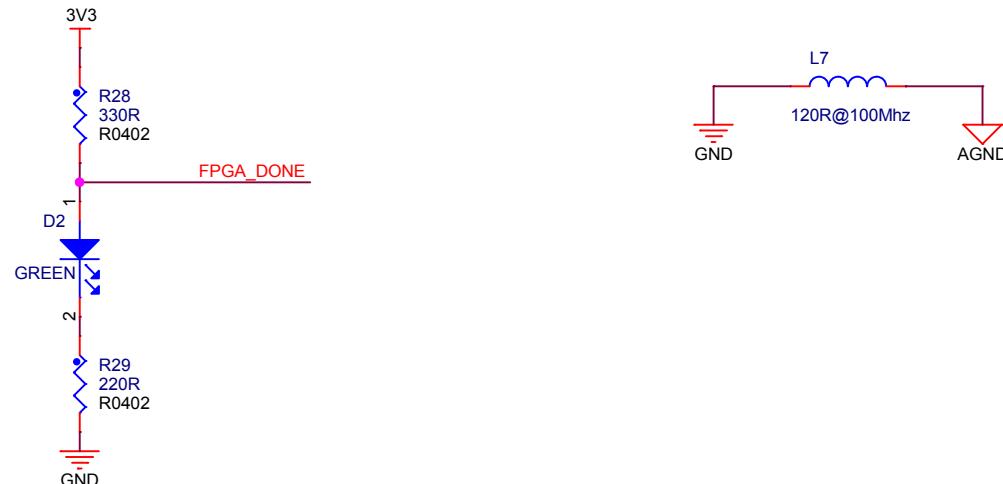
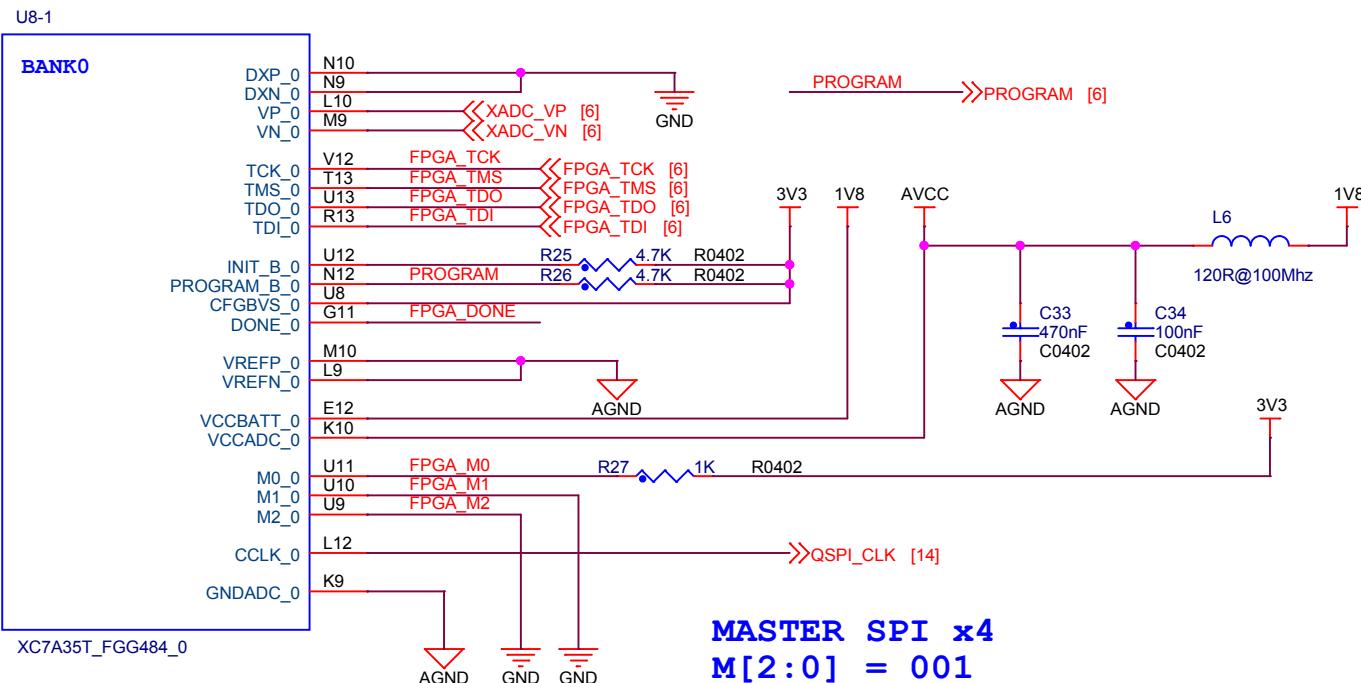
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BTB连接器



# BANK0



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# BANK13/14

U8-2

**BANK13**

IO_L0_N_T0_13	Y17
IO_L1P_T0_13	AA16
IO_L1N_T0_13	AB16
IO_L2P_T0_13	AB17
IO_L2N_T0_13	AA13
IO_L3P_T0_DQS_13	AB13
IO_L3N_T0_DQS_13	AA15
IO_L4P_T0_13	AB15
IO_L4N_T0_13	Y13
IO_L5P_T0_13	AA14
IO_L5N_T0_13	W14
IO_L6P_T0_13	Y14
IO_L6N_T0_VREF_13	AB11
IO_L7P_T1_13	AB12
IO_L7N_T1_13	AA9
IO_L8P_T1_13	AB10
IO_L8N_T1_13	AA10
IO_L9P_T1_DQS_13	AA11
IO_L9N_T1_DQS_13	V10
IO_L10P_T1_13	W10
IO_L10N_T1_13	Y11
IO_L11P_T1_SRCC_13	Y12
IO_L11N_T1_SRCC_13	W11
IO_L12P_T1_MRCC_13	W12
IO_L12N_T1_MRCC_13	V13
IO_L13P_T2_MRCC_13	V14
IO_L13N_T2_MRCC_13	U15
IO_L14P_T2_SRCC_13	V15
IO_L14N_T2_SRCC_13	T14
IO_L15P_T2_DQS_13	T15
IO_L15N_T2_DQS_13	W15
IO_L16P_T2_13	W16
IO_L16N_T2_13	T16
IO_L17P_T2_13	U16
IO_L17N_T2_13	

XC7A35T\_FGG484\_0

XC7A35T无BANK13

XC7A100T/XC7A200T有BANK13

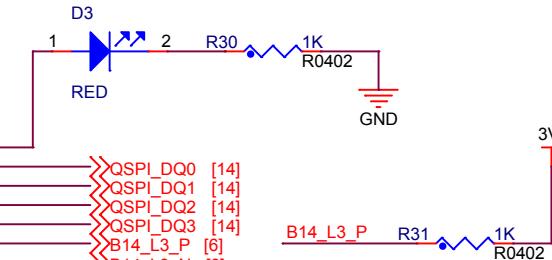
U8-3

**BANK14**

IO_0_14	P20
IO_L1P_T0_D00_MOS_14	P22
IO_L1N_T0_D01_DIN_14	R22
IO_L2P_T0_D02_14	P21
IO_L2N_T0_D03_14	U22
IO_L3P_T0_DQS_PUDC_B_14	V22
IO_L3N_T0_DQS_EMCLK_14	T21
IO_L4P_T0_D04_14	U21
IO_L4N_T0_D05_14	P19
IO_L5P_T0_D06_14	R19
IO_L5N_T0_D07_14	I19
IO_L6P_T0_FCS_B_14	T20
IO_L6N_T0_D08_VREF_14	W21
IO_L7P_T1_D09_14	W22
IO_L7N_T1_D10_14	AA20
IO_L8P_T1_D11_14	AA21
IO_L8N_T1_D12_14	Y21
IO_L9P_T1_DQS_14	Y22
IO_L9N_T1_DQS_D13_14	IO_L10P_T1_D14_14
AB21	AB22
IO_L10N_T1_D15_14	IO_L11P_T1_SRCC_14
U20	IO_L11N_T1_SRCC_14
V20	IO_L12P_T1_MRCC_14
W19	IO_L12N_T1_MRCC_14
W20	Y18
IO_L13P_T2_MRCC_14	IO_L13N_T2_MRCC_14
Y19	IO_L14P_T2_SRCC_14
V18	IO_L14N_T2_SRCC_14
IO_L15P_T2_DQS_RDWR_B_14	AA19
AB20	IO_L15N_T2_DQS_DOUT_CSO_B_14
W17	IO_L16P_T2_CSI_B_14
IO_L16N_T2_A15_D31_14	IO_L16P_T2_A14_D30_14
AA18	IO_L17P_T2_A13_D29_14
AB18	IO_L17N_T2_A13_D29_14
U17	IO_L18P_T2_A12_D28_14
U18	IO_L18N_T2_A11_D27_14
P14	IO_L19P_T3_A10_D26_14
R14	IO_L19N_T3_A09_D25_VREF_14
R18	IO_L20P_T3_A08_D24_14
T18	IO_L20N_T3_A07_D23_14
N17	IO_L21P_T3_DQS_14
P17	IO_L21N_T3_DQS_A06_D22_14
P15	IO_L22P_T3_A05_D21_14
R16	IO_L22N_T3_A04_D20_14
N13	IO_L23P_T3_A03_D19_14
N14	IO_L23N_T3_A02_D18_14
P16	IO_L24P_T3_A01_D17_14
R17	IO_L24N_T3_A00_D16_14
N15	IO_25_14

XC7A35T\_FGG484\_0

**USER\_LED**



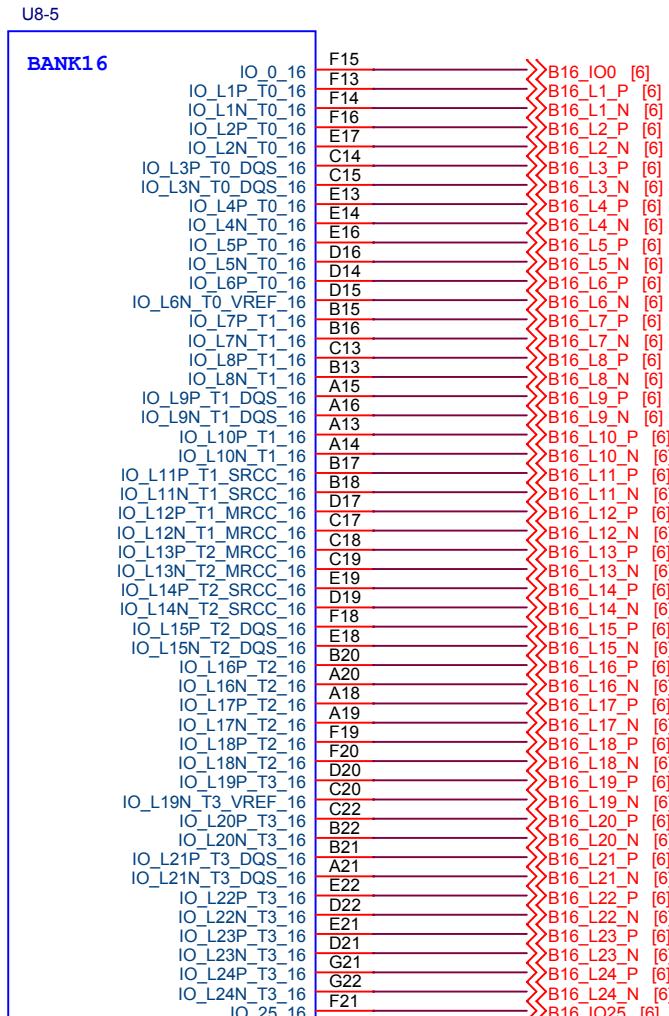
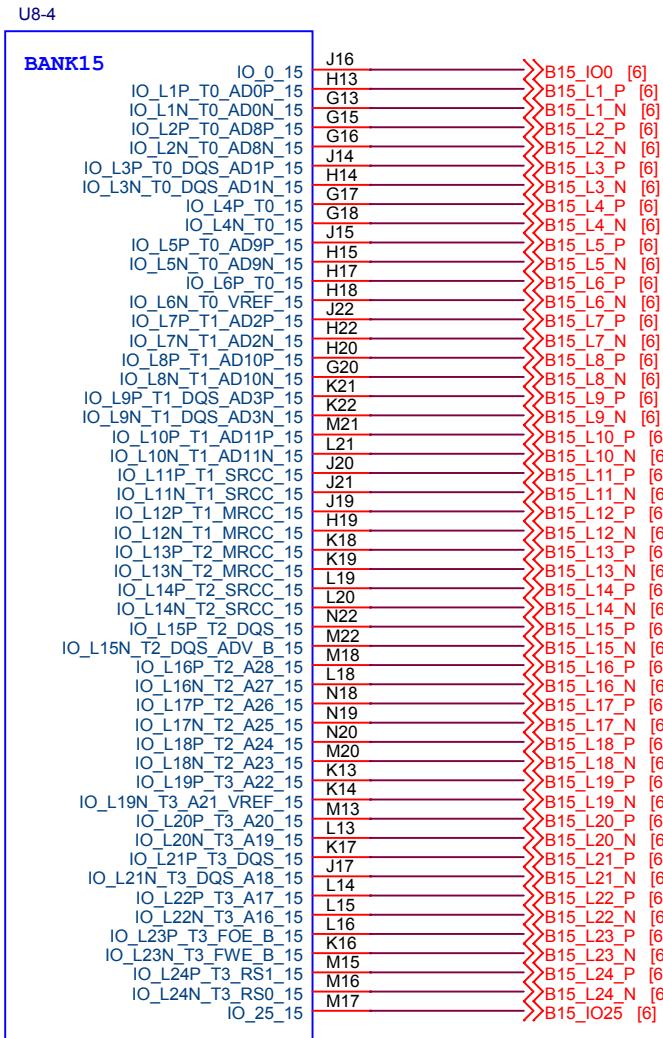
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野火\_XC7A35T核心板\_原理图

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A4	BANK34/BANK35/BANK13	V1.0

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# BANK15/16



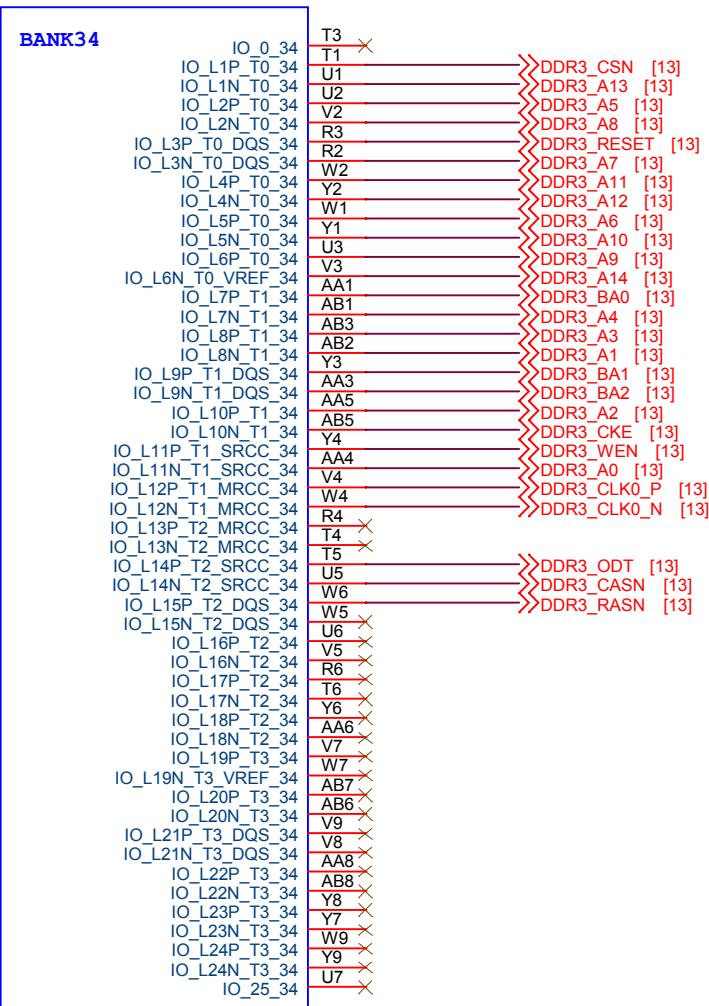
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Title  
野火\_XC7A35T核心板\_原理图

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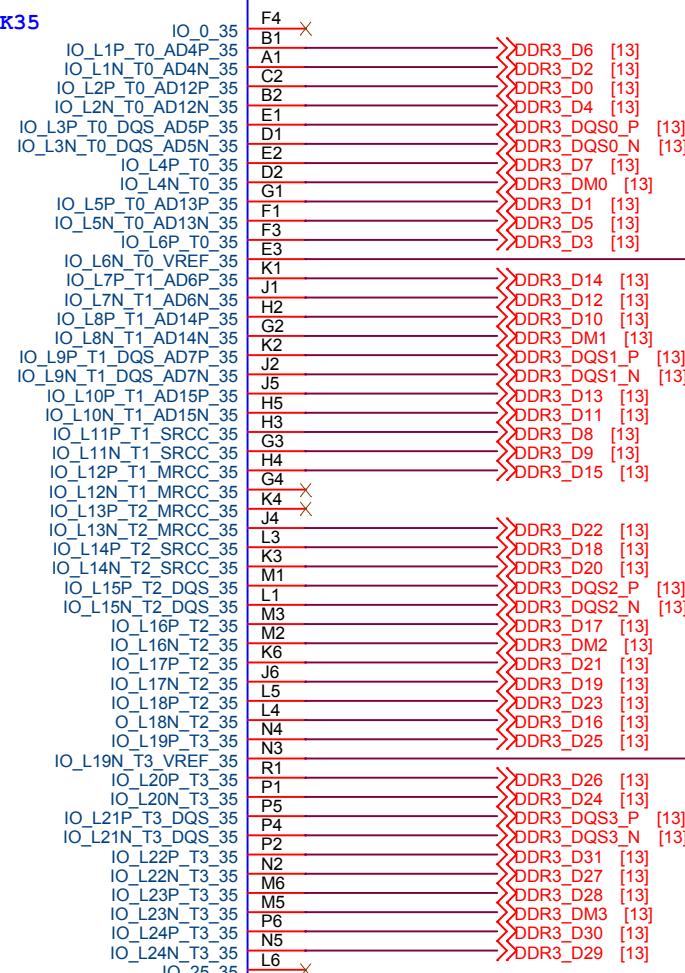
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U8-6



XC7A35T\_FGG484\_0

U8-7



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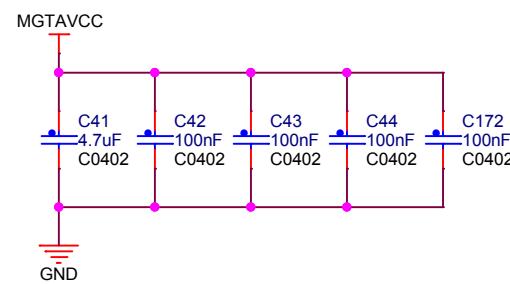
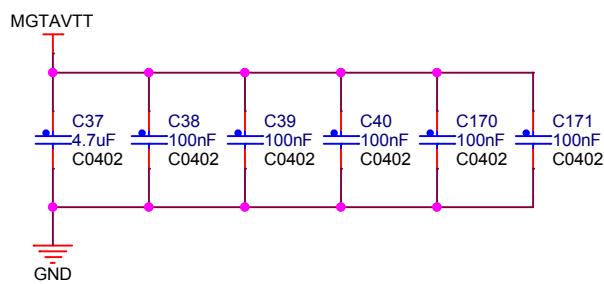
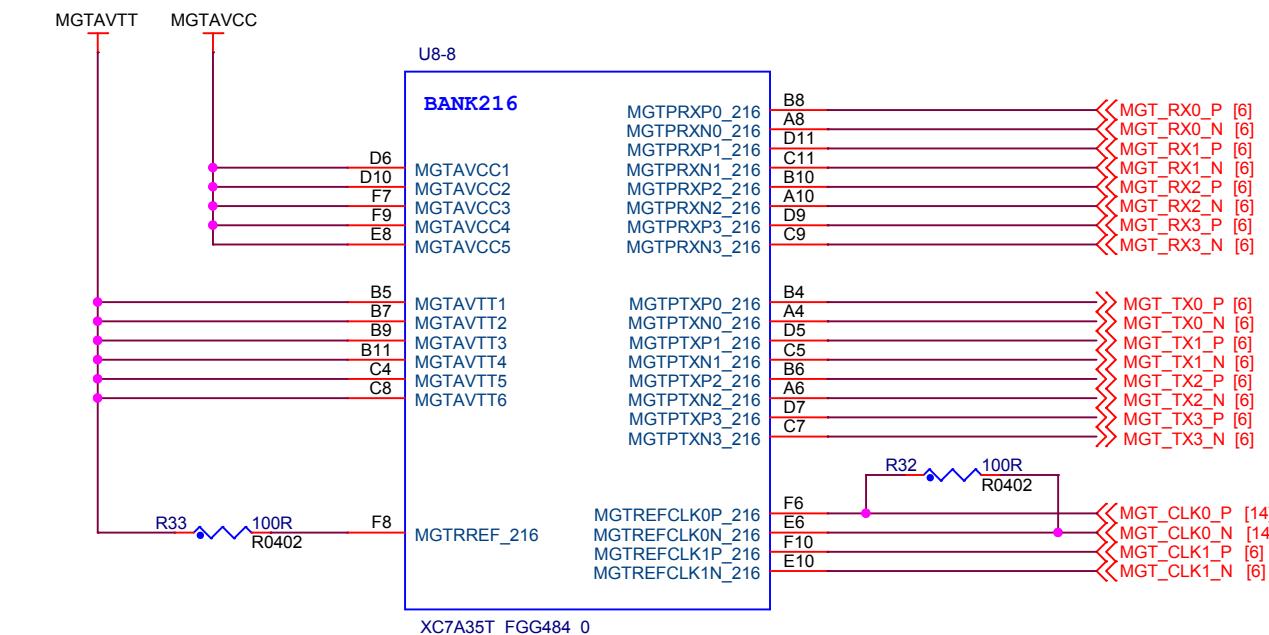
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野火\_XC7A35T核心板\_原理图

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# BANK216



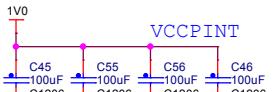
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# IO\_POWER/CORE\_POWER



GND



GND



GND

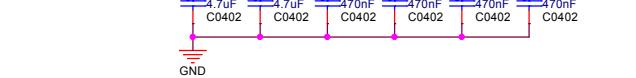
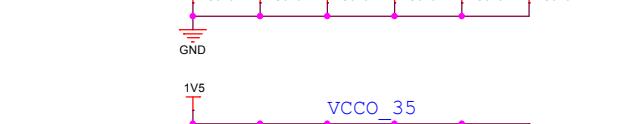
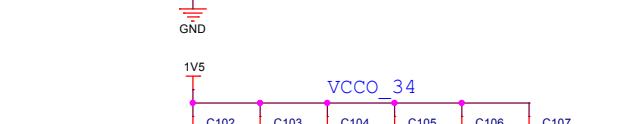
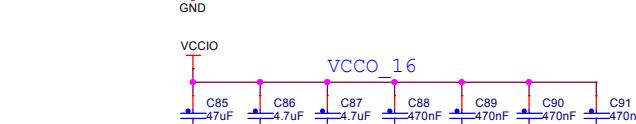
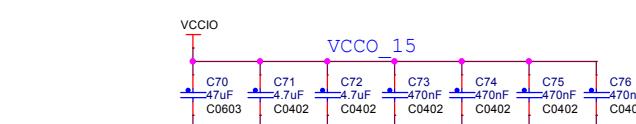
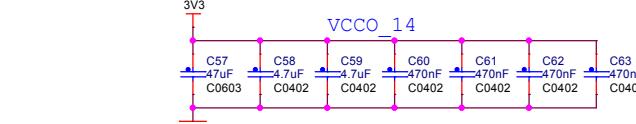
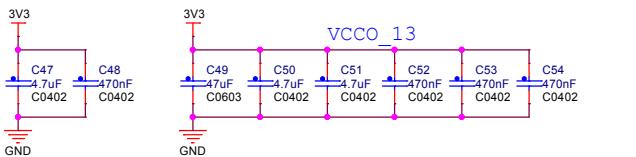
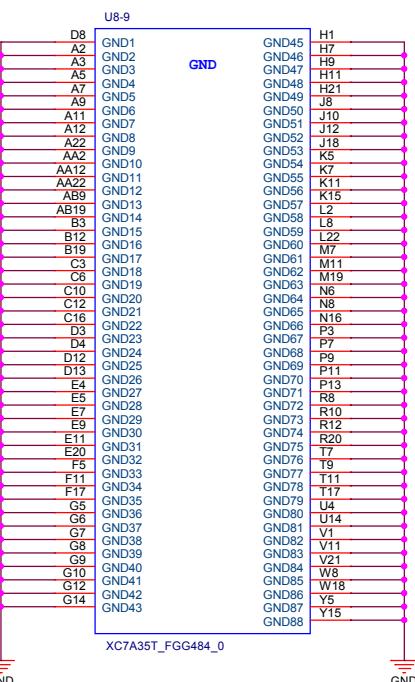
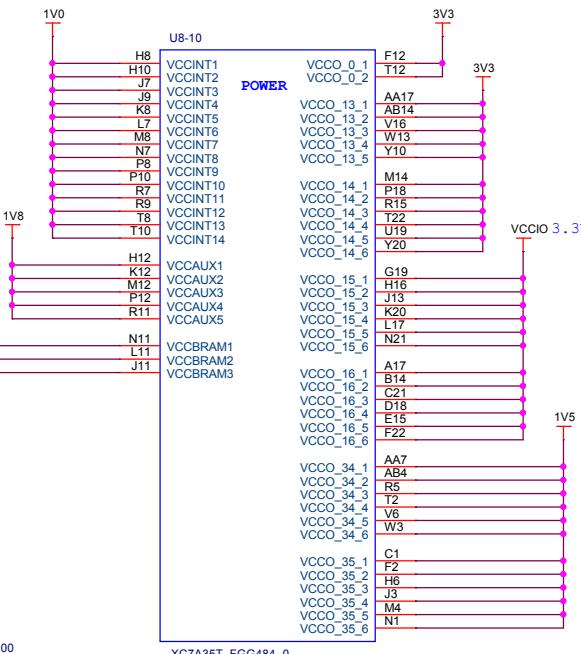


GND



GND

所有退耦电容靠近电源引脚放置

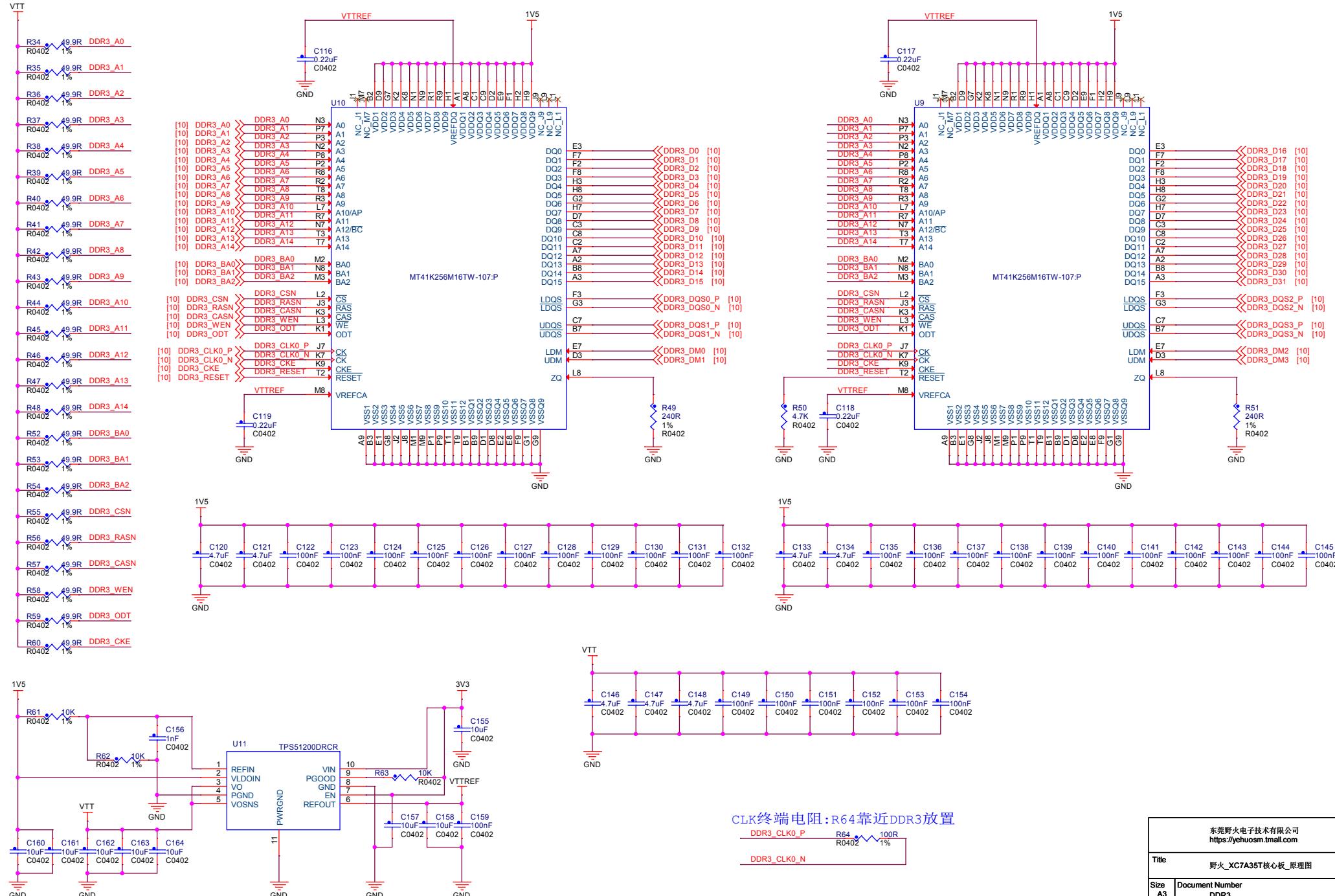


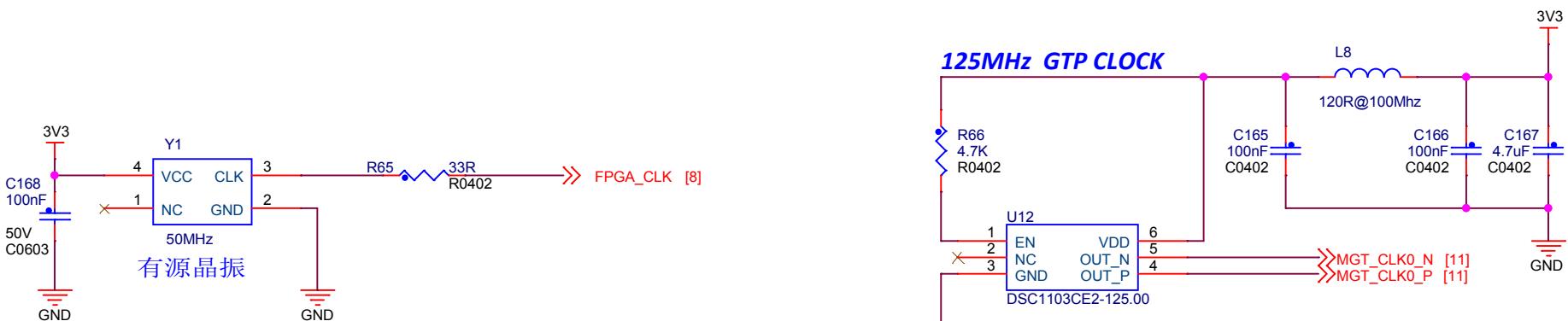
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Title 野火\_XC7A35T核心板\_原理图

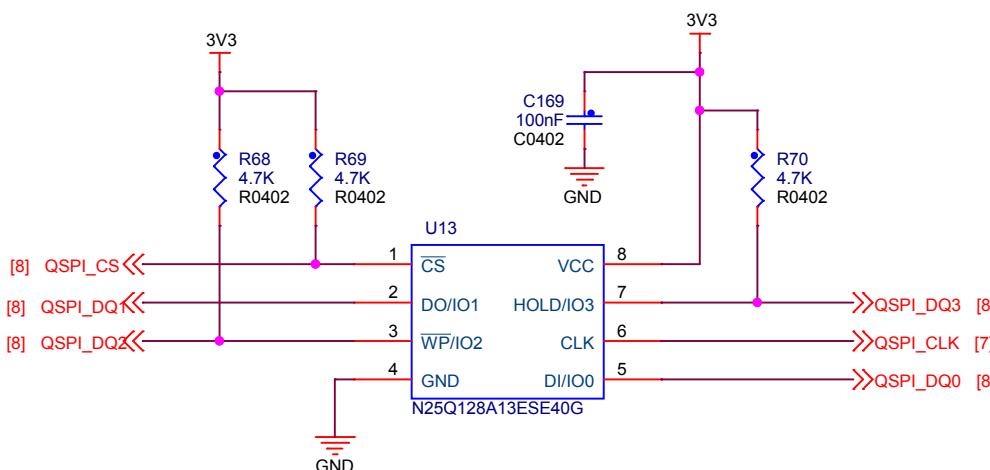
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**CLOCK****SPI\_FLASH**

容量: 16M字节



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