

Quartus II - C:/Users/Asus/Desktop/New folder/func201 - func201.v [func201.v]

File Edit View Project Assignments Processing Tools Window Help

func201

Project Navigator

Entity	Logic Cells	LC Registers	Memory Bits
FLEX10KE: AUTO	1 (1)	0	0
func201			

Hierarchy Files Design Units

Tasks

Flow: Compilation

Task	Time
Compile Design	00:00:05
Analysis & Synthesis	00:00:01
Filter (Place & Route)	00:00:01
Assembler (Generate programming files)	00:00:01
Classic Timing Analysis	00:00:01
EDA Netlist Writer	00:00:01
Program Device (Open Programmer)	

func201.v

```
1 module func201 (x1,x2,x3,s,f);
2   input x1,x2,x3;
3   input [2:0]s;
4   output reg f;
5   always @ (x1,x2,x3,s)
6   case (s)
7     0:f=1;
8     1:f=0;
9     2:f=1;
10    3:f=1;
11    4:f=1;
12    5:f=1;
13    6:f=1;
14    7:f=0;
15
16  endcase
17  endmodule
```

Compilation Report - Flow Summary

Simulation Report - Simulation Waveforms

Type Message

- Info: Using vector source file "C:/Users/Asus/Desktop/New folder/func201.vwf"
- Info: Option to preserve fewer signal transitions to reduce memory requirements is enabled
- Info: Simulation partitioned into 1 sub-simulations
- Info: Simulation coverage is 71.43 %
- Info: Number of transitions in simulation is 31
- Info: Quartus II Simulator was successful. 0 errors, 0 warnings

System [56] Processing [9] Extra Info Info [9] Warning Critical Warning Error Suppressed Flag

Message: 0 of 16

Location:

For Help, press F1

Ln 17, Col 10

ENG INTL

10:55 AM 2/23/2022

