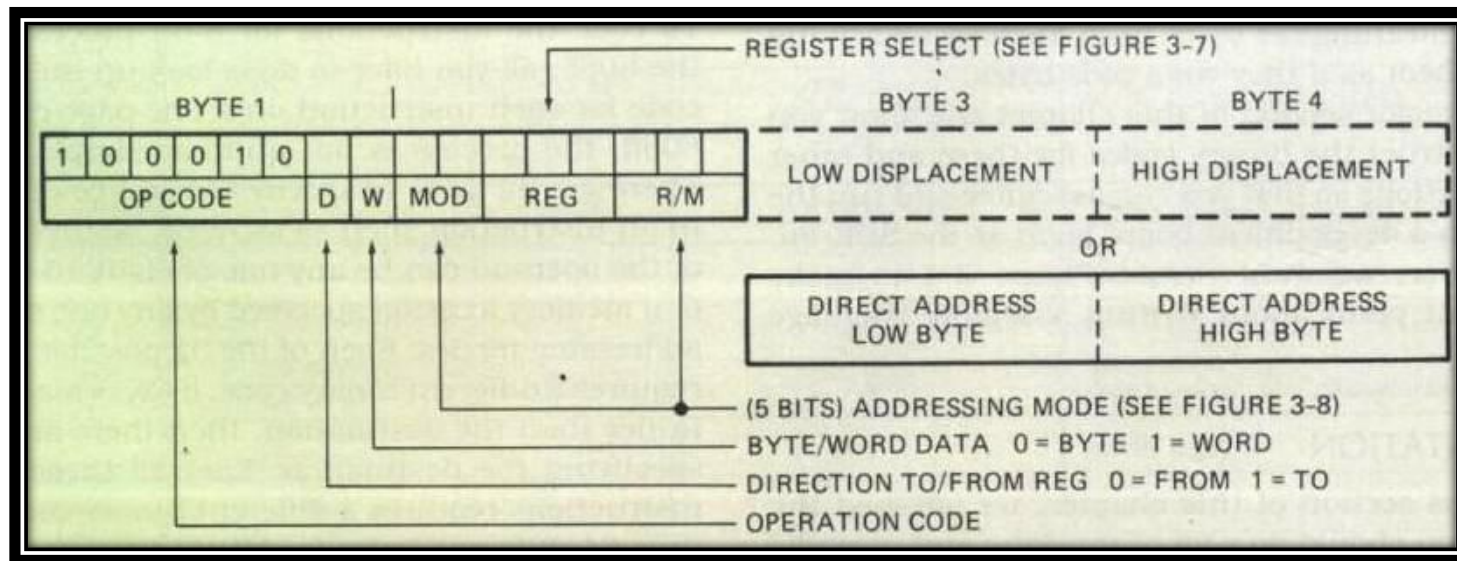


Machine Code Construction for 8086 Microprocessor

Coding template for 8086 instructions which 'MOV' data between 'registers' or between a 'register' and a 'memory location': (opcode 100010)

REGISTER		CODE
	W=1	W=0
AL	AX	000
BL	BX	011
CL	CX	001
DL	DX	010
AH	SP	100
BH	DI	111
CH	BP	101
DH	SI	110
SEGREG		CODE
	CS	01
	DS	11
	ES	00
	SS	10

Instruction codes for 8086 registers



R/M \ MOD	MOD			
	00	01	10	11
				W = 0 W = 1
000	[BX] + [SI]	[BX] + [SI] + d8	[BX] + [SI] + d16	AL AX
001	[BX] + [DI]	[BX] + [DI] + d8	[BX] + [DI] + d16	CL CX
010	[BP] + [SI]	[BP] + [SI] + d8	[BP] + [SI] + d16	DL DX
011	[BP] + [DI]	[BP] + [DI] + d8	[BP] + [DI] + d16	BL BX
100	[SI]	[SI] + d8	[SI] + d16	AH SP
101	[DI]	[DI] + d8	[DI] + d16	CH BP
110	d16 (direct address)	[BP] + d8	[BP] + d16	DH SI
111	[BX]	[BX] + d8	[BX] + d16	BH DI

For 1st consideration:

Resolve: 'D', 'W', 'REG'

For remaining part:

Resolve: 'MOD', 'R/M', (Displacement / Direct Address)