



CSE 430(B)

Topic: 4 Bit Shifter

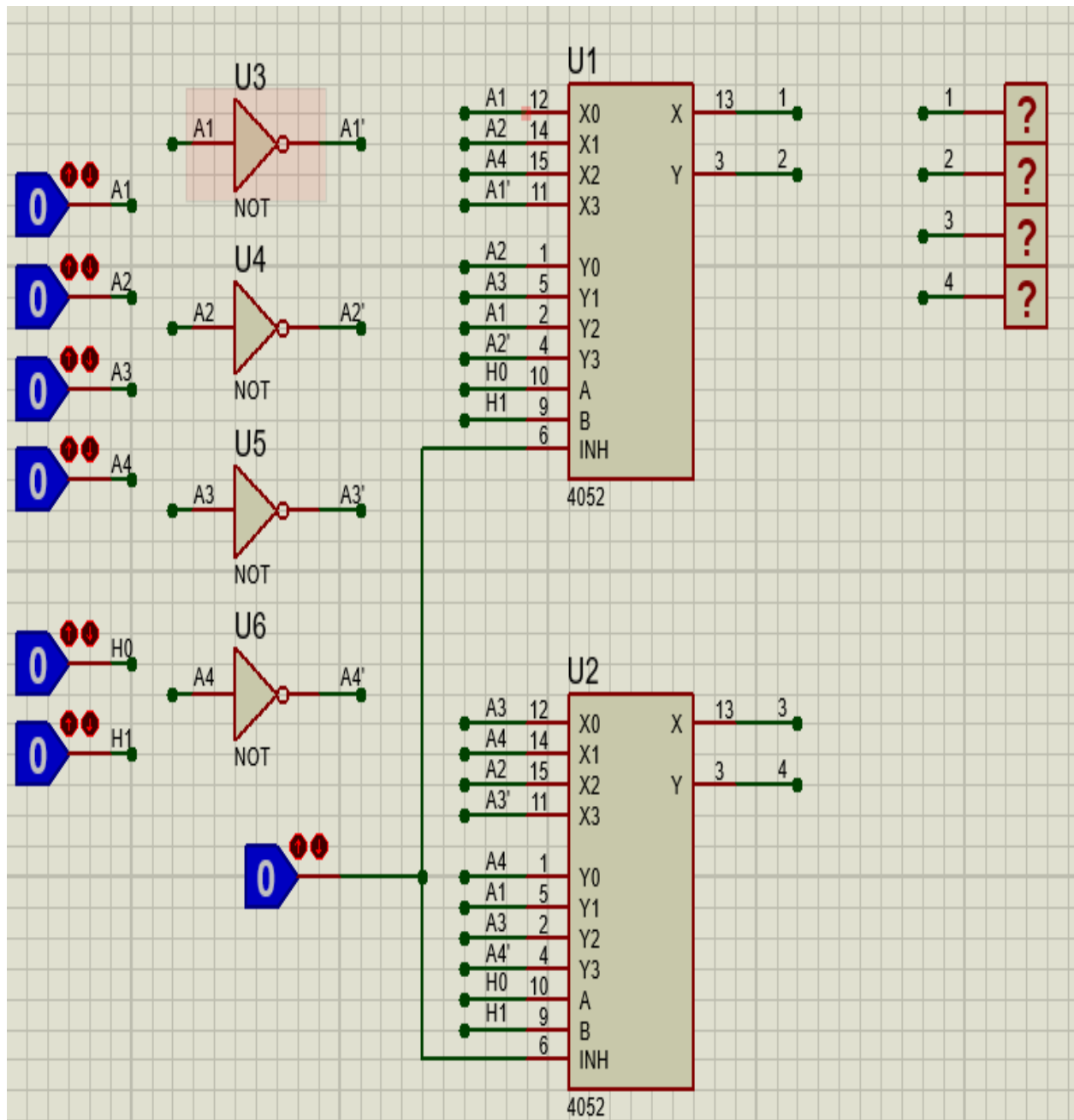
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Submitted To:

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TASK-1: 4 bit Shifter

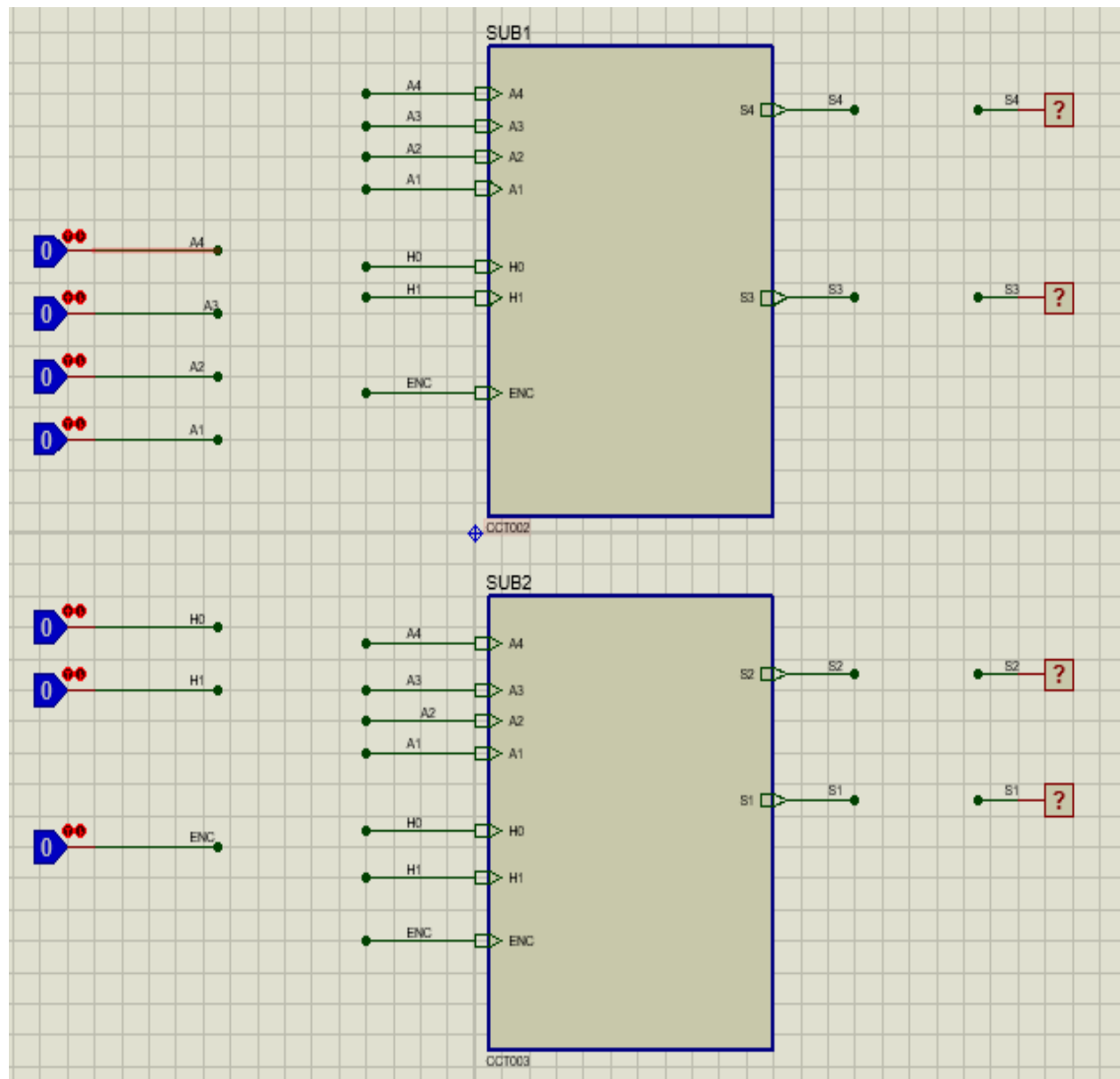


Truth Table:

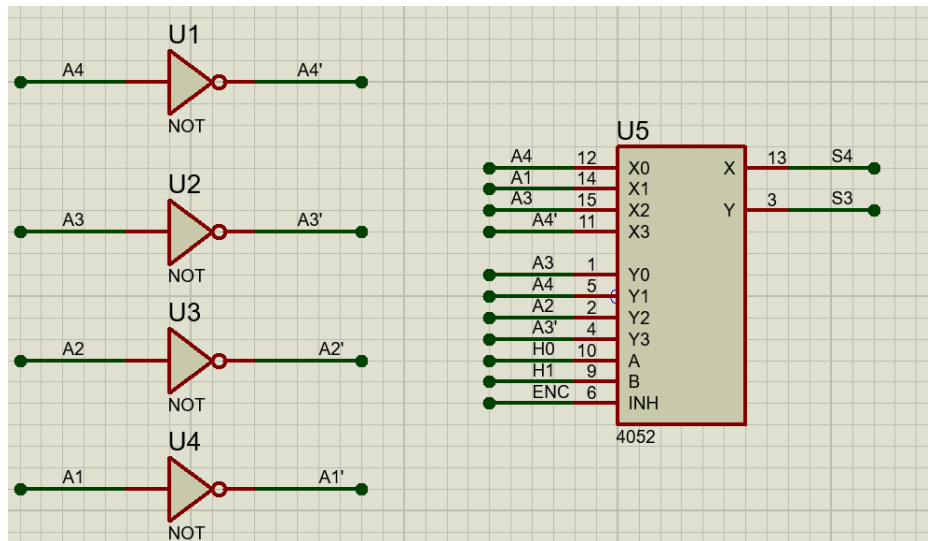
Input	00	01	10	11
0000	0000	0000	0000	1111
0001	0001	1000	0010	1110
0010	0010	0001	0100	1101
0011	0011	1001	0110	1100
0100	0100	0010	1000	1011
0101	0101	1010	1010	1010
0110	0110	0011	1100	1001
0111	0111	1011	1110	1000
1000	1000	0100	0001	0111
1001	1001	1100	0011	0110
1010	1010	0101	0101	0101
1011	1011	1101	0111	0100
1100	1100	0110	1001	0011
1101	1101	1110	1011	0010
1110	1110	0111	1101	0001
1111	1111	1111	1111	0000

TASK-2: Sub Circuit of 4 bit Shifter

Parent Sheet:



Child Sheet of Sub Circuit-1:



Child Sheet of Sub Circuit-2:

