

RECTIFIER

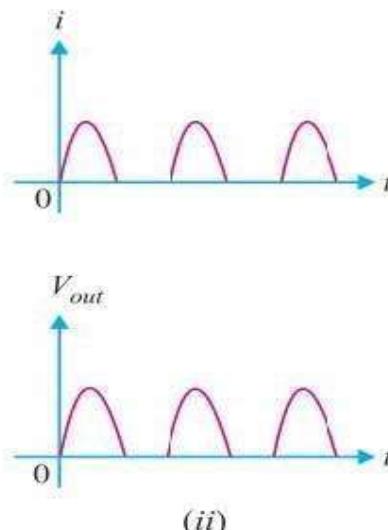
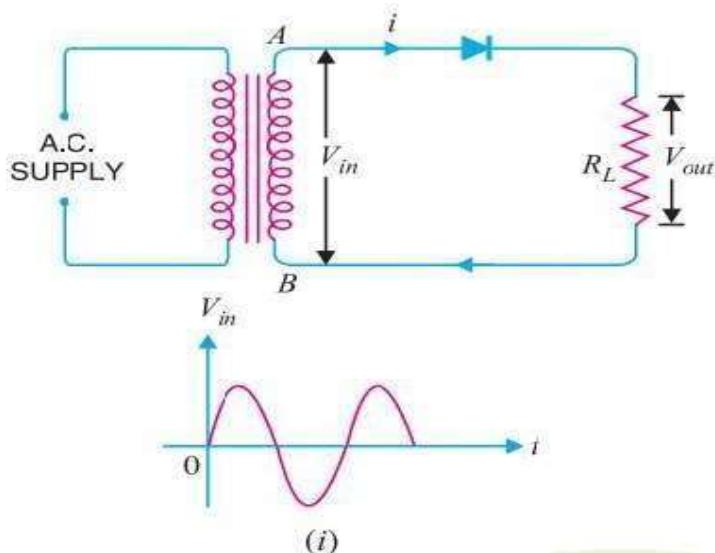


Rectifiers

- Rectifier: Rectifier is that circuit, that converts ac to dc.
- The following two types of rectifier circuit can be used:
 - I. Half wave rectifier
 - II. Full wave rectifier

Half wave Rectifier

- The process of removing one-half the input signal to establish a dc level is called *half-wave rectification*.
□
- In □ Half wave rectification, the rectifier conducts current during positive half cycle of input ac signal only.
- Negative half cycle is suppressed or clipped.



Half wave Rectifier

- AC voltage across secondary terminals AB changes its polarity after each half cycle.
- During negative half cycle terminal A is negative so diode reversed biased and conduct no current.so current flows through diode during positive half cycle only.
- In this way current flows through load RL in one direction

Half wave Rectifier

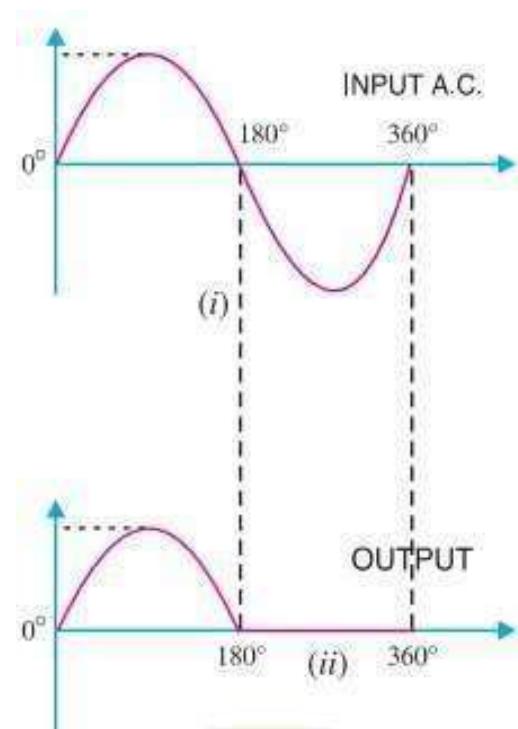
Disadvantage of Half wave rectifier:

- Since, power is delivered only during one half of the cycle of the input alternating voltage, therefore, its power output and rectification frequency is low.
- Transformer utilization factor is also low.
- The DC output power produced from the half wave rectifier is not satisfactory to make a general power supply.

Half wave Rectifier

- Output frequency of HWR:
 - Output frequency of HWR is equal to input frequency.
 - This means when input ac completes one cycle, rectified wave also completes one cycle.

$$f_{out} = f_{in}$$

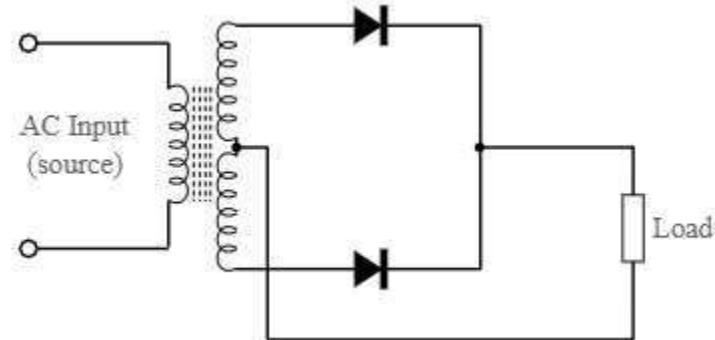
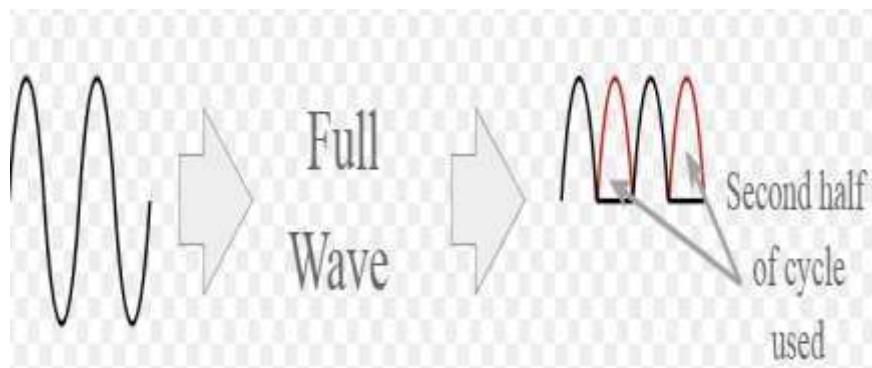


Full-Wave Rectifier

In Full wave rectification current flow through the load in same direction for both half cycle of input ac.

This can be achieved with two diodes working alternatively.

For one half cycle one diode supplies current to load and for next half cycle another diode works.



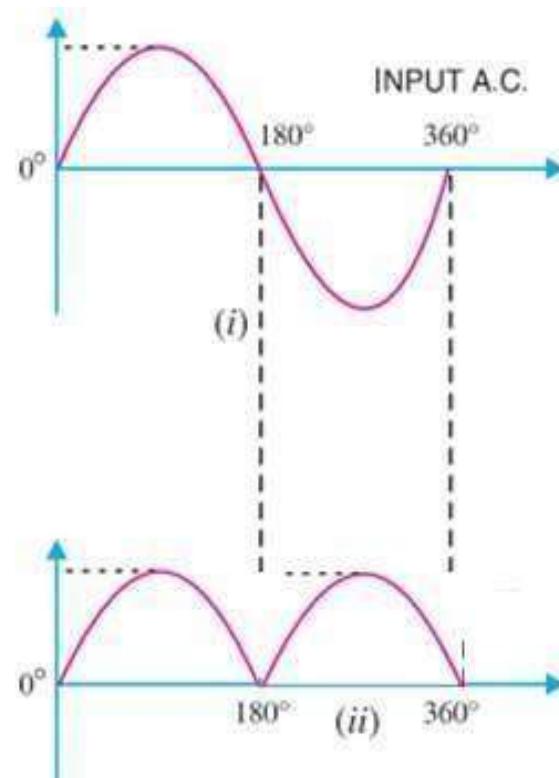
Full wave Rectifier

- Output frequency of FWR:

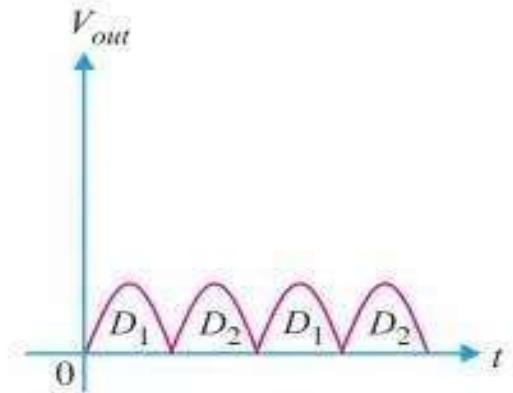
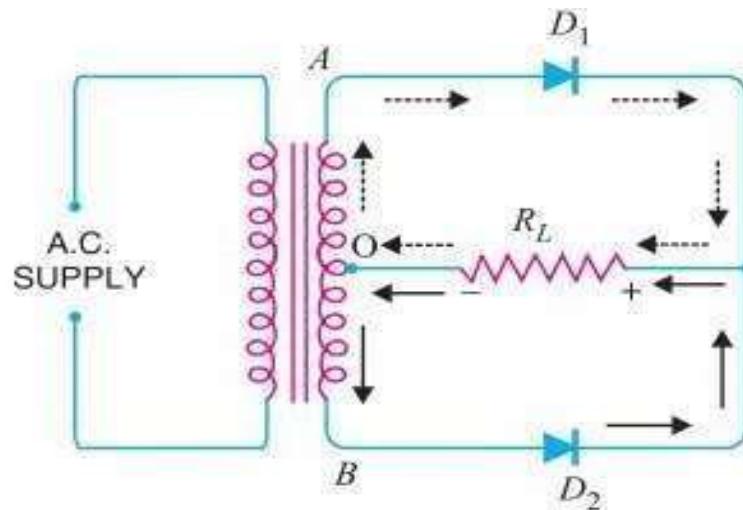
- Output frequency of FWR is equal to double of input frequency.

- This means when input ac completes one cycle, rectified wave completes two cycles

$$f_{out} = 2 f_{in}$$

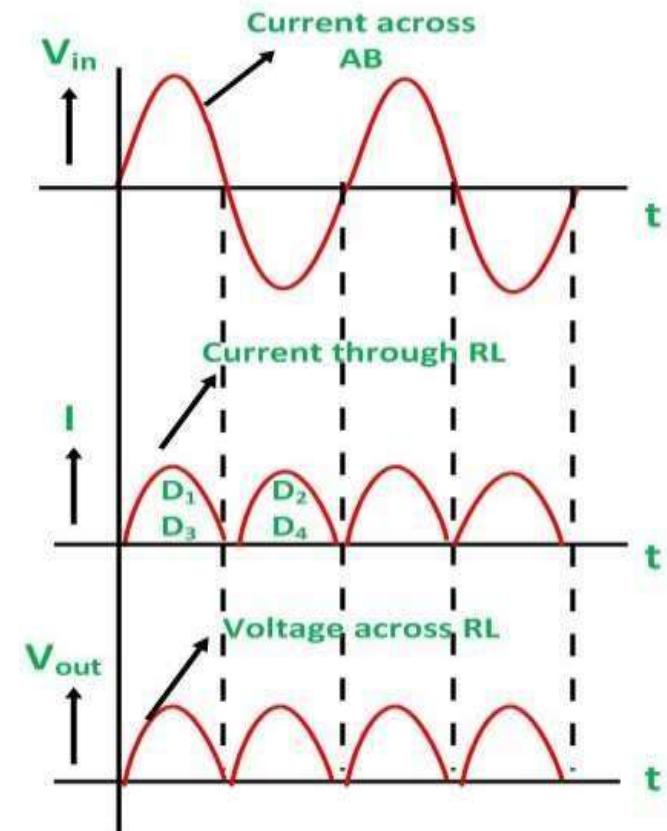
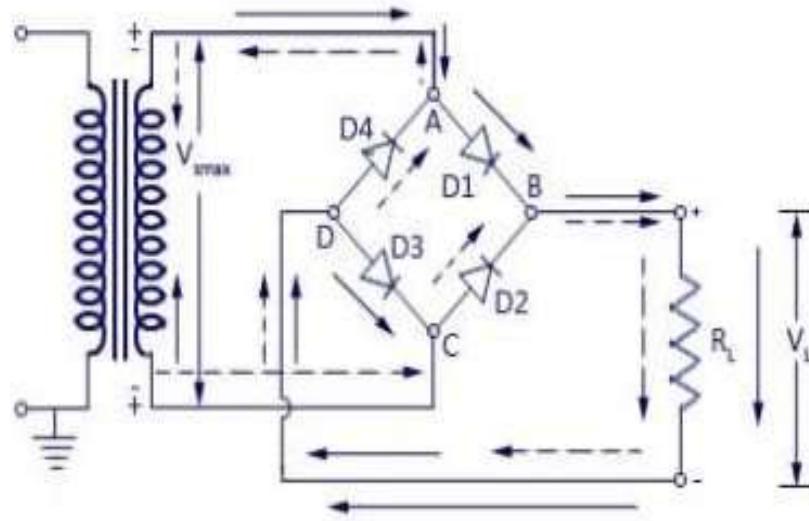


Centre Tap Full Wave Rectifier



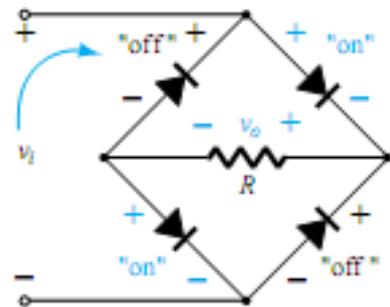
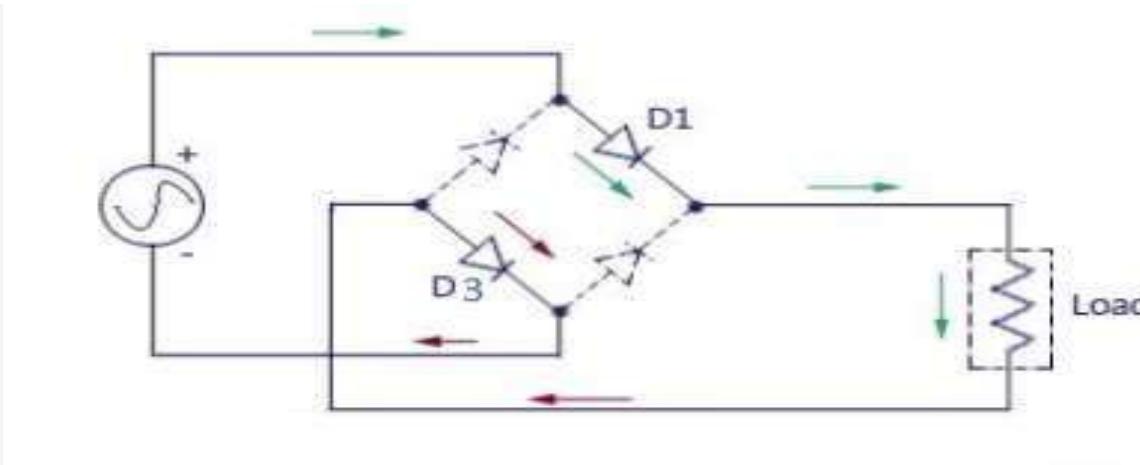
- Circuit has two diodes D_1 , D_2 and a centre tap transformer.
- During positive half cycle Diode D_1 conducts and during negative half cycle Diode D_2 conducts.
- It can be seen that current through load RL is in the same direction for both cycle.

Full Wave Bridge Rectifier



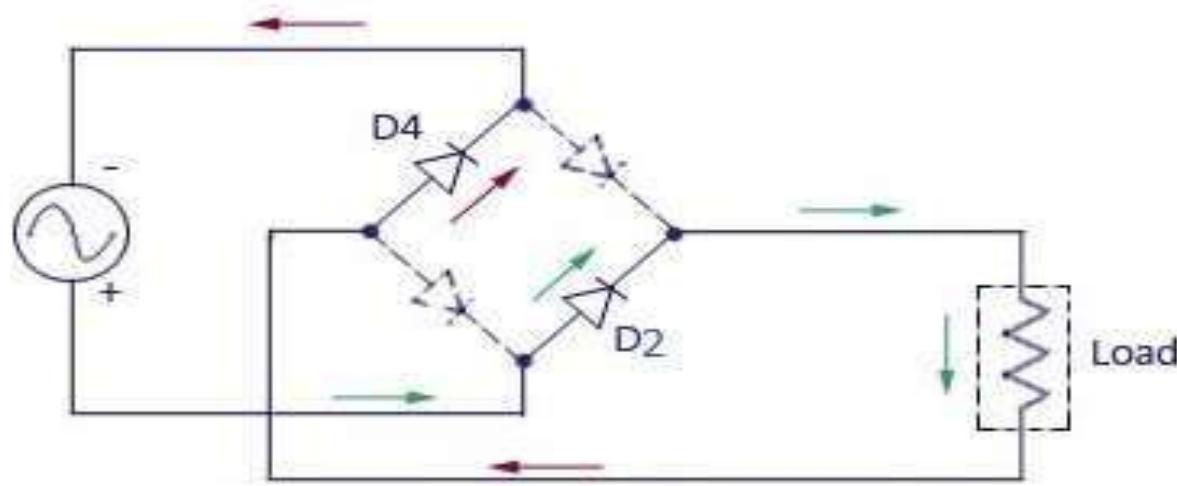
- Consists of 4 diodes instead of 2.

Full Wave Bridge Rectifier



➤ During first half cycle D1 and D3 are conducting while D2 and D4 are in the “off” state.

Full Wave Bridge Rectifier



- During 2nd half cycle D2 and D4 are conducting while D1 and D3 are in the “off” state.

Full Wave Bridge Rectifier



Advantage:

- I. Need for centre tap transformer is eliminated.
- II. Output is twice than that of centre tap circuit.

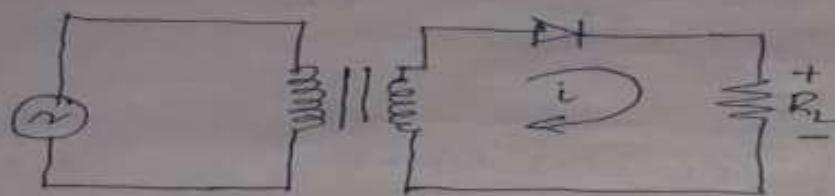
Disadvantage

- I. Requires 4 diodes.
- II. Internal resistance voltage drop is twice than that of Centre Tap Circuit.

Rectifier Performance

- ① Conduction Stains
- ② Peak Inverse Voltage
- ③ DC Current
- ④ DC Voltage
- ⑤ Rms Value of Current
- ⑥ Rms Value of Voltage
- ⑦ Ripple Factor
- ⑧ Efficiency
- ⑨ Transformer Utilization Factor
- ⑩ Regulation

Half wave Rectifier



$$i = I_m \sin \omega t \quad \text{when } 0 \leq \omega t \leq \pi$$

$$i = 0 \quad \text{when } \pi \leq \omega t \leq 2\pi$$

$$I_m = \text{Peak Value} = \frac{V_m}{R_f + R_L}$$

① PIV (Peak Inverse Voltage)

The max. voltage appears across the diode when diode is nonconducting (i.e. Reverse biased)
For $\frac{1}{2}$ wave Rectifier, PIV = V_m

$$\textcircled{2} \quad I_{dc} = \frac{1}{2\pi} \int_0^{2\pi} I_m \sin \omega t d(\omega t) = \frac{I_m}{\pi}$$

$$V_{dc} = \frac{I_m}{\pi} \cdot R_L$$

③ Rms value of current (I_{rms})

$$\begin{aligned} I_{rms}^2 &= \frac{1}{2\pi} \int_0^{2\pi} I_m^2 \sin^2 \omega t d(\omega t) \\ &= \frac{I_m^2}{4} \end{aligned}$$

$$I_{rms} = \frac{I_m}{2}$$

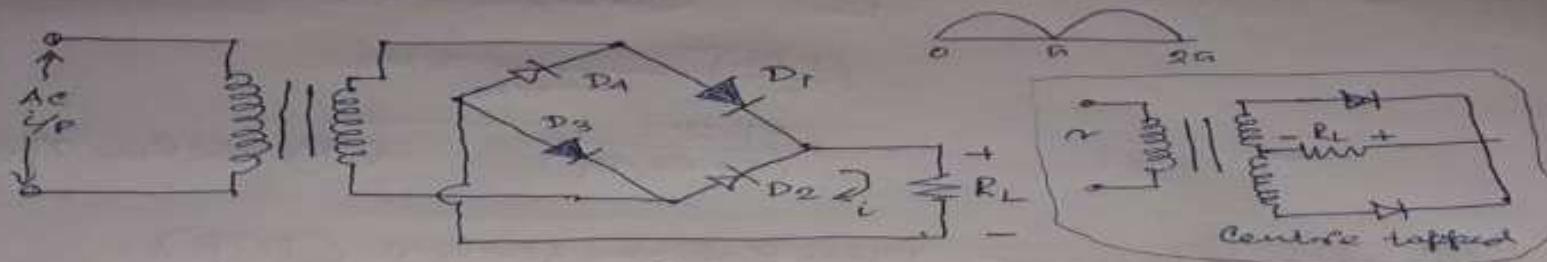
$$V_{rms} = \frac{I_m}{2} \cdot R_L$$

$$(4) \text{ Ripple factor } \gamma = \frac{I_{rms}}{I_{dc}} = \frac{V_{rms}}{V_{dc}}$$

$$\begin{aligned} i &= i - I_{dc} \\ I_{rms} &= \sqrt{\frac{1}{2\pi} \int_0^{2\pi} (i - I_{dc})^2 d(\omega t)} \\ &= \sqrt{\frac{1}{2\pi} \int_0^{2\pi} (i^2 - 2iI_{dc} + I_{dc}^2) d(\omega t)} \\ &= \sqrt{(I_{rms}^2 - I_{dc}^2)} \\ \gamma &= \frac{\sqrt{I_{rms}^2 - I_{dc}^2}}{I_{dc}} \\ &= \sqrt{\left(\frac{I_{rms}}{I_{dc}}\right)^2 - 1} \\ &= \sqrt{\left(\frac{I_{rms}/2}{I_{rms}/\pi}\right)^2 - 1} \\ &= \sqrt{\left(\frac{\pi}{2}\right)^2 - 1} = 1.21. \end{aligned}$$

$$\begin{aligned} (5) \eta - \text{Efficiency} &= \frac{P_{dc}}{P_{ac}} \times 100\% \\ &= \frac{I_{dc}^2 \cdot R_L}{(R_g + R_L) I_{rms}^2} \\ &= \left(\frac{I_{dc}}{I_{rms}}\right)^2 \cdot \frac{R_L}{R_g + R_L} \times 100 \\ &= \left(\frac{2}{\pi}\right)^2 \cdot \frac{1}{1 + R_g/R_L} \times 100 \\ &= \frac{40.6}{1 + R_g/R_L} \times 100 \end{aligned}$$

Full wave Rectifier



$$i = I_m \sin \omega t \quad \text{for } 0 \leq \omega t \leq \pi$$

$$i = I_m \sin \omega t \quad \text{for } \pi \leq \omega t \leq 2\pi$$

$$I_{m\text{a}} = \frac{V_m}{R_g + R_L} \quad \text{for Centre-tap rectifier}$$

$$I_{m\text{a}} = \frac{V_m}{2R_g + R_L} \quad \text{for Bridge rectifier}$$

① Peak Inverse Voltage (PIV)

The maximum voltage that appears across the diode when the diode is non-conducting (i.e. reverse biased)

$$② I_{DC} = \frac{1}{n} \int_0^{\pi} i_d(\omega t) d(\omega t) = \frac{1}{n} \int_0^{\pi} I_m \sin \omega t d(\omega t) = \frac{2I_m}{n}$$

$$③ V_{DC} = I_{DC} \cdot R_L = \frac{2}{n} I_m R_L$$

$$④ I_{RMS} = \sqrt{\frac{1}{n} \int_0^{\pi} I_m^2 \sin^2 \omega t d(\omega t)} = \frac{I_m}{\sqrt{2}}$$

$$⑤ V_{RMS} = \frac{I_m}{\sqrt{2}} \cdot R_L$$

$$⑥ \text{Ripple factor} = \gamma = \frac{I_{RMS}}{I_{DC}}$$

$$\text{Form factor} = \frac{I_m / \sqrt{2}}{2I_m / n} = \frac{I_{RMS}}{I_{DC}} = \frac{n}{2\sqrt{2}} = 1.11$$

$$\gamma = \sqrt{\frac{I_{RMS}^2 - I_{DC}^2}{I_{DC}^2}} = \sqrt{(FF)^2 - 1} = \sqrt{(1.11)^2 - 1} = 0.482$$

$$\begin{aligned}
 \textcircled{4} \quad \text{Efficiency} &= \frac{P_{dc}}{P_{ac}} = \left(\frac{I_{dc}}{I_{rms}} \right)^2 \cdot \frac{R_L}{R_S + R_L} \times 100\% \\
 &= \left(\frac{2\sqrt{2}}{\pi} \right)^2 \cdot \frac{1}{1 + R_S/R_L} \times 100 \\
 &= \frac{81.2\%}{(1 + R_S/R_L)} \quad \text{if } R_S \ll R_L, \eta \approx 81.2\%
 \end{aligned}$$

(3) Transformer Utilization factor (TUF)

$$TUF = \frac{P_{dc}}{P_{ac(\text{rated})}}$$

$$P_{dc} = I_{dc}^2 \cdot R_L \quad \text{if } P_{ac(\text{rated})} = V_{rms} \cdot I_{rms}$$

For half wave.

$$\begin{aligned}
 V_{rms} &= \frac{V_m}{\sqrt{2}}, \quad I_{rms} = \frac{I_m}{\sqrt{2}}, \quad I_{dc} = \frac{I_m}{\pi} \\
 TUF &= \frac{\frac{I_{dc}^2 \cdot R_L}{(V_m/\sqrt{2})(I_m/\sqrt{2})}}{=} = \frac{2\sqrt{2}}{\pi^2} \cdot \frac{1}{1 + (R_S/R_L)}
 \end{aligned}$$

$$R_S \ll R_L, \quad TUF = \frac{2\sqrt{2}}{\pi^2} = 0.287$$

For full wave.

$$\begin{aligned}
 TUF|_{\text{Primary}} &= \frac{I_{dc}^2 R_L}{V_{rms} \cdot I_{rms}} = \frac{\left(\frac{2I_m}{\pi} \right)^2 \cdot R_L}{\left(\frac{V_m}{\sqrt{2}} \right) \left(\frac{I_m}{\sqrt{2}} \right)} \\
 &= \frac{8}{\pi^2} \cdot \frac{R_L}{R_S + R_L}
 \end{aligned}$$

$$R_S \ll R_L, \quad TUF|_{\text{Primary}} = 0.812$$

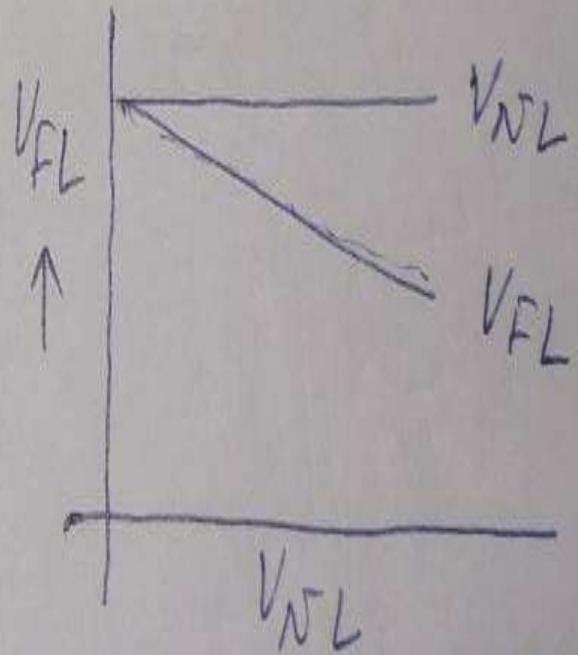
$$TUF|_{\text{Secondary}} = 2 \times TUF|_{\text{Primary}} = 2 \times 0.812 = 1.624$$

$$(TUF)_{av} = \frac{0.812 + 0.572}{2} = 0.692 = 0.572$$

Regulation

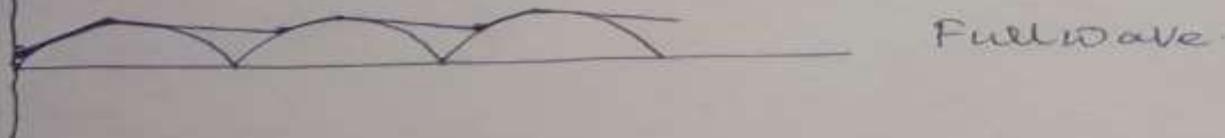
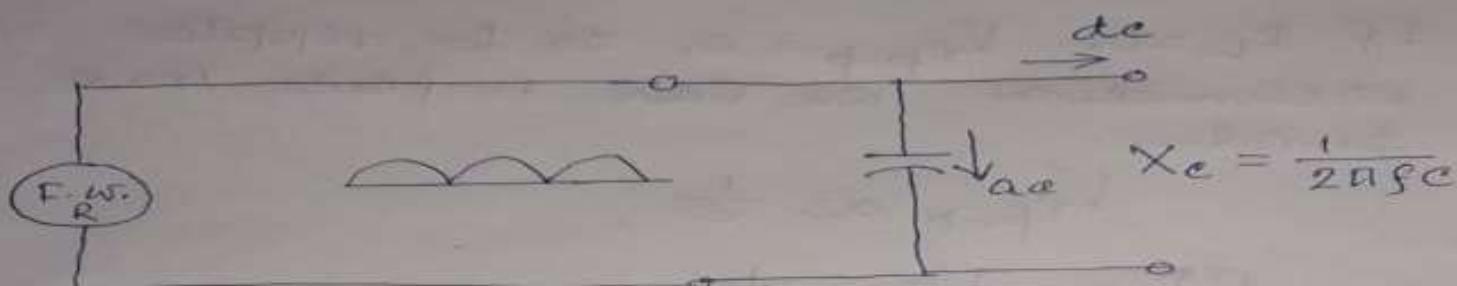
L

$$L = \frac{V_{NL} - V_{FL}}{V_{FL}} \times 100$$



Slope?

Filter Circuit



In order to bypass all ac components, we require to make $X_C = 0$. for that we have to set $C = \infty$. [Practically impossible]
So we cannot make it fully ripple free.

Selection of Capacitor

$$V_{rp-p} = \frac{I_L \text{ (maxm. load current)}}{f_r \cdot C}$$

\downarrow
Ripple frequency

Filter capacitor

If $I_L = 0$, $V_{rp-p} = 0$, so for ripple estimation we have to place load surely.

$$V_{rp-p} \propto \frac{1}{C}$$

$C \uparrow, V_{rp-p} \downarrow$

Practical Consideration.

$V_{rp-p} = .1\%$ of the o/p voltage.
then we calculate C .