EE 337: Interfacing to LCD Display
Lab 4

20 August 2018

In this set of experiments, we develop display and other utilities which will be useful to us for the later experiments. For these experiments, you will have to attach the LCD unit to the Pt-51 board. Remember that the display has to be plugged in such a way that it extends *outside* the board and *not* over it. Plugging in the display in the wrong orientation may damage it ! Refer to the attached tutorial on Liquid Crystal Display Control.

For this lab, you are provided with a subroutine (`lcd.asm`) which writes characters to the LCD display. Study the program to see how it works. You can use it or write your own code to achieve the same functionality.

Note: For doing these exercises you can make use of `bin2ascii` subroutine, developed in the previous lab, to display ascii characters to LCD. You can use any other subroutine developed as part of previous labs.

1   Homework

1. Understand working of `lcd.asm`.

2. Using the supplied routine for writing characters to the LCD, write a program that will display "EE 337 - Lab 4" on the first line and your name on the second line. Pad the display lines with spaces such that these are centered on the LCD when displayed. Your name should not be hard coded in the program, but stored as a 16 byte array of characters in the upper RAM. The program should display whatever is stored in this array. You should assemble, debug, download and run this program on the Pt-51 kit before coming to the lab. [5 points]

3. The following subroutines are to be written for displaying the element of an array whose index is specified using switches.

(a) Write a subroutine `readValues` that will call `readNibbles`, `packNibbles` (developed in previous lab and appropriately modified) to read in K bytes ($0 < K < 5$) and store them in an array in memory starting at location P. Location $50H$ has the value of K and $51H$ has the value of P. [5 points]

(b) Write another subroutine `displayValues` that reads a 4 bit value from the input switches and use it as an array index to display the corresponding element of the array (created in previous question) on the LCD. If the read value is greater than or equal to K (indicating an invalid input), the subroutine should clear the LCD and stop. Otherwise, the corresponding byte should be displayed on the LCD every 2 seconds. The subroutine must be able to continuously read the switches and update the LCD. [5 points]

2 Lab Work

1. Using the subroutines developed in homework problem-2, store K elements of an array by reading the switches of port P1. Use location $60H$ to store the values of array A.

Now, write another subroutine `shuffleBits` which does the following task: if the original array is $A[0]$, $A[1]$, ..., $A[K-1]$, generate an array B, such that it contains

$$A[0]\ XOR\ A[1], A[1]\ XOR\ A[2], \ldots, A[K-1]\ XOR\ A[0].$$

[10 points]

Location for array B is $70H$. Now, call `displayValues` from homework 2 to display the elements of B by reading the index from the port. Use the template code provided below.

```
;====================MAIN===================
ORG 0XXXH
MAIN:

MOV SP,#0CFH;----------------------Initialize STACK POINTER
MOV 50H,#XX;----------------------Set value of K
MOV 51H,#XX;----------------------Array A start location
MOV 4FH,#XX;----------------------Clear location 4FH
LCALL readValues

MOV 50H,#XX;----------------------Value of K
MOV 51H,#XX;----------------------Array A start location
MOV 52H,#XX;----------------------Array B start location
LCALL shuffleBits
```

```
    MOV 50H,#XX;----------------------Value of K
    MOV 51H,#XX;----------------------Array B start Location
    LCALL displayValues;---------------Display the last four bits of elements on LCD

    here:SJMP here;-------------------WHILE loop(Infinite Loop)
    END
    ; -------------------------------END MAIN---------------------------------
```

2. Write a program which will display the contents of 16 locations in the on-chip RAM. The location will be specified by setting switches on the board. These 4 bits will be interpreted as the most significant nibble of the RAM address, the least significant nibble will be taken as 0. You have to read the switches twice with a delay and only when the two values agree that you will proceed to display the memory contents.

   The contents should be displayed 4 bytes per line. Thus 8 bytes will be displayed at a time. The next 8 bytes should be displayed after a pause of 5 seconds.

   Notice that you have to check whether the address range in question is in the directly addressable memory (00-7FH) or in the indirectly addressable memory (80-FFH). The contents shown should have been fetched using the correct addressing mode.

   The whole sequence should repeat endlessly. That is, read switches, display 8 bytes, wait for 5 seconds, display the next 8 bytes, wait for 5 seconds, read the switches again and so on. [10 points]