

Sai Rohit Kumar Nandam

Bengaluru, Karnataka
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Summary

Skilled in fundamentals of Digital Electronics and Analog Circuit Design. Knowledgeable in VLSI Design methodologies and Verilog. Passionate in deeping my skills in the field of Electronics and Electricals.

Education

JSS Academy Of Technical Education,
Electronics And Communication Engineering
Bengaluru,Karnataka
Dec 2022-July 2026(Expected)

CGPA:7.2

PCMC State Board
Sadhana PU College, Bengaluru, Karnataka
2020 - 2022
Percentage: 75

School CBSE Board
Narayana E techno, Bengaluru, Karnataka
2018 - 2020
Percentage: 72

Skills

VLSI Design Methodologies , C++ , Python, Verilog , Digital Electronics, Microcontroller Programming , Data Structures Using C++, Digital Signal Processing , , ASIC Design flow
Software Used : Cadence Tool Suit , MATLAB , Arduino IDE.

Projects

- **Paralysis Healthcare Monitoring Device.**

Providing thorough and ongoing monitoring, treatment, and support for those who are affected by paralysis or mobility limitations is the goal of a paralysis healthcare monitoring system. Injuries to the nervous system, such as spinal cord injuries or neurological conditions like stroke , frequently result in paralysis, which is the loss of muscular function in a portion of your body.

- **Implementation of Approximate Bilateral Filters**

An approximate bilateral filters addresses the computational challenges by simplyfying the bilateral filtering process while maintaining edge presevation. It reduces the complexity by using faster approximation of the spatial and intensity weights.

Languages

English, Kannada, Hindi, Telugu

Interests

Digital Electronics

Chip design

Very Large Scale Integration

Verilog

Certifications

Verilog HDL: VLSI Hardware Design