📄 VAIBHAV CHANNALLI

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🎯 Career Objective

A passionate and detail-oriented Electronics and Communication Engineering student, eager to contribute to innovative projects through strong problem-solving and circuit design skills. Seeking internship opportunities to enhance practical experience.

🎓 Education

B.Tech – Electronics and Communication Engineering

JSS ACADEMY OF TECHNICAL EDUCATION,BENGALURU

Expected Graduation : December 2026

CGPA:8.05/10

Senior Secondary (XII)

VIDYANIKETAN PU COLLEGE,HUBLI – 2022

Percentage : 89.20%

Secondary School (X)

SRI KRISHNADEVERAYA EDUCATIONAL SOCIETY,SINDHANUR – 2020

Percentage : 91.20%

💻 Technical Skills

- **Programming Languages**: C, Python, Verilog
- **Tools**: MATLAB, LTspice, Keil uVision,Xilinx Vivado
- **Platforms**: Arduino, Raspberry Pi, Multisim
- **Others**: Digital Logic Design, Signal Processing Basics

🛠 Academic Projects

- **Arduino-Based Multifunctional Robot(Feb 2022 – May 2022)**
  Built a robot with obstacle avoidance, voice control, and remote control using Arduino. Integrated sensors and modules for real-time navigation, voice command processing, and manual control via Bluetooth and IR
- **Vedic Multiplier using Ripple Carry Adder(Oct 2024 – Dec 2024)**
  Designed and implemented an efficient Vedic multiplier architecture using ripple carry adders in Verilog. Improved computation speed for arithmetic operations by leveraging ancient Vedic algorithms with simple adder logic.

📑 Certifications

- "Python Essentials " – CISCO(2023)
- "MATLAB " – Onramp (2024)

🏆 Achievements & Extracurriculars

- **Team Head**: Led a project team, coordinated tasks, and ensured timely delivery with effective collaboration and leadership.

- **Trek Guide**: Volunteered as a trek guide, managing group safety, planning routes, and enhancing outdoor experiences with strong communication and decision-making skills.