

# Vignesh K

Bengaluru, Karnataka

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## Summary

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Skilled in fundamentals of Digital Electronics and Analog Circuit Design. Knowledgeable in VLSI Design methodologies and Verilog. Passionate in deeping my skills in the field of Electronics and Electricals.

## Education

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### Electronics And Communication Engineering

JSS Academy Of Technical Education,

Bengaluru,Karnataka

Dec 2022-July 2026(Expected)

CGPA:7.32

### PCME State Board

Sri Kumaran Children's Home Composite PU College, Bengaluru, Karnataka

2020 - 2022

Percentage: 88.16

### School State Board

Holy Child Convent, Bengaluru, Karnataka

2011 - 2020

Percentage: 92.16

## Skills

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VLSI Design Methodologies , C++ , Python, Verilog , Digital Electronics, Analog Circuit Design , Microcontroller Programming , Data Structures Using C++ , Digital Signal Processing , , ASIC Design flow

Software Used : Cadence Tool Suit , MATLAB , Arduino IDE , Linux Software.

## Projects

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- **Paralysis Healthcare Monitoring Device.**

Providing thorough and ongoing monitoring, treatment, and support for those who are affected by paralysis or mobility limitations is the goal of a paralysis healthcare monitoring system. Injuries to the nervous system, such as spinal cord injuries or neurological conditions like stroke , frequently result in paralysis, which is the loss of muscular function in a portion of your body.

- **Implementation of approximate bilateral filters for real-time and low energy imaging applications**

An approximate bilateral filters addresses the computational challenges by simplyfying the bilateral filtering process while maintaining edge presevation. It reduces the complexity by using faster approximation of the spatial and intensity weights.

## **Languages**

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English, Tamil, Kannada, telugu

## **Interests**

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Digital Electronics , Analog circuit design , Chip design,VLSI

## **Certifications**

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VLSI Design Methodologies(MAVEN SILICON), VLSI-SOC Design(MAVEN SILICON), Fundamentals of Digital Circuits And Systems.