SHRIDHAR DESAI

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Educational Qualification:

Examination	Institution	Year of passing	Percentage scored
B.E (ECE)	JSSATEB Engineering College, Bangalore	2026	82%*
PUC	SMJ SCIENCE PU College, SINDHANUR, RAICHUR DT	2022	92.5%
SSLC	DUDDUPUDI High School, , SINDHANUR, RAICHUR DT	2020	83.84%

Projects:

Vedic Multiplier using carry adders (2024)

 Description: The Vedic Multiplier is an efficient hardware architecture based on ancient Indian Vedic mathematics, particularly the Urdhva Tiryagbhyam Sutra, which means "Vertically and Crosswise." This method allows for parallel generation of partial products, making it faster than traditional multiplication techniques.

Configurable floating point matrix multiplier for AI ML chips:(2025 ongoing)

• **Description:**A Configurable Floating Point Matrix Multiplier (CFPMM) is a specialized digital hardware block designed to efficiently perform high-throughput matrix multiplication using floating point arithmetic. It's commonly used in AI and ML accelerators, particularly in neural networks where matrix operations are the core computational bottleneck.

Detection of Landslide using IOT

- Software used: Arduino IDE
- Hardware used: ESP8266 NodeMCU, DHT11 sensor, Soil moisture sensor, Accelerometer, Vibration sensor, Rain drop module sensor and Buzzer.
- The system addresses the need for an affordable, real-time landslide detection. By integrating IoT technology and environmental sensors, the system provides timely alerts, reducing risks and improving safety in vulnerable regions.

Certifications:

- Cisco Python Essentials
- Matlab Onramp Course

Strengths:

- Good communication skills, Flexibility, Good listener, Proactiveness, can work under pressure
- Ability to learn quickly, Willing to take responsibilities, Good interpersonal skills

Technical Skills:

- Basic C programming
- C++
- Python programming

Extra-curricular activity:

Participated for Technical Poster Presentation on the topic of Secured IOT Based Doorbell System

Known Languages:

- English
- Kannada
- Hindi