Siddartha Shetty S

Aspiring Engineer

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Bengaluru, Karnataka, 560078

Skills

- Microsoft PowerPoint
- Microsoft Word
- Active listening
- Leadership qualities
- Quick Learner
- Well Organized and proper time management
- Microsoft Excel
- Verilog software
- Public speaking

Extra Curricular

- Demonstrated leadership as the Sports Captain, excelling in team coordination and time management.
- Represented the college in volleyball, winning first prize at the district level, and actively participated as a football goalkeeper and cricket player.

Summary

Enthusiastic and detail-oriented 3rd-year Electronics and Communication Engineering student with a strong foundation in digital electronics, embedded systems, and communication technologies. Passionate about solving real-world problems through innovation and collaboration. Actively involved in college tech fests, workshops, and mini-projects, with a keen interest in IoT, data analyst , python with machine learning and signal processing. Seeking internship opportunities to apply technical skills and gain practical industry experience.

Education

BNM PU College, Department Of Pre-university Education, Karnataka
12th , CGPA: 70%
Bengaluru, Karnataka
Apr 2022

JSS Academy Of Technical Education, VTU
Bachelor of Engineering - Electronics Communication Engineering
Bengaluru, Karnataka
Aug 2022

- Actively participated in college and department fests as a volunteer and competitor, showcasing event management, teamwork, and creativity.
- Core volunteer in the National Service Scheme (NSS), contributing to social outreach and campus development, emphasizing leadership and community service.
- Developed a model-based Railway Track Crack Detection system with Arduino IDE/Raspberry pi as part of the Microcontroller course project, focused on safety automation using sensors and real-time alerts.
- Currently working on a web development project involving Verilog code, combining hardware knowledge with full-stack development.

Certifications

- Matlab onramp
- C programming

Projects

Railway Track Crack Detection System – Model-Based Project with Arduino IDE

- Developed a working hardware model to detect cracks in railway tracks using IR sensors and alert mechanisms.
- Programmed using Arduino IDE, integrating sensor data with real-time fault detection logic.
- Includes a simple web interface for status updates and safety alerts, showcasing embedded systems and IoT integration.
- Created as part of the Microcontroller course, focusing on practical applications of sensor interfacing and system automation.

Language

- English
- Kannada
- Hindi(Sanskrit)
- Telugu

Functional Verification of 16-bit ALU Operations – Web-Based Verilog Simulation (Ongoing)

- Working on building and verifying a 16-bit Arithmetic Logic Unit (ALU) using Verilog HDL, implementing operations like addition, subtraction, AND, OR, XOR, shift, and comparison.
- Developing a web-based interface to simulate ALU functionality and visualize waveform outputs for educational and testing purposes.
- Emphasizes functional verification using testbenches to ensure logic accuracy and timing behavior of ALU operations.
- Project bridges core concepts in digital system design and web technologies, aimed at making HDL learning more interactive and accessible.

Personal Information

- Enthusiastic and results-driven ECE student with strong interest in IoT systems and Data Analytics and solid foundation in embedded systems, digital design, and sensor integration.
- Proficient in Python, C/C++, and familiar with real-time hardware interfacing.
- Excellent communication, teamwork, and cross-functional collaboration skills.
- Quick learner with strong problem-solving and time management abilities
- Key player in college volleyball and football teams; demonstrated leadership and discipline
- Actively seeking opportunities to apply technical skills in innovative, real-world projects