

Furqan S. Bellary

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SUMMARY

Motivated 3rd-year Electronics and Communication Engineering student with a strong foundation in electronic systems and communication technologies. Proficient in C, C++, and Python, with basic knowledge of Data Structures and Algorithms (DSA). Familiar with Verilog basics and interested in digital design and semiconductor technologies. Quick learner with a passion for innovation and problem-solving.

Education

JSS Academy of Technical Education, BE in Electronics and communication Engineering	2022 - Present
• CGPA: 8.26	
Dr.RB Patil Mahesh PU College, Pre-University Course	2021 - 2022
• percentage: 85.33	
St.Anthony's Public School, Central Board Of Secondary Education(CBSE)	2019 - 2020
• percentage: 61.8	

Skills

Programming Languages: C, C++, Python, Verilog
Databases: MongoDB, SQL
Tools & Platforms: Git, GitHub

Projects

Animal Species Detection using Camera Trap	2024
<ul style="list-style-type: none">Developed a Raspberry Pi-based camera system for real-time animal detection, displaying identified species along with confidence scores to enable immediate recognition.Designed a user-friendly interface for monitoring and analyzing detection results in real-timeTools Used: python, OpenCV, tensorflow	
Preventing Human-wild Animals Conflicts with AI	2025
<ul style="list-style-type: none">To detect and track wild animals near villages and agricultural fields, providing early warnings to prevent damage to crops, livestock, and human lives.Designed the system to operate autonomously in remote areas with limited resources, supporting wildlife conservation and rural protection efforts.Enabled early detection of potentially harmful animals (e.g., wild boars, elephants, predators) to prevent human-wildlife conflicts.Tools Used: Python, XOLO, Tensorflow	

Certifications

- DSA Supreme batch – Codehelp(ongoing)
- Python Essentials by CISCO