KARTHIK JANNU
Student

Bengaluru | Bengaluru, Karnataka 560-060

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"Detail-oriented and motivated VLSI enthusiast with a strong foundation in digital design, Verilog, and semiconductor fundamentals. Passionate about optimizing circuits and solving complex problems. A collaborative learner eager to contribute innovative solutions in the semiconductor industry."

CERTIFICATIONS

Matlab Onramp Course

PROJECTS

DESIGNING OF TRACTION INVERTER FOR EV

The traction inverter is a critical component that is responsible for converting the DC power from the battery into the AC power required to drive the electric motor.

FAULT DIAGNOSIS AND PROTECTION OF EV INVERTERS USING MACHINE LEARNING

Diagnosing faults in electric vehicles (EVs) is a great challenge. The purpose of this project is to demonstrate the detection of faults in an electromechanical conversion chain for conventional or autonomous EVs

Education

BACHEOLAR OF ELECTRONICS AND COMMUNICATION ENGNEERING

J S S Academy of Technical Education, Bengaluru 560-060

under Visvesvaraya Technological University, Belgum (Graduating on APRIL 2026)

Pre – university Graduate in PCMCs (JUNE 2020 – MARCH 2022)

Chaitanya Pu, Sirsi, State Board

Secondary School Leaving Certificate (APRIL 2019 – MARCH 2020)

Shriniketana School ,Isloor, Sirsi, CBSE Board

Skills

- C, PYTHON, VERILOG (intermediate).
- • VHDL (Basic), CMOS Logic Design
- Problem Solving.
- Semiconductor Fundamentals
- Collaborative and adaptable in group projects.
- Intermediate knowledge about electronics and data structures.