

SUCHITH R S

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SUMMARY

A dedicated and meticulous student studying electronics and communication engineering, with a solid understanding of circuit design, embedded systems, and communication technologies. Excited to put their academic knowledge into practice and make meaningful contributions to cutting-edge projects. Gain practical experience with microcontrollers, MATLAB, and fundamental VLSI concepts, while also developing strong analytical and collaborative abilities. Excited about embarking on a career in core electronics or related tech fields, where I can continue to develop my skills and contribute to meaningful advancements.

EDUCATION

10th Graduated March 2020
77%
Srimati Pushpa Shamanur Mahalingappa Residential School, Davangere
Relevant coursework: **12th** Graduated April 2022
91%
MES PU College, Davangere
Relevant coursework: **B.E., Electronics and Communication Engineering** Graduating June 2026, pursuing
Visvesvaraya Technological University 7.96 CGPA, until 5th Sem
J.S.S. ACADEMY OF TECHNICAL EDUCATION BANGALORE
Relevant coursework: Hardware Design Languages and Programmable Logic, Digital Electronics, Embedded Systems, VLSI

TECHNICAL SKILLS

Data Analysis and Statistics: Signal Processing, Circuit Design, VLSI, Computer Architecture

Design and Modeling Tools: Verilog, Simulink, RTL Coding, MATLAB, Microsoft Office

Programming: Python, C, C++

Certifications: MATLAB, Mathworks Training - January 2024

ACADEMIC PROJECTS

Efficient Implementation of Elliptic Curve Scalar Multiplication over a Generic Prime Field $GF(p)$

Oct 2024 – Dec 2024

Collaborated in a team of two to implement a Elliptic Curve Scalar Multiplication.

- Optimized for resource-constrained embedded systems.
- Focused on low-power, area-efficient architecture.
- Achieved performance optimizations via hardware-level parallelism and pipelining.
- Verified functionality and efficiency using xilinx vivado and cadence simulation tools.

A Tiny ECC Processor

Feb 2025 - Present

Led team of two to design and develop a tiny ECC Processor.

- Design and implementation of a Tiny ECC (Elliptic Curve Cryptography) processor for lightweight cryptographic operations.
- Supported core ECC functionalities including point addition, scalar multiplication, key generation, and digital signatures.
- Will deployed the ECC processor on an FPGA to validate functionality and optimize real-time performance in hardware.

ACTIVITIES

PCB Workshop

Feb 2023

Volunteered for more than 100 people at the PCB workshop.

- Assisted in organizing and conducting hands-on workshops focused on PCB design, fabrication, and soldering techniques.
- Collaborated with a team to troubleshoot circuit issues and ensure successful project completion by all attendees.