Bhadri Naga Chandana

+91 6300650956 chandanab6300@gmail.com Bengaluru, Karnataka in linkedin.com/in/nagachandana in github.com/chandana

Profile

Motivated Electronics and Computer Engineering (ECE) student with hands-on experience in circuit design, embedded systems, and software development. Proficient in MATLAB, C, and Python, with a strong foundation in digital signal processing and control systems. Adept at problem-solving and eager to contribute to innovative projects in a dynamic engineering environment. Passionate about leveraging technical skills to drive advancements and solve real-world challenges.

Areas of Expertise

MATLAB - Python - C - Verilog - Control Systems

Online Courses & Certifications

- Object Oriented Programming in C++ (Jan. 2024) MATHWORKS
- MATLAB ONRAMP (2023) MATHWORKS

Education

BE in Electrical and Communication Engineering

JSS Academy of Technical Education, Bengaluru, Karnataka

2022 - 2026

Relevant Courses: Circuit Design, Embedded Systems, Digital Signal Processing, Control Systems, MATLAB, C, Python

PUC (Pre-University)

Narayana Junior College, Anantapur, Andhra Pradesh

2020 - 2022

Work Experience

Intern, Emertxe Information Technologies

Bengaluru, India

01/2025 - 02/2025

- Collaborated on real-time embedded system development.
- Enhanced skills in microcontroller programming and simulation tools.

Projects

Microwave Oven Project - Emertxe Information Technologies

Bengaluru, India

01/2025 - 02/2025

- Objective: Developed a microwave oven control system using embedded programming and simulation.
- Technologies: MPLAB X IDE, PICSimLab, PIC microcontroller.
- Outcome: Simulated full embedded functionality including UI, timing, and power control; enhanced skills in microcontroller programming and system design.

Design and Implementation of RISC-V Processor

Remote

09/2024 - 12/2024

• **Objective:** Designed and simulated a simplified 32-bit RISC-V (RV32I) processor using Verilog and Iverilog, implementing core components like ALU, control unit, register file, and instruction memory.

- Technologies: Verilog, Iverilog, GTKWave.
- **Outcome:** Implemented instruction execution for arithmetic, logic, and branch operations in a single-cycle architecture; validated functionality using testbenches and waveform analysis.

Certifications

- Internship at Emertxe Information Technologies
- Infosys Springboard: Pragathi Path to Future (Cohort 3)

Skills

- Data Visualization: Excel
- Digital Marketing: Project Management, Social Media Optimization, Content Writing, Campaign Analysis & Reporting,
 PPC & SEM Strategies, CRO, PPC Advertising
- Software: MS Project, Adobe Photoshop, Audition, Canva
- **Soft Skills:** Presentation, Planning, Organized, Creative Problem-Solving, Teamwork, Active Listening, Adaptability, Analytical Thinking

Extracurricular Activities

National Entrepreneurship Challenge (NEC), (E-Cell IIT Bombay)

Mumbai, Maharashtra

08/2024 - 12/2024

- Objective: Gaining hands-on experience in entrepreneurship by developing and pitching innovative business ideas.
- **Outcome:** Organized Aadi Antha Prerna, a startup pitch competition, and enhanced entrepreneurial and leadership skills.

Pragathi: The Path to Future (Infosys Springboard)

Bengaluru, Karnataka

11/2024 - 03/2025

- Objective: Engaging in a 12-week career development program focused on digital skills and career readiness.
- Outcome: Gained critical thinking, analytical abilities, and professional mentorship through online and offline sessions.

Languages

• **English** [Proficient]

• Kannada [Beginner]

• **Hindi** [Beginner]

• **Telugu** [Native]