

Switching Theory & Logic Design Laboratory
CSE Department, IIT Kharagpur
Spring Semester 2023-24

Module 1 : Combinational Logic Circuits

Assignment 1: Realize the following function:

$$f(w, x, y, z) = \Sigma (6, 12, 13, 14)$$

(a) Using AND, OR & NOT gates (TTL IC)

(b) Using only NAND gates (TTL IC)

(1 day)