

Switching Theory & Logic Design Laboratory
CSE Department, IIT Kharagpur
Spring Semester 2023-24

Module 2 : SEQUENTIAL CIRCUITS AND FSM

Assignment 6 :

1. Design of flip-flops
 - a. Implement a master-slave JK flip-flop using NAND gates, and verify its functionality.
 - b. Configure it as a T flip-flop and verify that it divides the frequency of an input pulse train by two.
 - c. Configure it as a D flip-flop and verify its functionality.

(1 day)