**Week-2 Reading Material**

1.1 FPGA basics

* Checkout this playlist: <https://youtu.be/WY-F3knih7c?si=qYrc8PSqPlRQR-uo>

1.2 Circuits components in Verilog

* [J K Flip flop](https://www.javatpoint.com/verilog-jk-flip-flop)
* [D flip flop](https://www.javatpoint.com/verilog-d-flip-flop)
* [T Flip Flop](https://www.javatpoint.com/verilog-t-flip-flop)
* [D latch](https://www.javatpoint.com/verilog-d-latch)
* [Ripple Counter](https://www.javatpoint.com/verilog-ripple-counter)
* [Ring Counter](https://www.javatpoint.com/verilog-ring-counter)
* [4-bit Counter](https://www.javatpoint.com/verilog-4-bit-counter)
* [Full Adder](https://www.javatpoint.com/verilog-full-adder)
* [Encoder](https://www.javatpoint.com/verilog-priority-encoder)
* [Multiplexer](https://www.javatpoint.com/verilog-multiplexer)

1.3 Image Processing

* [Convolution and correlation](https://youtu.be/2OW2aLFaV8A?si=ofZwI1VPOR_kDXFj)