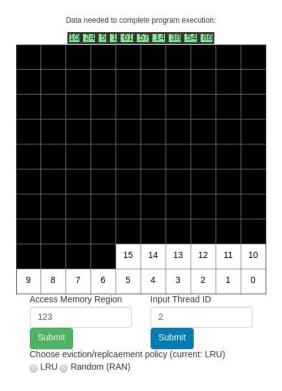
Figure 1. Initially Filling the Cache



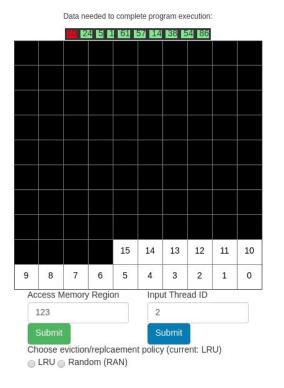
The Current Cache:

Line Number: 0, Age: 1	0	1	2	3
Line Number: 1, Age: 2	4	5	6	7
Line Number: 2, Age: 3	8	9	10	11
Line Number: 3, Age: 4	12	13	14	15

Most Recent (at top of list):

Cache Line 3 caused a cold miss! Cache Line 2 caused a cold miss! Cache Line 1 caused a cold miss! Cache Line 0 caused a cold miss!

Figure 2. Accessing a number within the cache, but also selecting data needed to complete a program



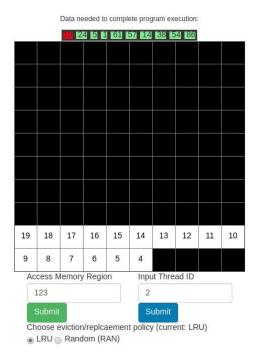
The Current Cache:

			_	120
Line Number: 0, Age: 1	0	1	2	3
Line Number: 1, Age: 2	4	5	6	7
Line Number: 2, Age: 5	8	9	10	11
Line Number: 3, Age: 4	12	13	14	15

Most Recent (at top of list):

Cache was hit by the cache line #2! Cache Line 3 caused a cold miss! Cache Line 2 caused a cold miss! Cache Line 1 caused a cold miss! Cache Line 0 caused a cold miss!

Figure 3. LRU eviction/replacement policy taking effect.



The Current Cache:

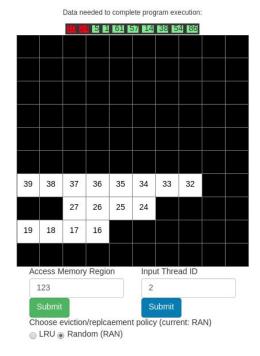
Line Number: 4, Age: 6	16	17	18	19
Line Number: 1, Age: 2	4	5	6	7
Line Number: 2, Age: 5	8	9	10	11
Line Number: 3, Age: 4	12	13	14	15

Most Recent (at top of list):

CONFLICT MISS: The Cache Line #0 was replaced by Cache Line #4
Cache was hit by the cache line #2!
Cache Line 3 caused a cold miss!
Cache Line 2 caused a cold miss!

Cache Line 1 caused a cold miss!
Cache Line 0 caused a cold miss!

Figure 4. Changed to Random eviction/replacement policy and taking effect.



The Current Cache:

Line Number: 4, Age: 6	16	17	18	19
Line Number: 8, Age: 10	32	33	34	35
Line Number: 6, Age: 8	24	25	26	27
Line Number: 9, Age: 11	36	37	38	39

Most Recent (at top of list):

CONFLICT MISS: The Cache Line #7 was replaced by Cache Line #9 CONFLICT MISS: The Cache Line #5 was replaced by Cache Line #8 CONFLICT MISS: The Cache Line #3 was replaced by Cache Line #7 CONFLICT MISS: The Cache Line #2 was replaced by Cache Line #6 CONFLICT MISS: The Cache Line #1 was replaced by Cache Line #5 CONFLICT MISS: The Cache Line #0 was replaced by Cache Line #4 Cache was hit by the cache line #2!

Cache Line 3 caused a cold miss! Cache Line 2 caused a cold miss! Cache Line 1 caused a cold miss!