

### LINEAR INTEGRATED CIRCUIT

# LTL431

# PROGRAMMABLE PRECISION REFERENCE

#### **DESCRIPTION**

The LRC LTL431 is a three-terminal adjustable regulator with a guaranteed thermal stability over applicable temperature ranges. The output voltage may be set to any value between Vref(approximately 2.5V) and 36V with two external resistors. It provides very wide applications, including shunt regulator, series regulator, switching regulator, voltage reference and others.

# 3 2 SOT-23

Pin 1: Cathode; 2: Ref; 3: Anode

#### **FEATURES**

Programmable output Voltage to 36V.

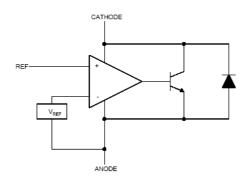
Low dynamic output impedance 0.2.

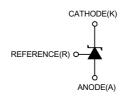
Sink current capability of 1 to 100mA.

Equivalent full-range temperature coefficient of 50ppm/C typical for operation over full rated operating temperature range.

Pb-Free package is available

#### **BLOCK DIAGRAM**







## LTL431 LINEAR INTEGRATED CIRCUIT

#### ABSOLUTE MAXIMUM RATINGS (Operating temperature range applies unless otherwise specified))

PARAMETER	SYMBOL	VALUE	UNIT
Cathode Voltage	VKA	36	V
Cathode Current Range(Continuous)	lka	-100 ~ +150	mA
Reference Input Current Range	Iref	-0.05 ~ +10	mA
Operating Junction Temperature	Tj	150	°C
Operating Ambient Temperature	Topr	0~+70/-40~+125	°C
Storage Temperature Temperature	Tstg	-65 ~ +150	°C

#### RECOMMENDED OPERATING CONDITIONS

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
Cathode Voltage	VKA	VREF		36	V
Cathode Current	IKA	1		100	mA

#### ELECTRICAL CHARACTERISTICS (Ta=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Reference Input Voltage *	Vref	VKA=VREF,IKA=10mA			2.50		V
					2.495		
Deviation of reference Input Voltage	$\Delta Vref/\Delta T$	VKA=VREF,IKA=10mA			4.5	17	mV
Over temperature(note 1)		TMIN<=TA<=TMAX					
Ratio of Change in Reference Input			ΔVKA=10V~VREF		-1.0	-2.7	mV/V
Voltage to the Change in Cathode	$\Delta Vref / \Delta VKA$	IKA=10mA	ΔVκA=36V~10V		-0.5	-2.0	
Voltage							
Reference Input Current	Iref	Iκa=10mA,R1=10kΩ, R2=∞			1.5	4	μΑ
Deviation of Reference Input Current	$\Delta$ Iref/ $\Delta$ T	Iκa=10mA,R1=10kΩ, R2=∞			0.4	1.2	μΑ
Over Full Temperature Range		TA=full Temperature					
Minimum Cathode Current for	IKA(min)	VKA=VREF			0.45	1.0	mA
Regulation							
Off-State Cathode Current	IKA(OFF)	VKA=36V,VREF=0			0.05	1.0	μΑ
Dynamic Impedance	ZKA	VKA=VREF, IKA=1 to 100mA			0.15	0.5	Ω
		f≤1.0kHz					

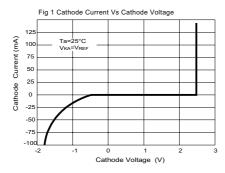
#### \* CLASSIFICATION OF Vref AND PACKAGE

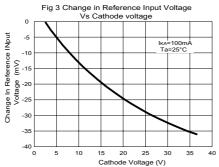
Туре	RanK	Range(V)	Marking	Packa	Topr
LTL431ALT1G	0.5%	2.487~2.512	LA	SOT-23	0~+70 °C
LTL431BLT1G	1%	2.475~2.525	LB	SOT-23	0~+70 °C
LTL431APLT1G	0.5%	2.482~2.507	LA1	SOT-23	0~+70 °C
LTL431BPLT1G	1%	2.470~2.520	LB1	SOT-23	0~+70 °C
LTL431ATLT1G	0.5%	2.487~2.512	LA2	SOT-23	-40~+125 °C
LTL431BTLT1G	1%	2.475~2.525	LB2	SOT-23	-40~+125 °C
LTL431APTLT1G	0.5%	2.482~2.507	LA3	SOT-23	-40~+125 °C
LTL431BPTLT1G	1%	2.470~2.520	LB3	SOT-23	-40~+125 °C

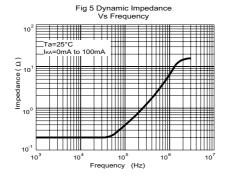


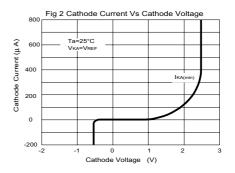
## LTL431 LINEAR INTEGRATED CIRCUIT

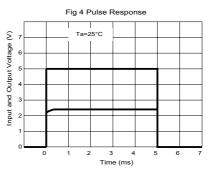
#### TYPICAL PERFORMANCE CHARACTERISTICS

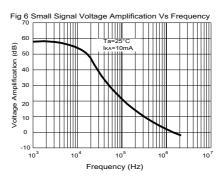








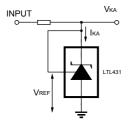


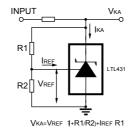




## LTL431 LINEAR INTEGRATED CIRCUIT

#### **TEST CIRCUIT**





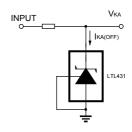
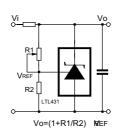


Fig 7 Test Circuit For VKA=VREF

Fig 8 Test Circuit for VKA >= VREF

Fig 9 Test Circuit For IKA(OFF)

#### APPLICATION CIRCUIT





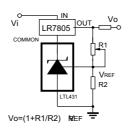


Fig 11 Output Control of a Three -Terminal Fixed Regulator

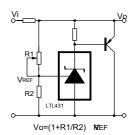


Fig 12 Higher-current Shunt Regulator

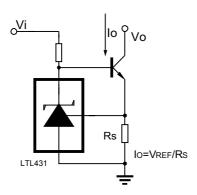


Fig 13 Constant-current Sink

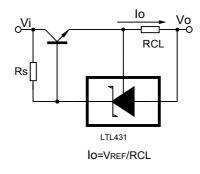
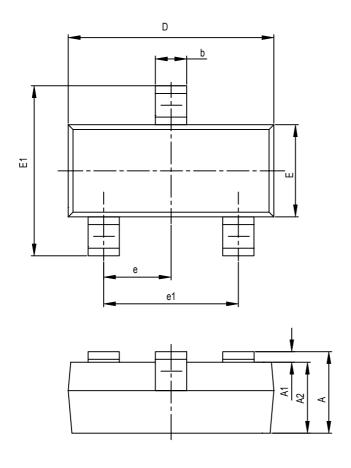
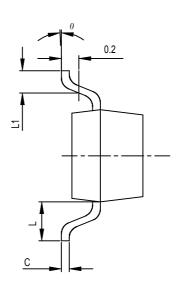


Fig 14 Current Limiting or Current Source



#### **SOT-23 PACKAGE OUTLINE DIMENSIONS**





Symbol	Dimensions I	n Millimeters	Dimensions In Inches		
	Min	Max	Min	Max	
Α	0.900	1.100	0.035	0.043	
A1	0.000	0.100	0.000	0.004	
A2	0.900	1.000	0.035	0.039	
b	0.300	0.500	0.012	0.020	
С	0.080	0.150	0.003	0.006	
D	2.800	3.100	0.110	0.118	
E	1.200	1.610	0.047	0.055	
E1	2.250	2.550	0.089	0.100	
е	0.950TPY		0.037TPY		
e1	1.800	2.000	0.071	0.079	
L	0.550REF		0.022REF		
L1	0.300	0.500	0.012	0.020	
θ	0°	8°	0°	8°	