



JSS MAHAVIDYAPEETHA  
ಶ್ರೀ ಜಯಚಾಮರಾಜೇಂದ್ರ ಇಂಜಿನಿಯರಿಂಗ್ ಕಾಲೇಜು, ಮೈಸೂರು - ೫೭೦ ೦೦೬  
**SRI JAYACHAMARAJENDRA COLLEGE OF ENGINEERING**  
**MYSORE - 570 006.**

Autonomous Institution under Visvesvaraya Technological University, Belgaum - 590 018. Karnataka, State, India

### BACHELOR OF ENGINEERING (B.E)

#### GRADE CARD

Name of the Student : SUMUKHA H S  
University Seat Number : 4JC13EC107  
Father's Name : SHRINIVASA H K  
Mother's Name : ROOPA  
Branch : ELECTRONICS & COMMUNICATION ENGINEERING  
Semester : IV  
Month and Year : MAY 2015

Sl. No	Subject Code	Subject Title	Credits	Grade Awarded	Grade Points
1	MA410	PROBABILITY AND STATISTICS	4.0	S	10
2	EC410	LINEAR INTEGRATED CIRCUITS AND SYSTEMS	4.0	A	9
3	EC420	SWITCHING SYSTEMS & ACCESS NETWORKS	4.0	C	7
4	EC430	SIGNALS AND SYSTEMS	4.0	B	8
5	EC440	DIGITAL SYSTEM DESIGN	4.0	B	8
6	EC450	CHANNEL THEORY	4.0	D	5
7	EC41L	L.I.C. LABORATORY	1.5	S	10
8	EC42L	DIGITAL SYSTEM DESIGN LAB USING VERILOG	1.5	A	9
9	HU410	CONSTITUTION OF INDIA AND PROFESSIONAL ETHICS	0.0	PP	0
10					

Medium of Instruction : English Semester Credits Earned : 27.00 SGPA : 8.02

Cumulative Credits Earned : 104.00 CGPA : 8.78

Date : 02/07/2015

Controller of Examinations

Principal