마이크로프로세서응용



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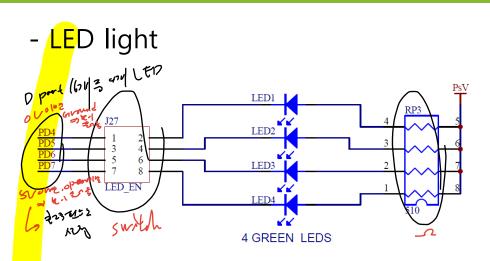
Evaluation Beard

Switch Inpre LED Output



1. LED 회로

GPIO



Port D

D[0]/flexray0 CA TX/etimer1 ETC[1]/flexpwm0 B[1]	125	PD0
	3	PD1
D[1]/etimer1 ETC[2]/ctu0 EXT TRG/flexray0 CA RX	140	PD2
D[2]/flexray0 CB RX/etimer1 ETC[3]/flexpwm0 X[3]	128	PD3
Divide a CD TD TALL: A PROJECT A DIVI	129	PD4
D[4]/flexray0 CB TR EN/etimer1 ETC[5]/flexpwm0 B[3]	33	PD5
D[5]/fcu0 F[0]/dspi3 SOUT/dspi0 CS3	34	PD6
D[6]/dspi3 SCK/flexpwm0 FAULT[1]/dspi0 CS2	37	PD7
D[7]/fcu0 F[1]/dspi3 SIN/dspi0 CS4/dspi1 CS3	22	DIDO
D[8]/flexpwm0 FAULT[3]/dsp Q CS5/dspi1 CS2	26	PD9
D[8]/flexpwm0 FAULT[3]/dspA CS5/dspA CS5 D[9]/lin1 TXD/flexpwm0 X[0]		
D[8]/flexpwm0 FAULT[3]/dsp/Q CS5/dspr1 CS2 D[9]/lin1 TXID/flexpwm0 X[0] D[10]/flexpwm0 A[0]/dspi3 CS0	26	PD9
D[8]/flexpwm0 FAULT[3]/dsp/Q CS5/dspi1 CS2 D[9]/lin1 TX10/flexpwm0 X[0] D[10]/flexpwm0 A[0]/dspi3 CS0 D[11]/flexpwm0 B[0]/dspi3 CS1/dspi3 SCK	26 76	PD9 PD10
D[8]/flexpwm0 FAULT[3]/dspACCS5/dsp11 CS2 D[9]/lin1 TXD/flexpwm0 X[0] D[10]/flexpwm0 A[0]/dspi3 CS0 D[11]/flexpwm0 B[0]/dspi3 CS1/dspi3 SCK D[12]/flexpwm0 X[1]/lin1 RXD	26 76 78	PD9 PD10 PD11
D[8]/flexpwm0 FAULT[3]/dsp/Q CS5/dspi1 CS2 D[9]/lin1 TXD/flexpwm0 X[0] D[10]/flexpwm0 A[0]/dspi3 CS0 D[11]/flexpwm0 B[0]/dspi3 CS1/dspi3 SCK D[12]/flexpwm0 X[1]/lin1 RXD D[13]/flexpwm0 A[1]/dspi3 CS2/dspi3 SOUT	26 76 78 99	PD9 PD10 PD11 PD12
D[8]/flexpwm0 FAULT[3]/dspQ CSS/dsp1 CS2 D[9]/lin1 TXID/flexpwm0 X[0] D[10]/flexpwm0 A[0]/dspi3 CS0 D[11]/flexpwm0 B[0]/dspi3 CS1/dspi3 SCK D[12]/flexpwm0 X[1]/lin1 RXD D[13]/flexpwm0 A[1]/dspi3 CS2/dspi3 SOUT D[14]/flexpwm0 B[1]/dspi3 QS3/dspi3 SIN	26 76 78 99 95	PD9 PD10 PD11 PD12 PD13
D[8]/flexpwm0 FAULT[3]/dsp/Q CS5/dspi1 CS2 D[9]/lin1 TXD/flexpwm0 X[0] D[10]/flexpwm0 A[0]/dspi3 CS0 D[11]/flexpwm0 B[0]/dspi3 CS1/dspi3 SCK D[12]/flexpwm0 X[1]/lin1 RXD D[13]/flexpwm0 A[1]/dspi3 CS2/dspi3 SOUT	26 76 78 99 95 105	PD9 PD10 PD11 PD12 PD13 PD14

LED에 연결된 GPIO Pin

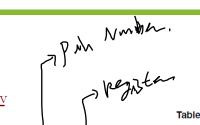


Table 3-3. Pin muxing (continued)

	Pad				I/O	Pad s	peed ⁵	Pin	No.
Port pin	configuration register (PCR)	Alternate function ^{1,2}	Functions Inplaced	Peripheral ³	direction 4	SRC = 0	SRC = 1	100-pin	144-pin
D[2]	PCR[50]	ALT0 ALT1	GPIO[50]	SIUL	I/O	Slow	Medium	97	140
		ALT2 ALT3	ETC[3] X[3] CB_RX	eTimer_1 FlexPWM_0 FlexRay_0	I/O I/O I				
D[3]	PCR[51]	ALT0 ALT1 ALT2 ALT3	CB_TX ETC[4] A[3]	SIUL FlexRay_0 eTimer_1 FlexPWM_0	I/O O I/O O	Slow	Symmetric	89	128
D[4]	PCR[52]	ALT0 ALT1 ALT2 ALT3	PIO[52] CB_TR_EN ETC[5] B[3]	SIUL FlexRay_0 eTimer_1 FlexPWM_0	I/O O I/O O	Slow	Symmetric	90	129
D[5]	PCR[53]	ALT0 ALT1 ALT2 ALT3	GPIO[53] CS3 F[0] SOUT	SIUL DSPI_0 FCU_0 DSPI_3	I/O O O	Slow	Medium	22	33
D[6]	PCR[54]	ALT0 ALT1 ALT2 ALT3	GPIO[54] CS2 SCK — FAULT[1]	SIUL DSPI_0 DSPI_3 FlexPWM_0	I/O O I/O —	Slow	Medium	23	34
D[7]	PCR[55]	ALT0 ALT1 ALT2 ALT3	GPIO[55] CS3 F[1] CS4 SIN	SIUL DSPI_1 FCU_0 DSPI_0 DSPI_3	I/O O O O	Slow	Medium	26	37





- LE<mark>D l</mark>ight 11.5.2.8 Pad Configuration Registers (PCR[0:107]) The Pad Configuration Registers allow configuration of the static electrical and functional characteristics associated with I/O pads. Each PCR controls the characteristics of a single pad. Address: Base + 0x0040 (PCR0) Access: User read/write Base + 0x0116 (PCR107) 108 registers 0 0 SMC APC PA[1:0] ODE SRC WPE WPS NOTE Table 11-10. PCR[0:107] field descriptions Field Description PA[1:0] Pad Output Assignment This field selects the function that is allowed to drive the output of a multiplexed pad. The PA field size can vary from 0 to 2 bits, depending on the number of output functions associated with this pad. 00: Alternative mode 0: GPIO GPIO의 기능 01: Alternative mode 1 (see Chapter 3, "Signal Description) 42768 10: Alternative mode 2 (see Chapter 3, "Signal Description) 11: Alternative mode 3 (see Chapter 3, "Signal Description) Note: The number of bits in the PA bitfield depends of the number of actual alternate functions provided for each pad. Please see the MPC5604P Datasheet (MPC5604P). OBE Output Buffer Enable This bit enables the output buffer of the pad in case the pad is in GPIO mode. Output 0: Output buffer of the pad disabled when PA = 00 1: Output buffer of the pad enabled when PA = 00 Input Buffer Enable This bit enables the input buffer of the pad. Input 0: Input buffer of the pad disabled

1: Input buffer of the pad enabled





```
- LED light
- 기본 Pin Setting
```

```
roger Enable => output? 2 2
```

```
/* Pad Configuration Register PCR[52] CB_TR_EN/ETC_1[5]/PWM_B[3]/PD[4] (129)
   SIU.PCR[521 R 0x0200) // LED1
* Selected Function : PD[4] (ALT 0)
        /* Output Buffers : Enabled
        /* Input Buffers : Disabled
        /* Output Drain : Disabled

✓* Slew Rate Control : Minimum

        /* Weak Pull Up/Down : Disabled
        /* Weak Pull Up/Down Select : Down
/*
* Pad Configuration Register PCR[53] CS3_0/FCU[0]/SOUT_3/PD[5] (33) */
/*
   SIU.PCR[53].R = 0x0200; // LED2
        /* Selected Function : PD[5] (ALT 0)
        /* Output Buffers : Enabled
        /* Input Buffers : Disabled
        /* Output Drain : Disabled
        /* Slew Rate Control : Minimum
                                                                                 Gulpre 2
        /* Weak Pull Up/Down : Disabled
        /* Weak Pull Up/Down Select : Down
SIU.PCR[54].R = 0x0200; //LED3
        /* Selected Function : PD[6] (ALT 0)
        /* Output Buffers : Enabled
        /* Input Buffers : Diabled
        /* Output Drain : Disabled

✓* Slew Rate Control : Minimum

        /* Weak Pull Up/Down : Disabled
        /* Weak Pull Up/Down Select : Down
/* Pad Configuration Register PCR[55] CS3_1/FCU[1]/CS4_0/SIN_3/PD[7] (37) */
   SIU.PCR[55].R = 0x0200; //LED4
        /* Selected Function : PD[7] (ALT 0)
        /* Output Buffers : Enabled
        /* Input Buffers : Disabled
        /* Output Drain : Disabled
        /* Slew Rate Control : Minimum
        /* Weak Pull Up/Down : Disabled
        /* Weak Pull Up/Down Select : Down
```





- Free<mark>M</mark>ASTER를 이용하여 LED를 On/Off

```
3.1 함수
            char LED1=1, LED2=1, LED3=1, LED4=1; - FreeMASTER에서 쓸 변수
            void LED_Ctr(void)
                SIU.GPDO[52].B.PDO = LED1;
                SIU.GPDO[53].B.PDO = LED2;
                                                - LED On/Off 함수
                SIU.GPDO[54].B.PDO = LED3;
                SIU.GPDO[55].B.PDO = LED4;
3.2 실행/ 위치(in main함수)
            /* Loop forever */
            for (;;)
                FMSTR Recorder():
                                      T) Your loop
                FMSTR_Poll();
                LED Ctr();
                1++;
            }
```





- FreeMASTER를 이용하여 LED를 On/Off
 - 3.5 FreeMASTER 변수 추가

•		Period
?	DEC	1000
?	DEC	0
?	DEC	0
?	DEC	0
	Not	connected
	· ·	? DEC ? DEC





- FreeMASTER를 이용하여 LED를 On/Off

<mark>3</mark>.6 결과

Name	Value	Unit	Period
i	20025974	DEC	1000
LED1	1	DEC	0
LED2	1	DEC	0
LED3	1	DEC	0
Name i LED1 LED2	Value 28803567	DEC DEC DEC	Period 1000 0 0
LED3	1	DEC	0
Name	Value 35702215	Unit DEC	Period 1000
LED1 LED2 LED3	0 0	DEC DEC	0 0 0
LED1 LED2 LED3	0 ,0 1	DEC DEC Unit	0 0 Period
LED1 LED2 LED3 Name	0 ,0 1 1 Value 40215082	DEC DEC Unit DEC	0 0 0 Period 1000
LED1 LED2 LED3 Name i LED1	0 ,0 1	DEC DEC Unit DEC DEC	0 0 Period 1000
LED1 LED2 LED3 Name	0 ;0 1 1 Value 40215082 0	DEC DEC Unit DEC DEC DEC	0 0 0 Period 1000
LED1 LED2 LED3 Name i LED1	0 ;0 1 1 Value 40215082	DEC DEC Unit DEC DEC DEC	0 0 Period 1000





















- FreeMASTER를 이용하여 LED On/Off 주기 변화

```
4.1 함수
```

1ms PIT 함수 내부에 위와 같이 입력





- FreeMASTER를 이용하여 LED On/Off 주기 변화

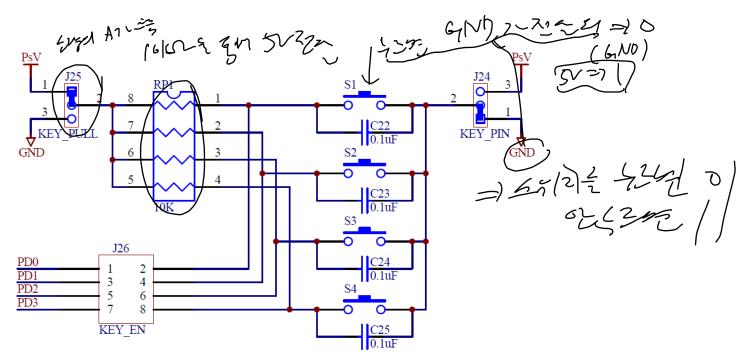
4.2 FreeMASTER 변수 추가

Name	Value	Unit	Period
i	3456373	DEC	1000
LED1	1	DEC	0
LED2	1	DEC	0
LED3	1	DEC	0
Blank	1000 -	DEC	0

Blank변수 값을 변경하여 LED4의 On/Off 주기를 조절 1ms 함수 안에 있으므로 1000번 카운트하면 1s가 된다.



- Switch 회로



MPU Port D

D[0]/flexray0 CA TX/etimer1 ETC[1]/flexpwm0 B[1]
D[1]/etimer1 ETC[2]/ctu0 EXT TRG/flexray0 CA RX
D[2]/flexray0 CB RX/etimer1 ETC[3]/flexpwm0 X[3]
D[3]/flexray0 CB TX/etimer1 ETC[4]/flexpwm0 A[3]

125	PD0
3	PD1
140	PD2
128	PD3
120	DLJA

Switch에 연결된 GPIO Pin





- Switch를 이용하여 간단한 Code 작성

- Switch Count Code char dnt=0: char $\$V[4]=\{0, 0, 0, 0\};$ char [5Wold[4]={0, 0, 0, 0}; int $\mathbb{I}ED[4]$; void(Switch(void) SWINT OF WALL SWold[0]=SW[0]; SWold[1]=SW[1]; SWold[2]=SW[2]; SWold[3]=SW[3]: SW[0]=SIU.GPDI[48].B.PDI; SW[1]=SIU.GPDI[49].B.PDI; SW[2]=SIU.GPDI[50].B.PDI; SW[3]=SIU.GPDI[51].B.PDI; if(!SWold[0] && SW[0]) cnt += 1; if(!SWold[1] && SW[1]) cnt += 1; if(!SWold[2] && SW[2]) cnt += 1;if(!SWold[3] && SW[3]) cnt += 1: 저장된 old 값과 새로운 값을 비교하여 rising 순간 Count





- Switch를 이용하여 간단한 Code 작성
 - Switch Count Code 실행 위치(in main함수)

```
/* Loop forever */
for (;;)
{
    FMSTR_Recorder();
    FMSTR_Poll();

    LED_Ctr();
    Switch();

    i++;
}
```





- Switch를 이용하여 간단한 Code 작성

- Switch를 이용하여 LED로 2진법 표현(최대 15까지)

```
void Switch(void)
    SWold[0]=SW[0];
    SWold[1]=SW[1];
    SWold[2]=SW[2];
    SWold[3]=SW[3]:
    SW[0]=SIU.GPDI[48].B.PDI;
    SW[1]=SIU.GPDI[49].B.PDI;
    SW[2]=SIU.GPDI[50].B.PDI;
    SW[3]=SIU.GPDI[51].B.PDI;
    if(!SWold[0] \&\& SW[0]) cnt += 1;
    if(!SWold[1] && SW[1]) cnt += 2;
    if(!SWold[2] \&\& SW[2]) cnt += 4;
    if(!SWold[3] \&\& SW[3]) cnt += 8;
    cnt = cnt%16;
    LED[0]=!(cnt&0x01);
    LED[1] = !(cnt&0x02);
    LED[2] = !(cnt&0x04);
    LED[3] = !(cnt&0x08);
    SIU.GPDO[52].B.PDO = LED[0];
    SIU.GPDO[53].B.PDO = LED[1];
    SIU.GPDO[54].B.PDO = LED[2];
    SIU.GPDO[55].B.PDO = LED[3]:
```

