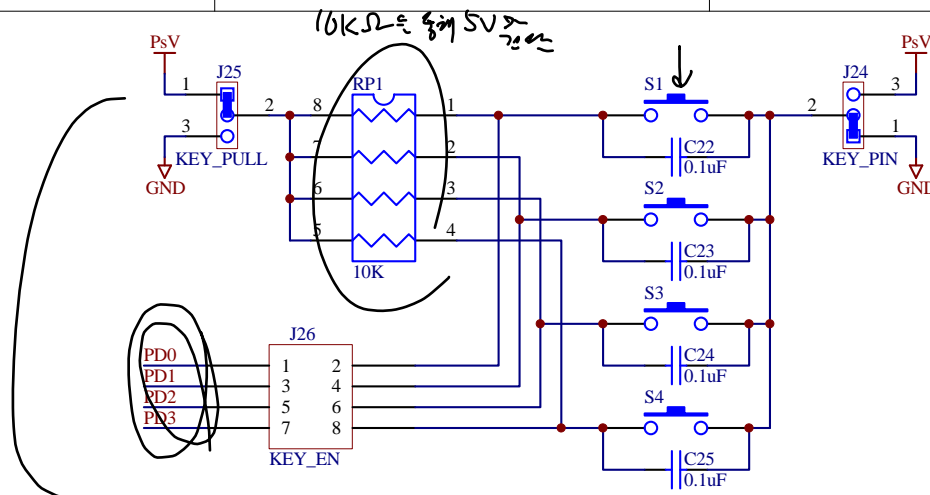
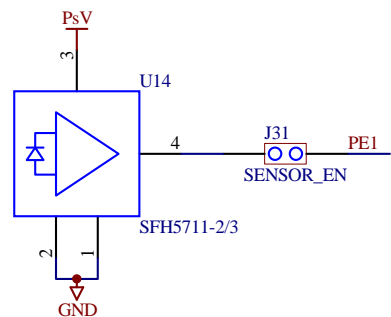


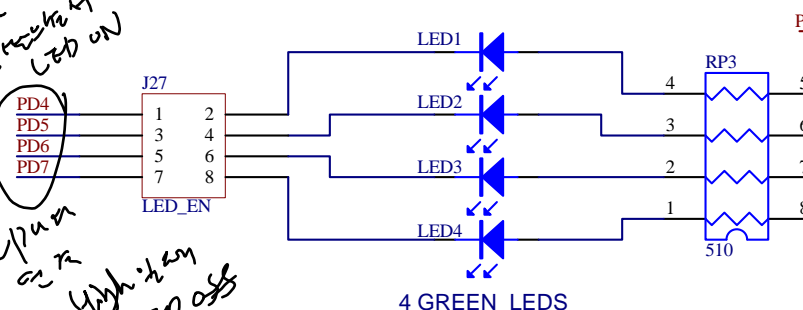
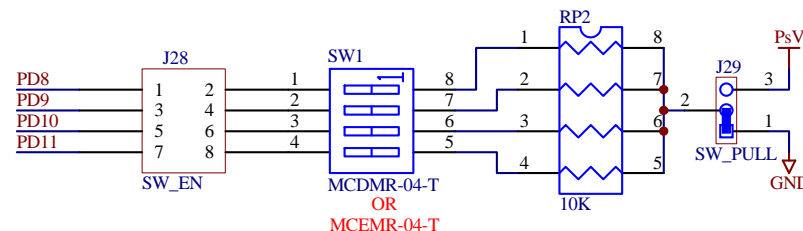
ANALOG INPUT



CP V22  
22.2kΩ  
LED ON

Input 10  
or LED  
22.2kΩ

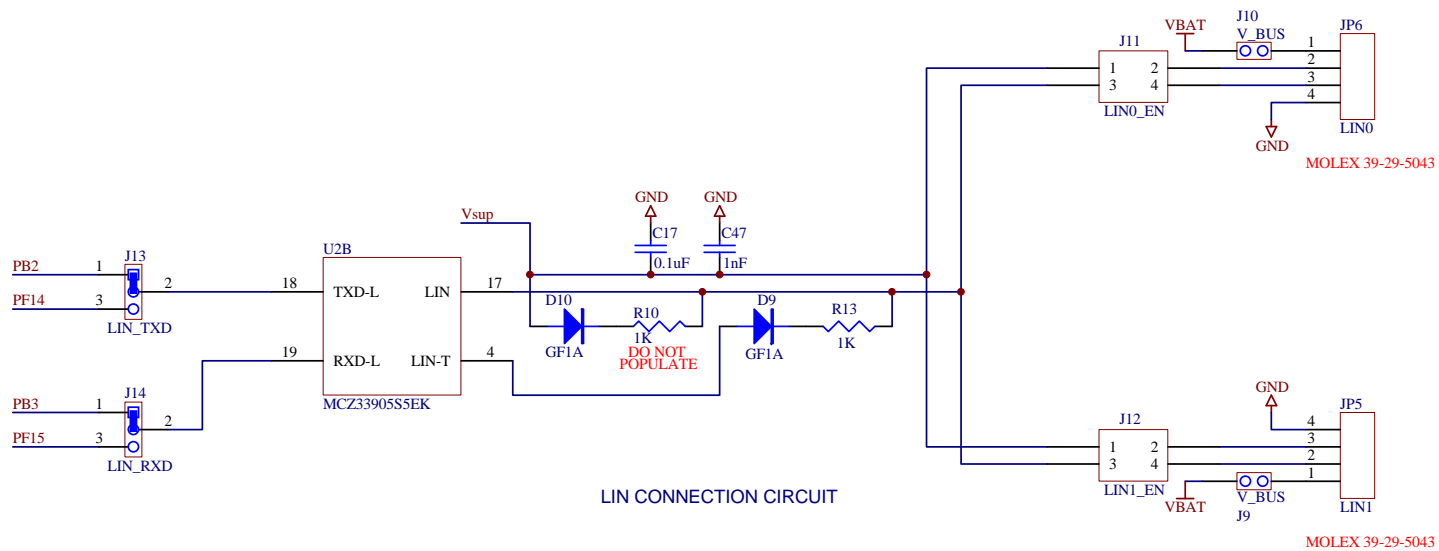
CP V22  
or 22.2kΩ  
High level  
LED OFF



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Watertown  
MA 02472  
USA

TRK-MPC5604P Schematic - USER IO

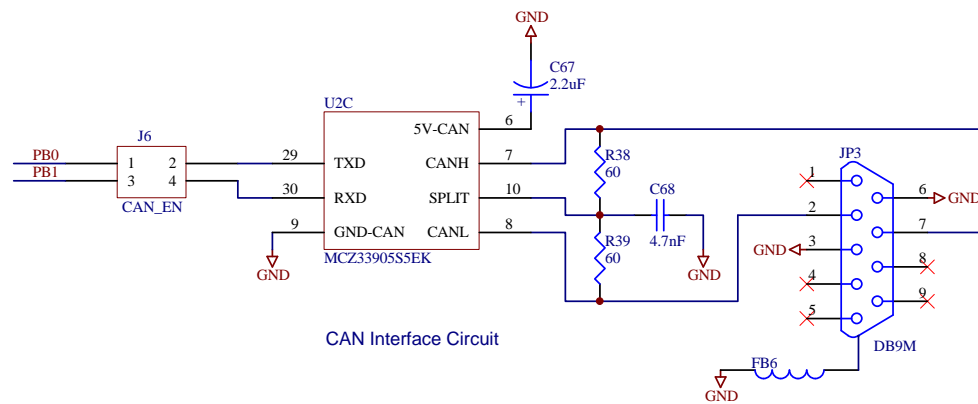
Size A	FCSM No.	DWG No. 0	Rev A
Scale	Sheet 4 of 10		



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TRK-MPC5604P Schematic - LIN

Size A4	FCSM No.	DWG No. 0	Rev A
Scale	Sheet 5 of 10		



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TRK-MPC5604P Schematic - CAN

Size  
A4

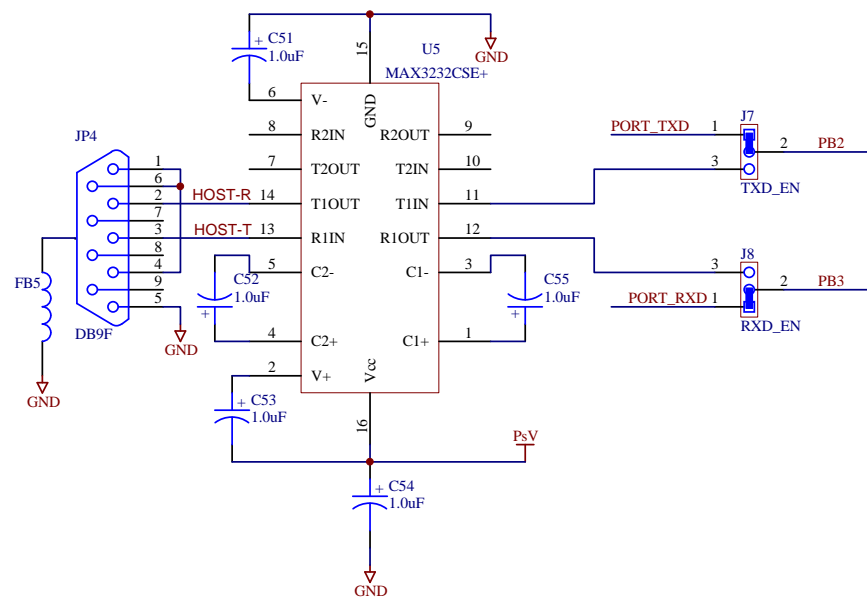
FCSM No.

DWG No.  
0

Rev  
A

Scale

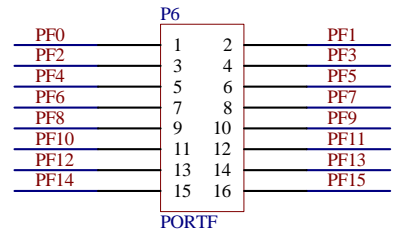
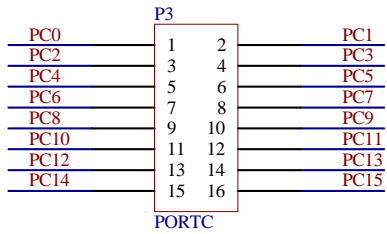
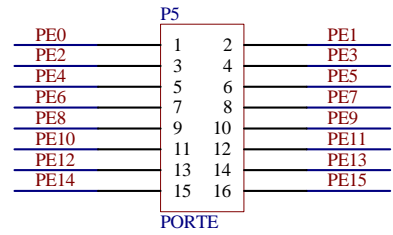
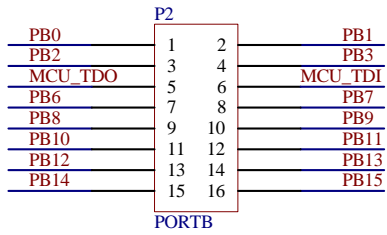
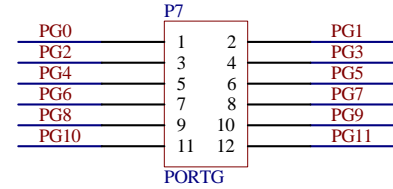
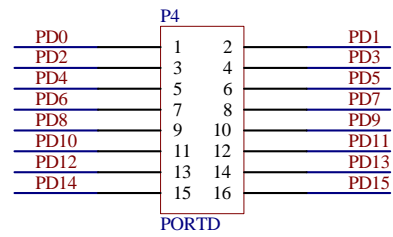
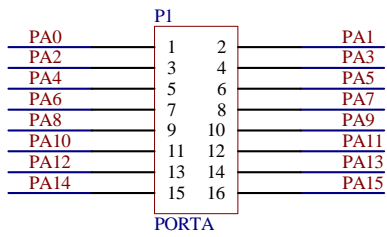
Sheet  
6 of 10



RS232 CIRCUITRY  
DO NOT POPULATE

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TRK-MPC5604P Schematic - RS232

Size A4	FCSM No.	DWG No. 0	Rev A
Scale	Sheet 7 of 10		



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TRK-MPC5604P Schematic - PORT IO

Size  
A

FCSM No.

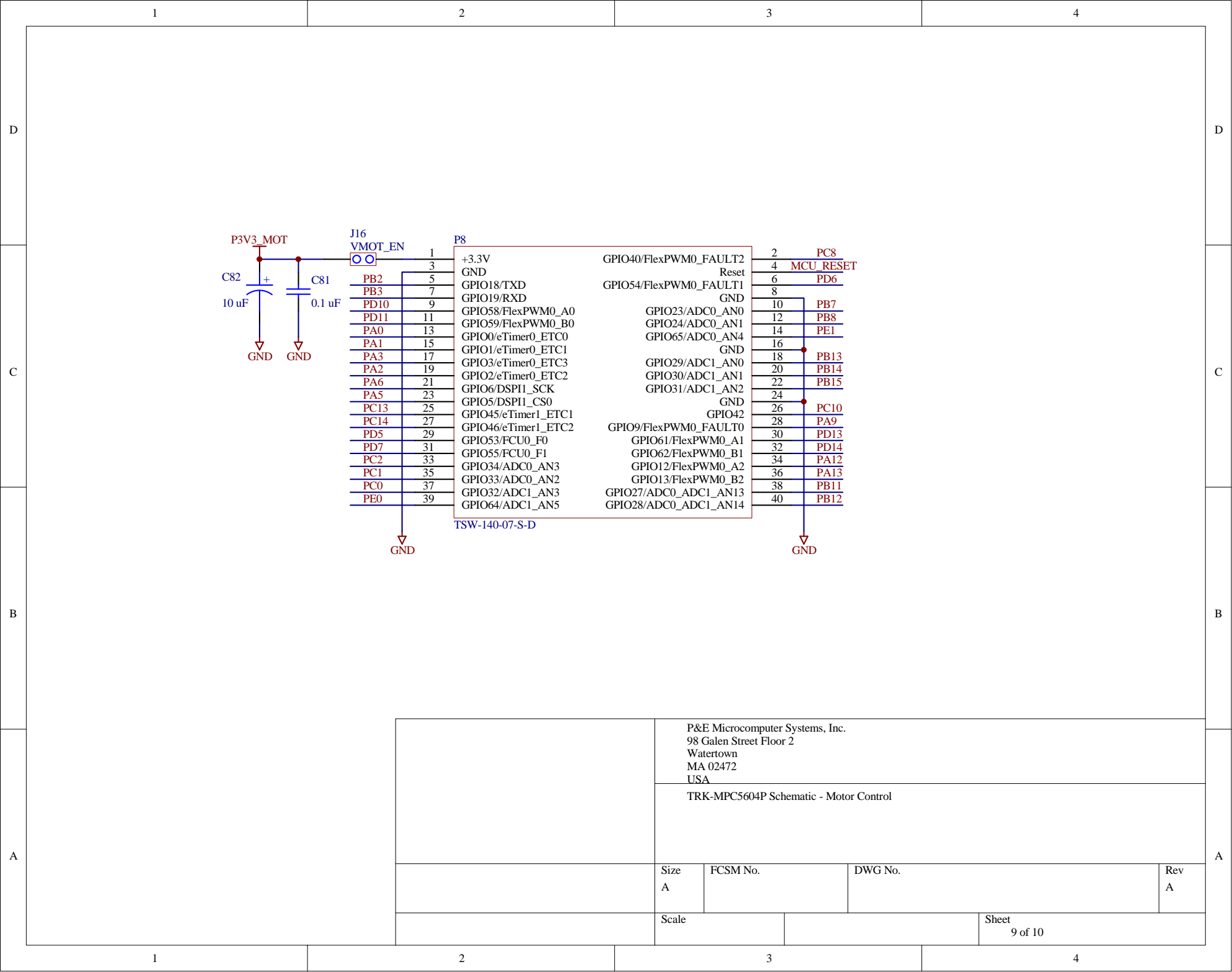
DWG No.  
0

Rev  
A

Scale

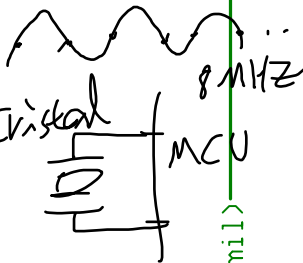
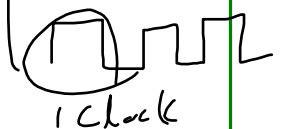
Sheet  
8 of 10





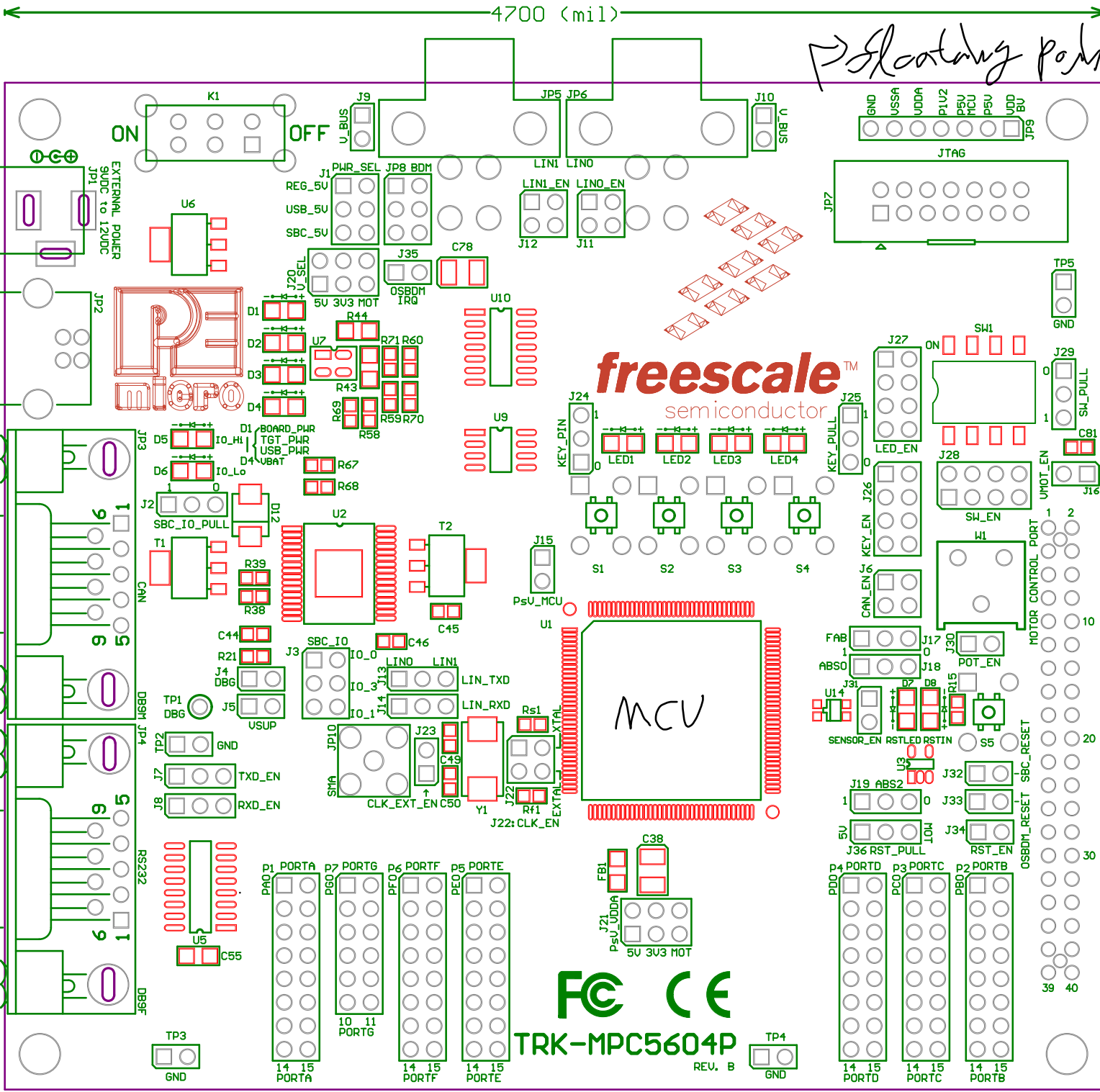


OSC - 8MHz  
 PLL => 84MHz (64MHz)  
 5V, 3.3V 전원.



Flash memory = USB  
 RAM (32KB)  
 Hard disk

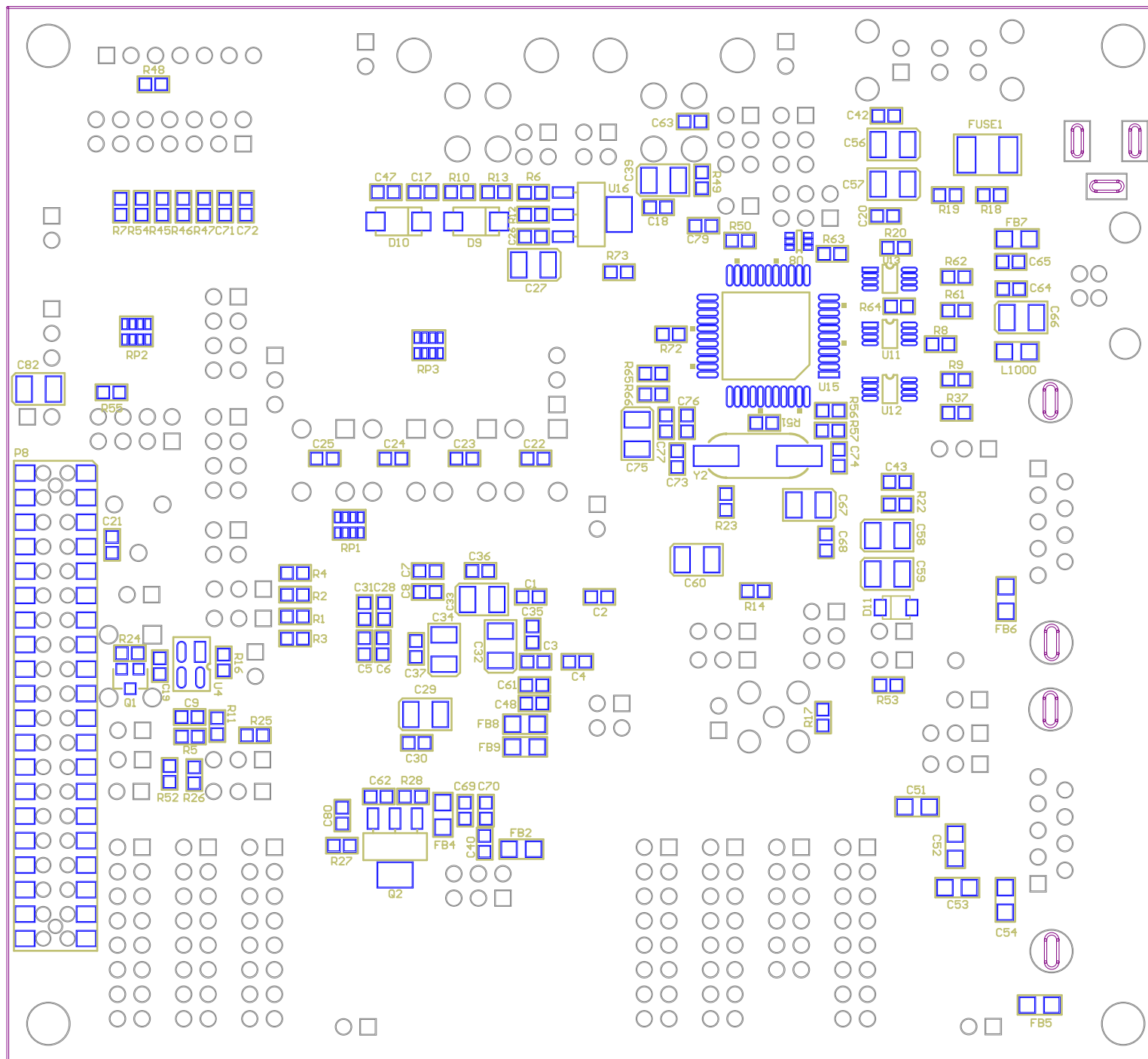
1 clock  
 1MHz  
 Hard disk  
 RAM  
 Booting

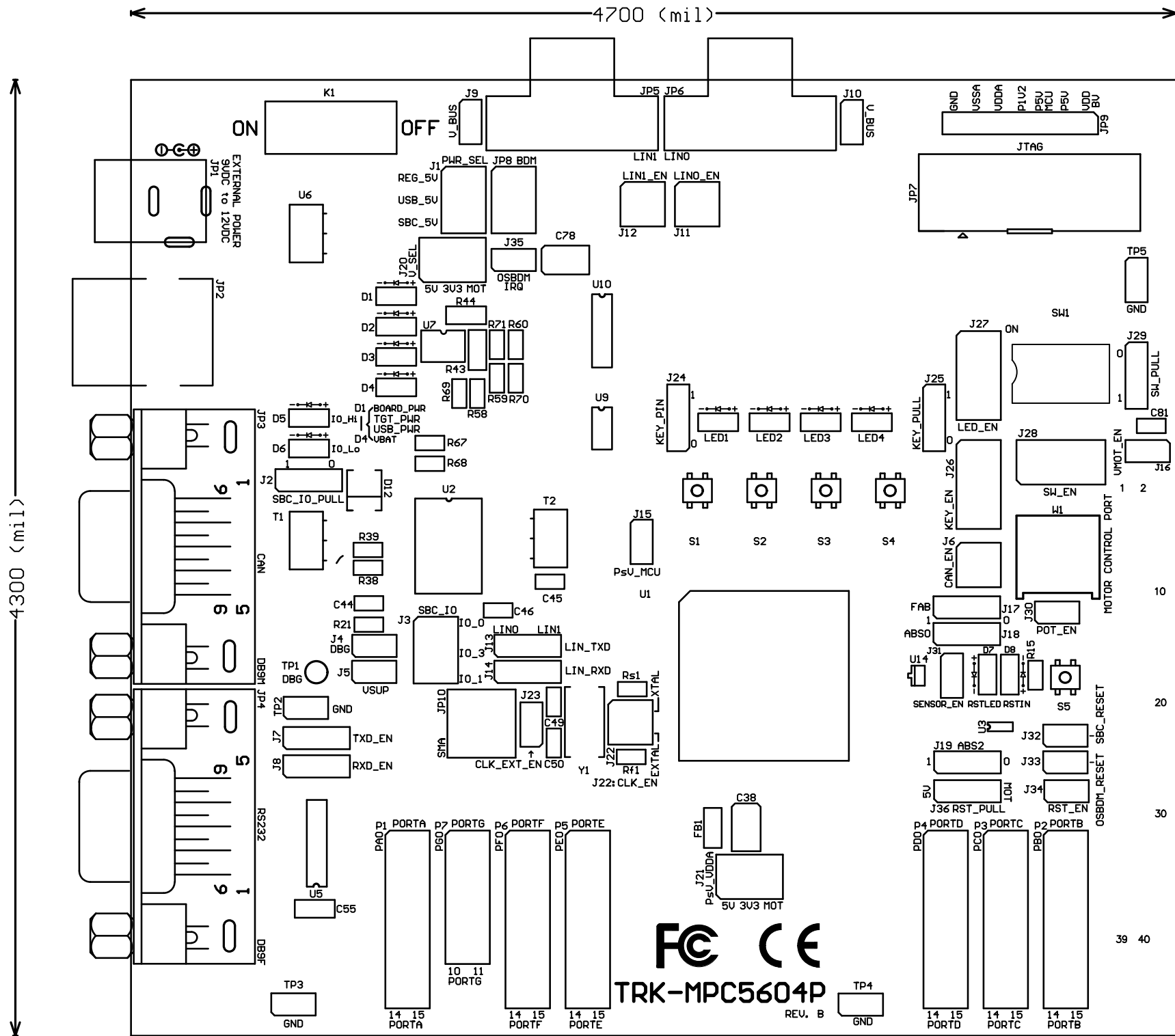


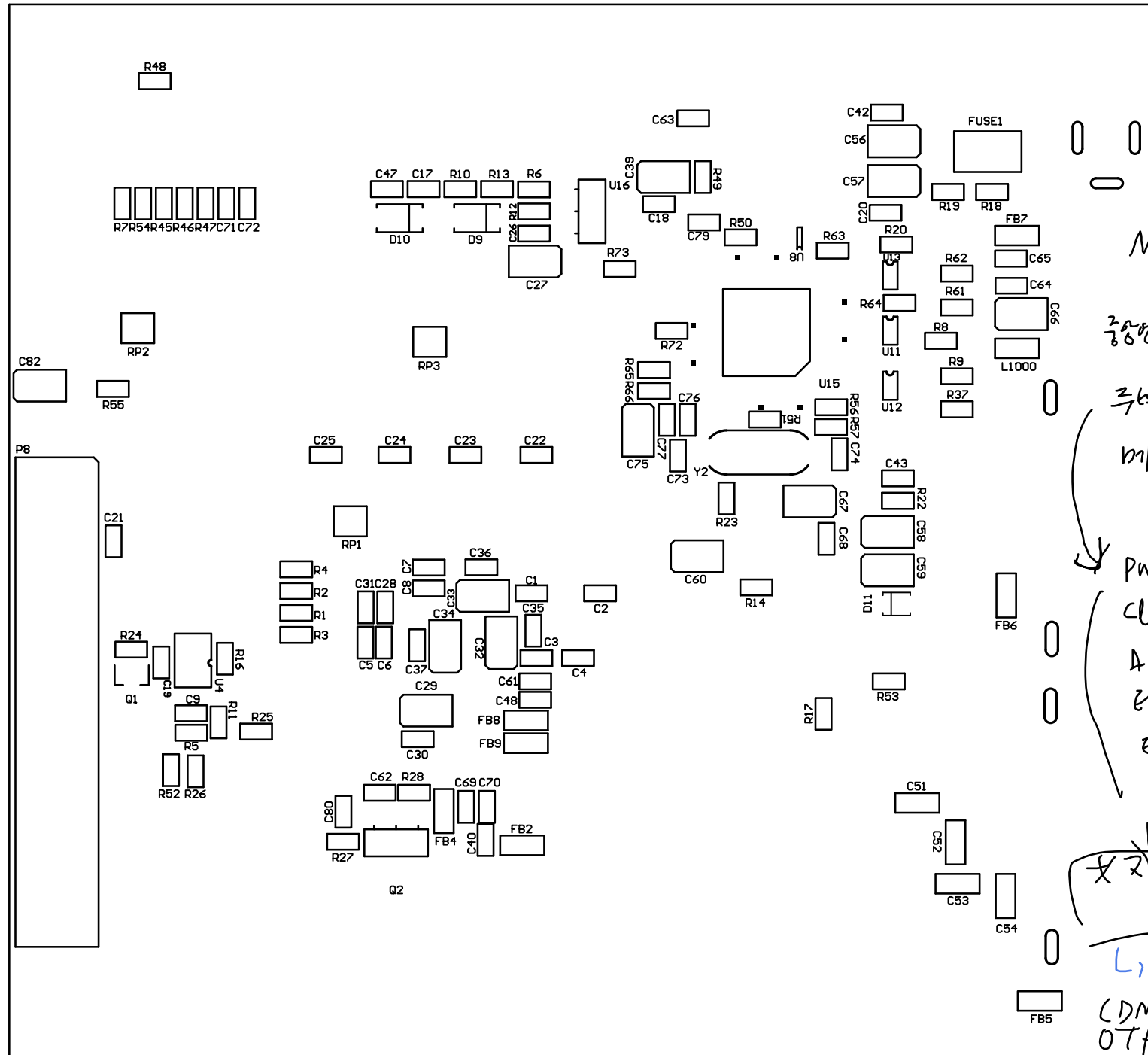
floating point x

fixed point  
 (소수점)

구분  
 99%







$M \subset U = \{ \text{오}, \text{ㄴ}, \text{ㄷ}, \text{ㄹ}, \text{ㄱ} \}$   
 $C_1 = \{ \text{오}, \text{ㄴ}, \text{ㄷ} \}$   
 $C_2 = \{ \text{오}, \text{ㄴ}, \text{ㄷ}, \text{ㄹ}, \text{ㄱ} \}$

MCU	CPU
-----	-----

$\frac{2}{3} \times \frac{4}{5} = \frac{8}{15}$	$\frac{2}{3} \times \frac{4}{5} = \frac{8}{15}$
$\frac{2}{3} \times \frac{4}{5} = \frac{8}{15}$	$\frac{2}{3} \times \frac{4}{5} = \frac{8}{15}$

m/32 / Flash  
RAM

PWM  $\frac{1}{2} \text{VCC}$   
 Clock out (7.5 MHz)  
 ADC  $\frac{1}{2} \text{VCC}$   
 Enol  $\frac{1}{2} \text{VCC}$   
 EM  $\frac{1}{2} \text{VCC}$   
 CAN  
 RS232  
 SPI  
 0-5V  $\rightarrow$  on 0-5V  
 ADC  
 DAC  
 0-5V  $\rightarrow$

[illegible]