

Chosen standard cell to design: Comparator 4-bit if possible, 2-bit implementation is most possible

Chosen track: 9T

VDD: 3V3

Proposal Structure

1. Team members
2. Team background
 1. Academic experience
 2. Work experience
3. Project information
 1. Goal of the project
 2. Datasheet/Design
 1. Functionality
 2. Block diagram
 3. Application (How to use the project?)
4. Week-by-week scheduling (Optional)
5. References