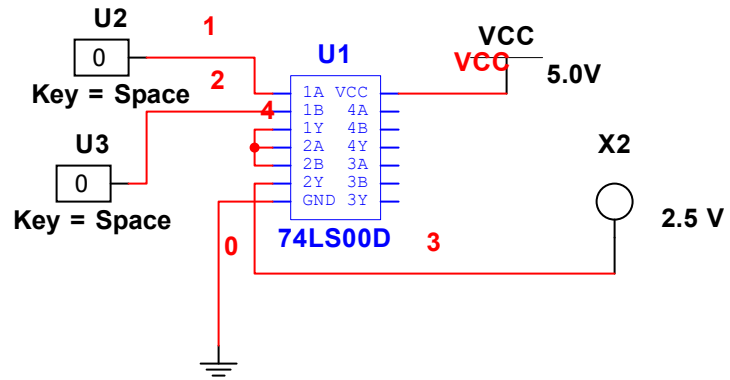
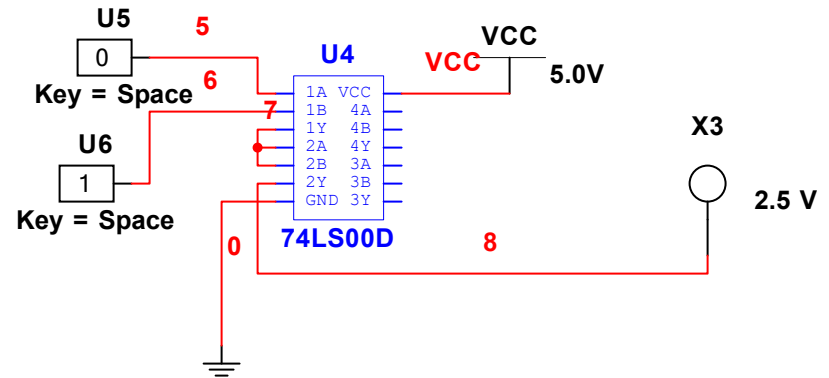


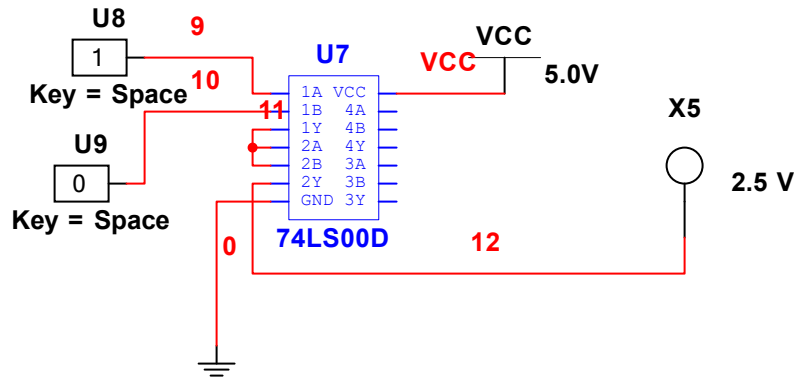
AND GATE USING NAND GATE



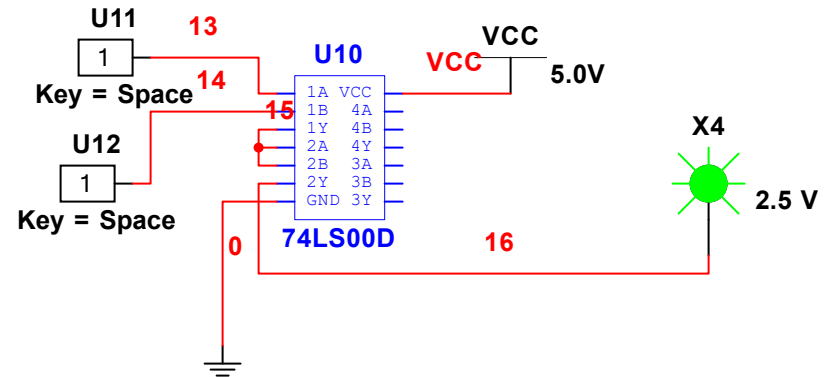
CASE-(0,0)



CASE-(0,1)

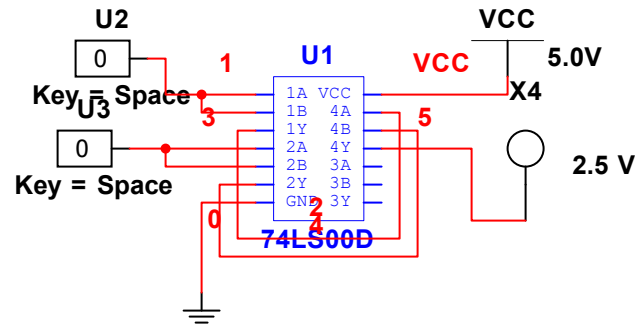


CASE-(1,0)

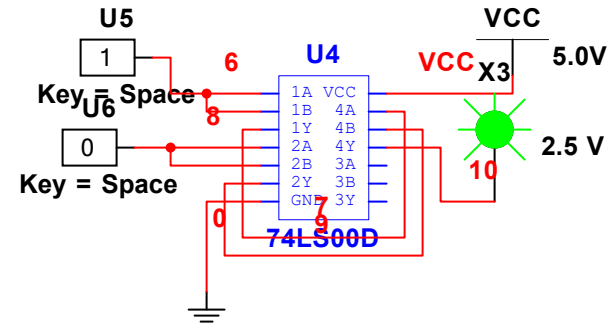


CASE-(1,1)

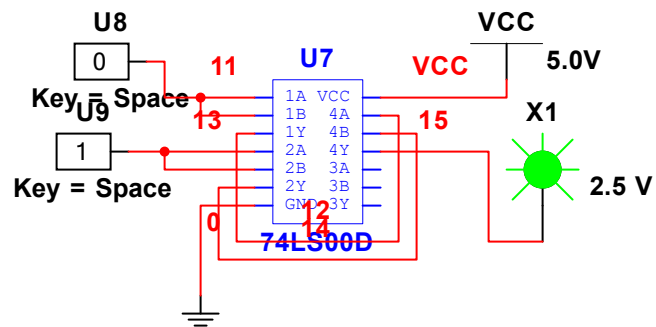
OR USING NAND GATE



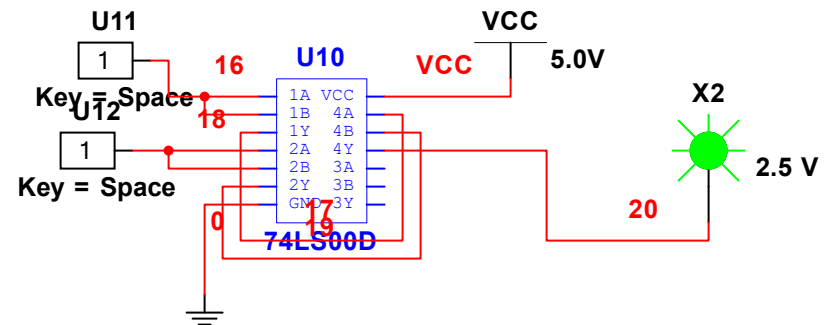
(0,0) CASE



(1,0) CASE

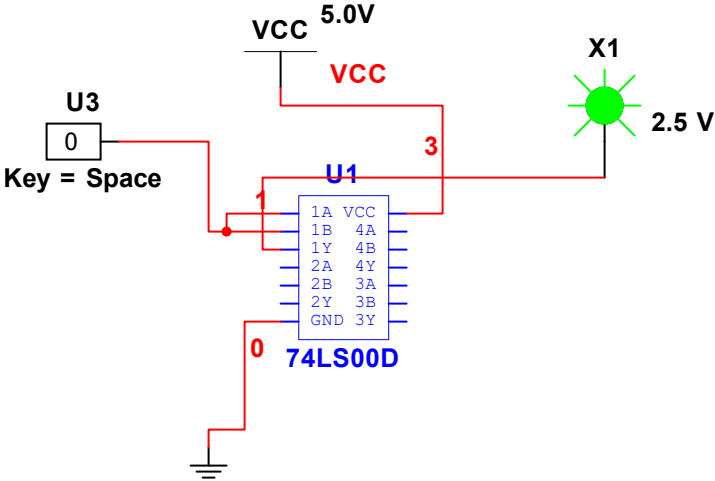


(0,1) CASE

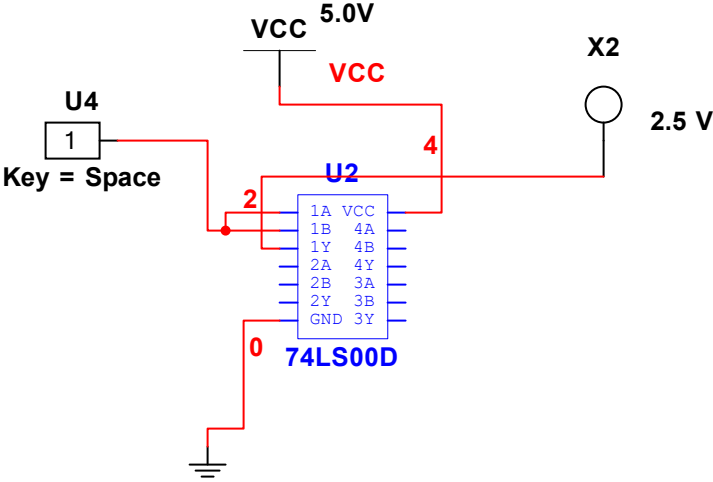


(1,1) CASE

NOT USING NAND GATE

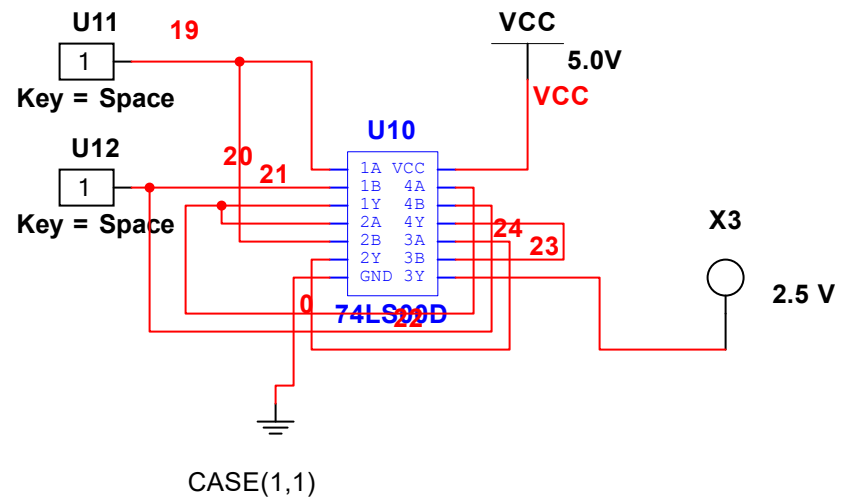
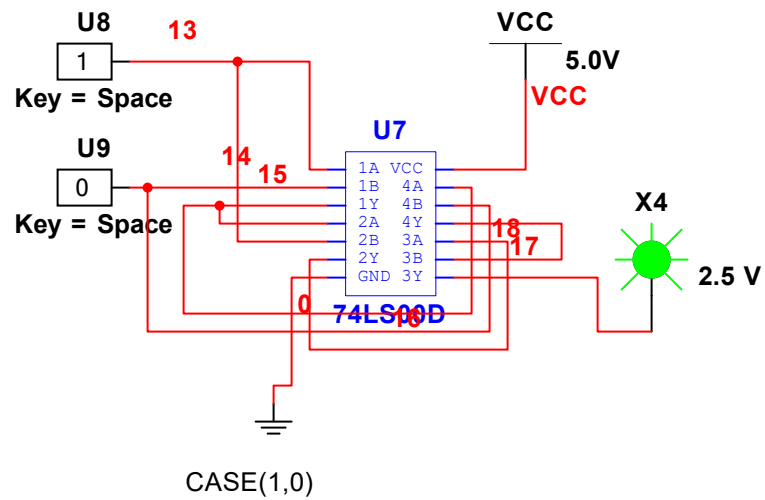
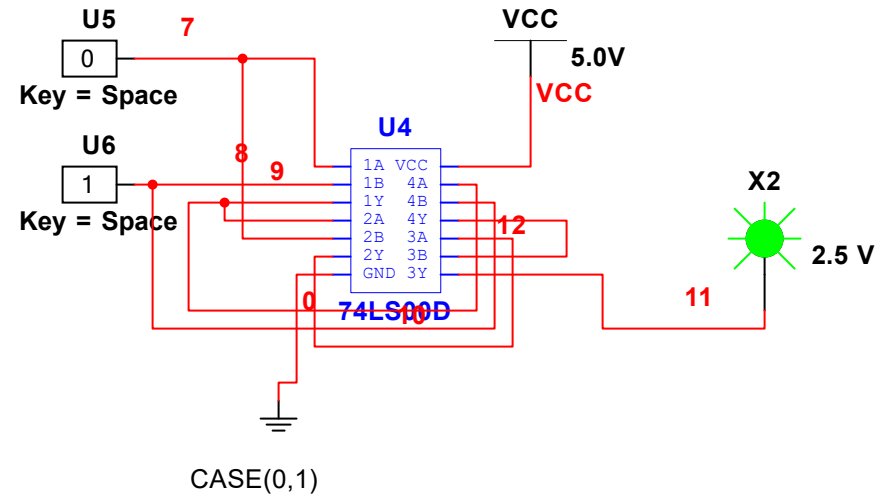
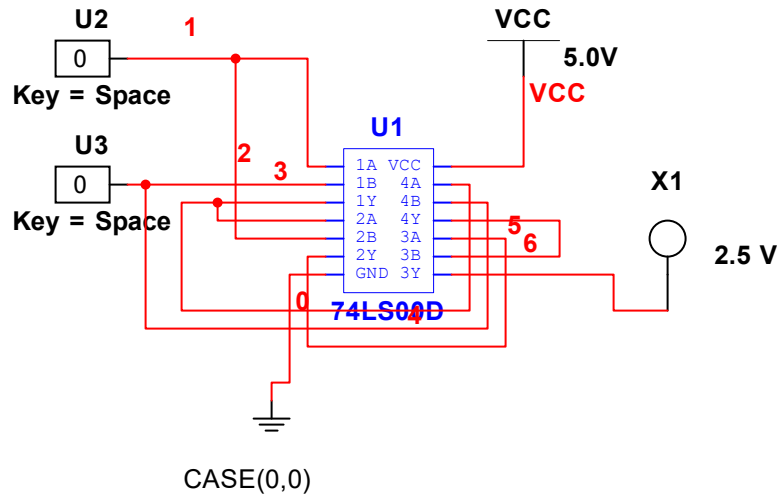


CASE-0



CASE-1

EX-OR USING NAND GATE



EX-NOR USING NAND GATE

