

Assignment No 6

Aim: Code Generation using LEX and YACC.

Objective:

1. To understand working of Code Generation Phase of Compiler.
2. To generate a code using sethi-ullman algorithm..

Software Requirement:

1. Linux Operating System
2. Lex compiler
3. Yacc compiler

Mathematical Model:

Consider a set S consisting of all the elements related to a program. The mathematical model is given as below,

$S = \{s, e, X, Y, Fme, DD, NDD, Mem\}$ shared Where,

s = Initial State e = End State

X = Input data. Here it is arithmetic expression. for eg. $a+b*c$.

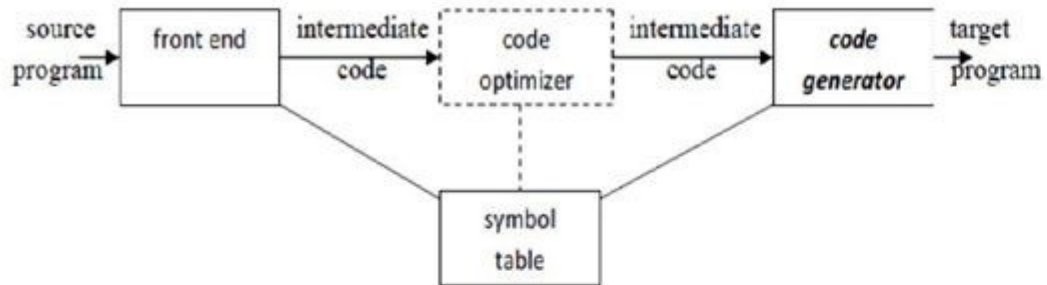
Y = Output. Here it is An assembly statements such as `Mov a,R0`.

Fme = Algorithm/Function used in program. for eg. `createnode()`, `labelling()`, `gencode()`

DD = Deterministic Data

NDD = Non deterministic Data $Mem\ shared$ = Memory shared by processor.

Position of code generator



The final phase in compiler model is the code generator. It takes as input an intermediate representation of the source program and produces as output an equivalent target program. The code generation techniques presented below can be used whether or not an optimizing phase occurs before code generation.

Issues in Code Generation Phase:

Following generic issues are concerned while designing code generator:

1. Input to the code generator:

The input to the code generator consists of the intermediate representation of the source program produced by the front end, together with information in the symbol table that is used to determine the run time addresses of the data objects denoted by the names in the intermediate representation.

We assume that prior to code generation the front end has scanned, parsed, and translated the source program into a reasonably detailed intermediate representation. The code generation phase can proceed on the assumption that its input is free of errors. In some compilers, this kind of semantic checking is done together with code generation.

2. Target program:

The output of the code generator is the target program. The output may take on a variety of forms: absolute machine language, relocatable machine language, or assembly language.

3. Memory Management:

Mapping names in the source program to addresses of data objects in run time memory is done cooperatively by the front end and the code generator. We assume that a name in a three-address statement refers to a symbol table entry for the name.

4. Instruction Selection:

For each type of three address statement, we can design a code skeleton that outlines the target code to be generated for that construct. Depending on the target machine, instruction generation might be having uniformity and completeness.

For example: $x = y + z$ can be written as,

```
MOV R1,z
ADD R1,y
MOV x,R1
```

5. Register Allocation :

Instructions involving register operands are usually shorter and faster

than those involving operands in memory. Therefore, efficient utilization of registers is particularly important in generating good code. The use of registers is often subdivided into two subproblems :

- (a) During register allocation , we select the set of variables that will reside in registers at a point in the program.
- (b) During a subsequent register assignment phase, we pick the specific register that a variable will reside in.

6. Evaluation Order :

Sometimes, order of evaluation also impacts the performance of system (such as efficiency of target code).

SETHI-ULLMAN ALGORITHM FOR CODE GENERATION :

The sethi-ullman code generation algorithm generates the shortest sequence of instructions. It is Provably optimal algorithm (w.r.t. length of the sequence). This algorithm is suitable for expression trees (basic block level). It is considered as a Machine model, wherein all computations are carried out in registers and instructions are of the form op R,R or op M,R. Always computes the left subtree into a register and reuses it immediately.

There are two phases while generating a code using sethi-ullman algorithm.

1. Labelling Phase:

It labels each node of the tree with an integer and fewest no. of registers require to evaluate the tree with no intermediate stores to memory.

- (a) For leaf nodes if n is the leftmost child of its parent then, $\text{label}(n) := 1$ else $\text{label}(n) := 0$
- (b) For internal nodes $\text{label}(n) = \max(l_1, l_2)$, if $l_1 < l_2$ then $l_2 = l_1 + 1$, if $l_1 = l_2$

2. Code Generation Phase:

Procedure $\text{gencode}(n)$

- (a) Case 0: if
 n is a leaf representing operand N and is the leftmost child of its parent then `print(Load N, top(RSTACK))`
- (b) Case 1: else if
 n is an interior node with operator OP , left child $n1$, and right child $n2$ then
if `label(n2) == 0` then
let N be the operand for $n2$;
`gencode(n1); print(OP N, top(RSTACK));`
- (c) Case 2: else if `((1 < label(n1) < label(n2)) and (label(n1) < r))`
then
`swap(RSTACK); gencode(n2); R := pop(RSTACK); gencode(n1);`
`/* R holds the result of n2 */ print(OP R, top(RSTACK)); push`
`(RSTACK,R); swap(RSTACK);`
- (d) Case 3: else if `((1 <= label(n2) <= label(n1)) and (label(n2) < r))`
then
`gencode(n1);`
`R := pop(RSTACK); gencode(n2); /* R holds the result of n1 */`
`print(OP top(RSTACK), R); push (RSTACK,R);`
- (e) Case 4: both labels are $\geq r$ else `gencode(n2); T:= pop(TSTACK);`
`print(Load top(RSTACK), T); gencode(n1); print(OP T, top(RSTACK));`
`push(TSTACK, T);`

Command :

```
$ lex jprogram namej.l
$ yacc -d jprogram namej.y
$ gcc lex.yy.c y.tab.c -ll -ly
$ ./a.out
```

CONCLUSION :

Thus, we have implemented sethi-ullman code generation algorithm using

LEX and YACC.

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<i>BECOC357</i>	Sunny Shah	23 / 09 / 2017	29 / 09 / 2017	

1 PLAGARISM REPORT :

Result:

54% Unique

ASSIGNMENT - 6 AIM : Code Generation using LEX and YACC.	- Unique
of Compiler. • To generate a code using sethi-ullman algorithm.	- Unique
• Yacc compiler INPUT : Input data as any valid arithmetic	- Unique
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data. Here it is arithmetic expression. for eg. $a+b*c$. Y	- Unique
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final phase in compiler model is the code generator. It	- Unique
program and produces as out- put an equivalent target program.	- Plagiarized
whether or not an optimizing phase occurs before code generation.	- Unique
are concerned while designing code generator: 1. Input to	Unique

Figure 1: Plagarism Checker www.smallseotools.com/plagarism-checker