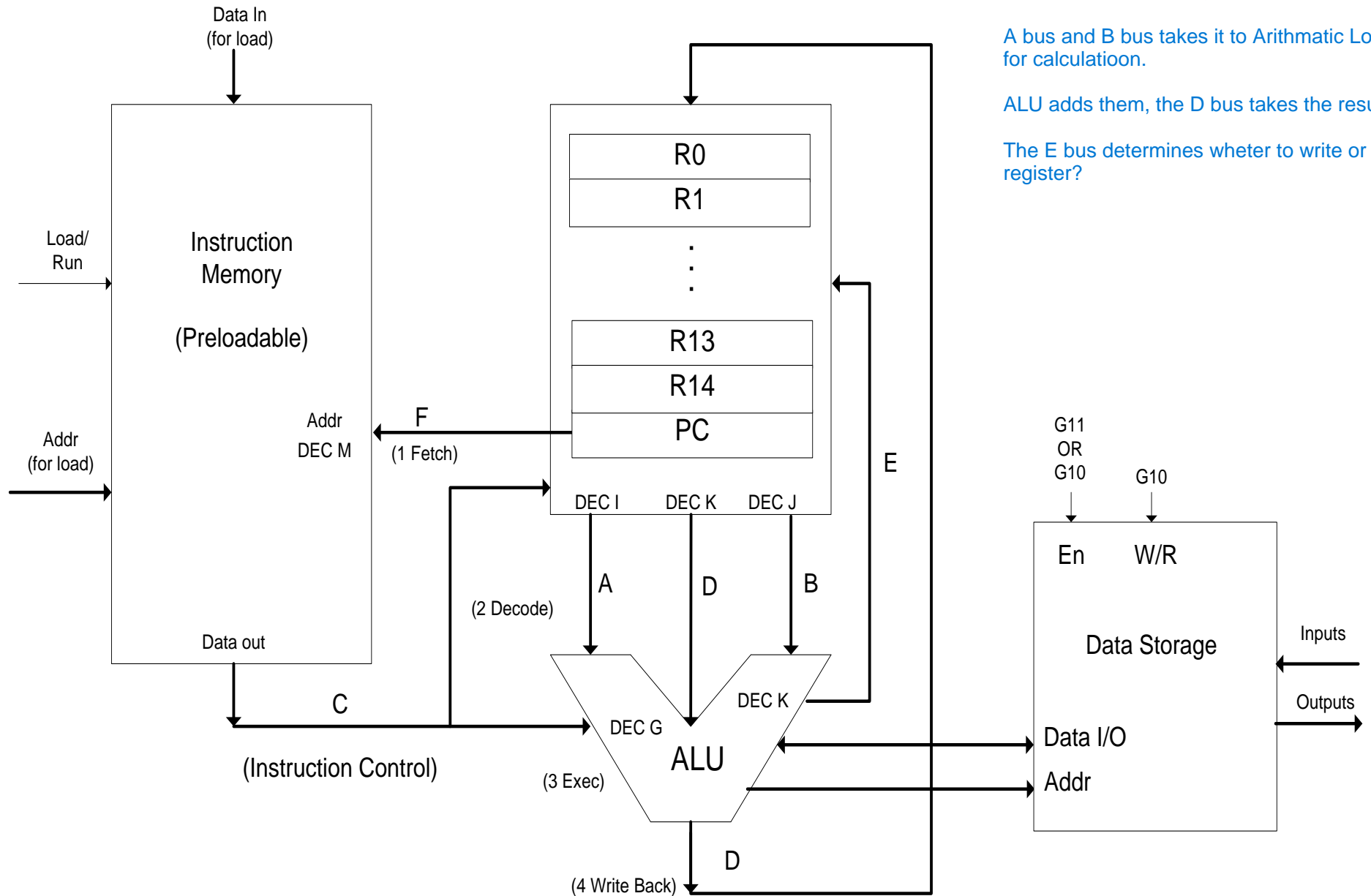


1. Fetch
2. Decode
3. Execute
4. Write result back to register

If we don't write anything, PC goes +1.

BCPU (The Big Picture)



Add(r2, r3, r4)

Add is 4 bit, r2, r3, r4 is 4 bit each - total 16 bit.

Decoder I decodes ?

Decoder J decodes r3, r4?

A bus and B bus takes it to Arithmetic Logic Unit for calculation.

ALU adds them, the D bus takes the result bus?

The E bus determines wheter to write or not in register?