

POWER ELECTRONICS PROJECT

(PE-4)

GROUP-29

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TITLE:

20W, 12 V to 24V Boost converter, 10% inductor current ripple, 2% output voltage ripple.

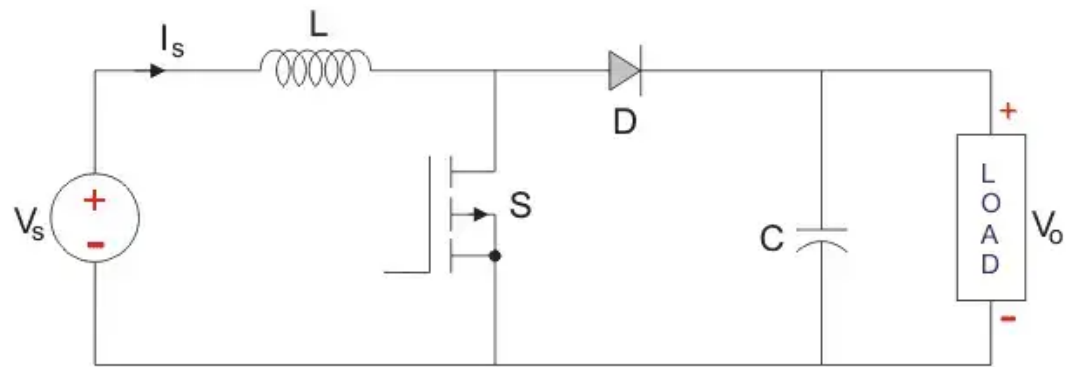
OBJECTIVE :

To study the operation of a boost converter.

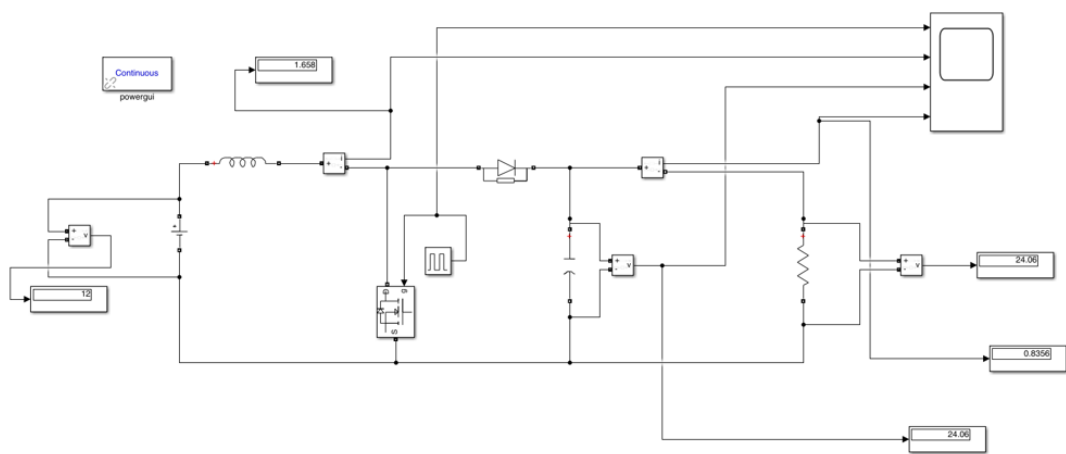
MATERIALS REQUIRED

Serial number	Item	Rating	Quantity
1	Capacitor	8.784 μ F, 60V, 5A	1
2	Inductor	729.176 μ H, 1A DC, 1.4A saturation current	1
3	MOSFET (IRF840)	40V, 1A	1
4	Diode	60V, 2A	1
5	Resistance	28.8 Ω	1
6	Voltage Source	12 V	1
7	PCB	-	1
8	Multimeter	-	1
9	Connecting wires	-	-
10	Gate trigger circuit	15V/0V output	1
11	Two pin terminal block	-	2

CIRCUIT DIAGRAM:

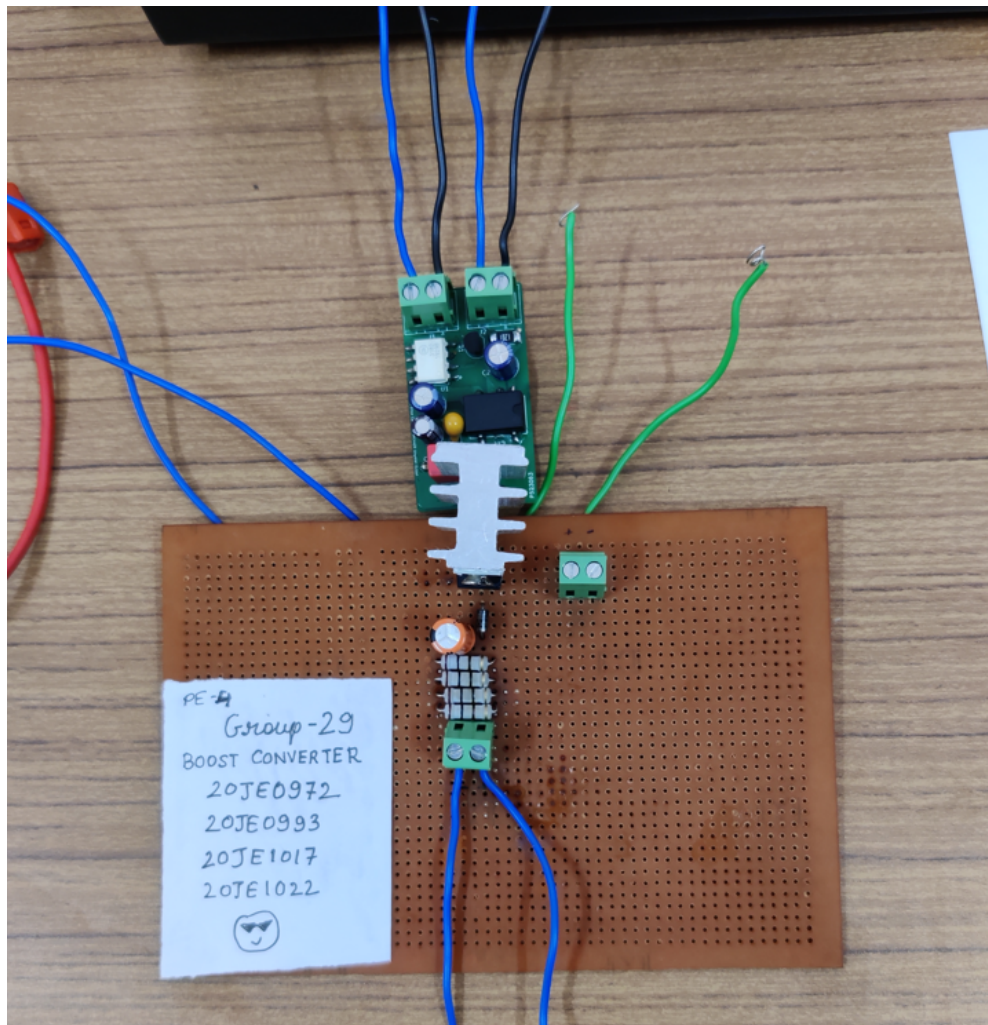


BLOCK DIAGRAM:



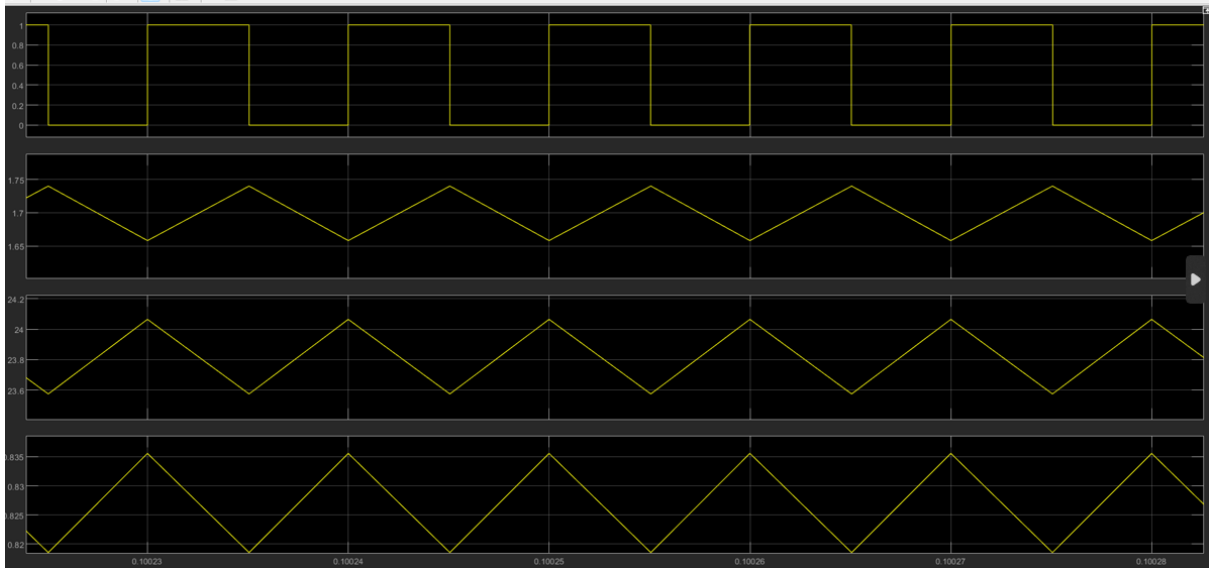
Block diagram of the booster circuit with theoretical values in MATLAB

HARDWARE CIRCUIT:



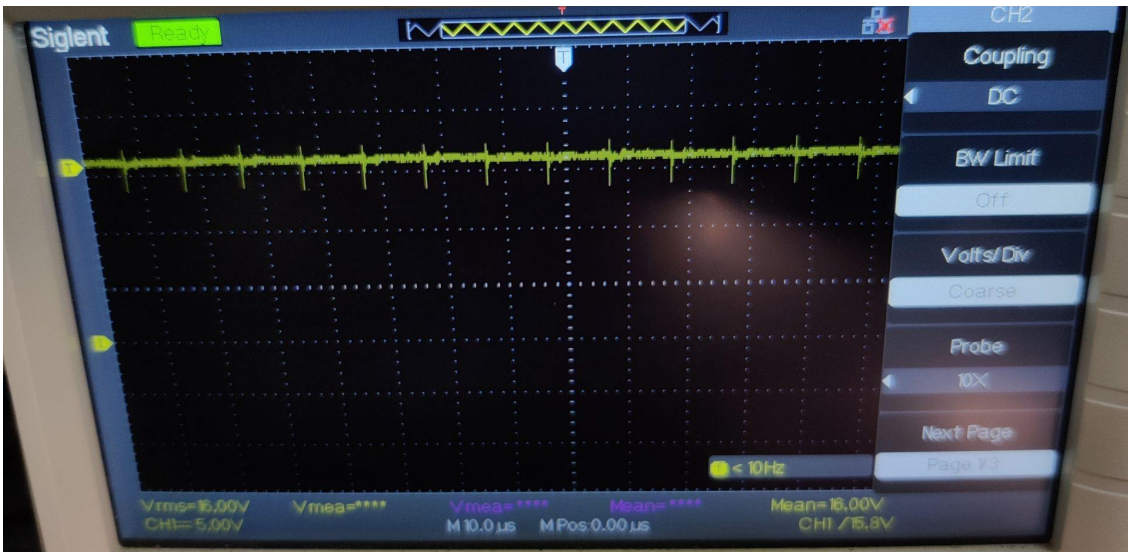
The circuit made using the components from the lab

SIMULATION RESULTS:



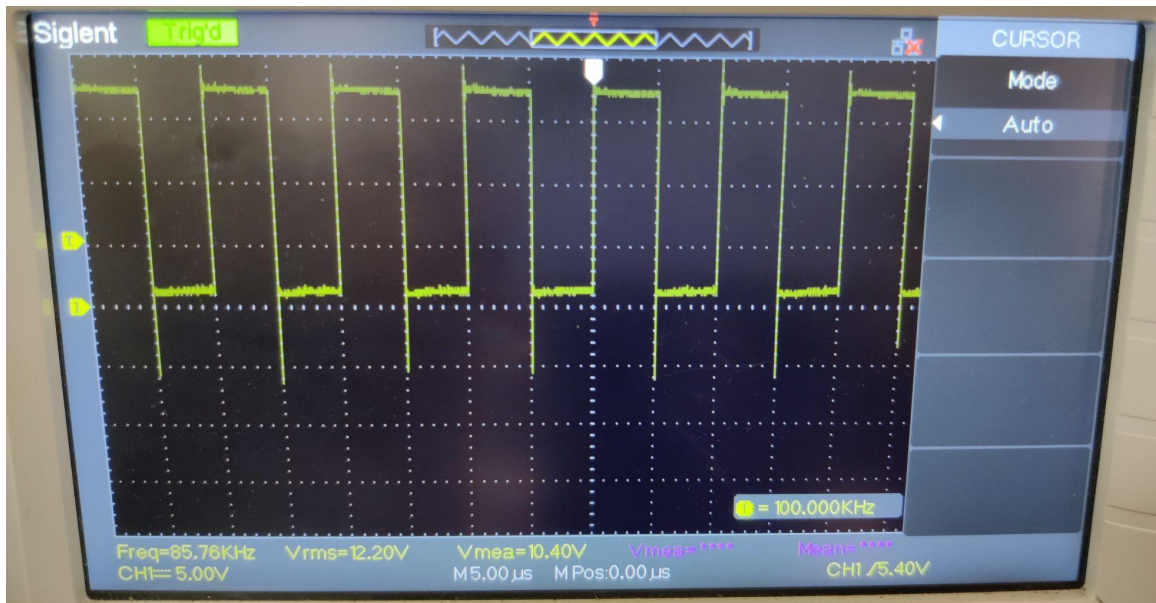
The output waveforms obtained after running the block diagram in MATLAB

CIRCUIT RESULTS:



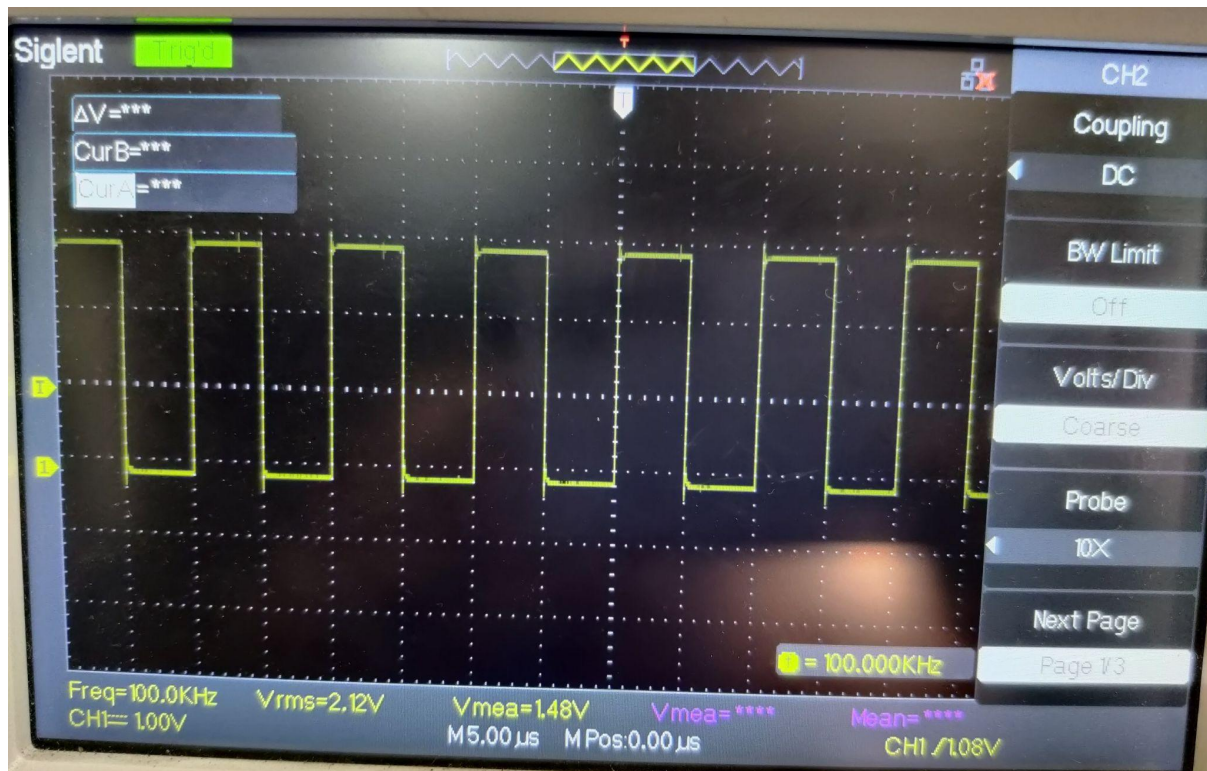
Output

voltage



Pulse

gate output voltage(1)



Gate pulse output (2)

THEORY:

A boost converter is a type of DC-DC converter that steps up the input voltage to a higher output voltage. It operates based on the principle of inductor energy storage and transfer.

Boost converters are widely used in various applications such as battery charging, LED drivers, and power supplies. The basic operation of a boost converter involves two main stages: the on-time (charging) and the off-time (discharging) of the inductor.

The key components in a boost converter are the inductor (L), the switch (typically a MOSFET), the diode (D), and the capacitor (C).

During the on-time, the switch is closed, and the input voltage is applied across the inductor, causing the current through the inductor to increase linearly. The inductor stores energy in its magnetic field. The diode is reverse-biased and prevents the output voltage from being applied across the inductor.

During the off-time, the switch is open, and the inductor discharges its stored energy to the output load through the diode, as well as charges the output capacitor. The output voltage is higher than the input voltage due to the energy stored in the inductor.

It is already established that the net change in current in inductor over the entire cycle is zero:

$$\begin{aligned}\therefore (\Delta i_L)_{closed} + (\Delta i_L)_{open} &= 0 \\ \left(\frac{V_{in} - V_o}{L} \right) (1 - D)T + \left(\frac{-V_o}{L} \right) DT &= 0 \\ \frac{V_o}{V_{in}} &= \frac{1}{1 - D}\end{aligned}$$

CALCULATIONS:

Using the voltage second balance method we can find the duty cycle for obtaining circuit of these specifications:

Volt-sec balance
stored= released

$$(V_s \times D \times T) + (V_s - (V_o + V_d))(1 - D)T = 0$$

Using this we obtain

$$D = (V_o + V_d - V_s) / (V_o + V_d)$$

$$D = (24 + 0.3 - 12) / (24 + 0.3)$$

$$D = 0.50617 \text{ or } 50.6017\%$$

1) Determine the output power

The output power of the converter is given as 20W.

2) Determine the output current

The output current of the converter can be calculated by dividing the output power by the output voltage:

Output current = Output power / Output voltage

$$\text{Output current} = 20\text{W} / 24\text{V}$$

$$\text{Output current} = 0.833\text{A}$$

3) Determine the switching frequency

The switching frequency can be chosen based on the desired trade-off between efficiency and size/cost of components. A higher switching frequency results in smaller and cheaper components, but lower efficiency due to switching losses. A lower switching frequency results in higher efficiency but larger and more expensive components.

Assuming a switching frequency of 100 kHz.

$$f = 100 \times 10^3 \text{ Hz}$$

4) Determine the inductor value

The inductor value can be calculated using the following formula:

$$L = (V_{in} \times D) / (f \times \Delta I_L)$$

where V_{in_min} is the minimum input voltage, D is the duty cycle, f is the switching frequency, and ΔI_L is the inductor current ripple.

Assuming a minimum input voltage of 12V and a duty cycle of 50%, the inductor value can be calculated as follows:

$$L = (12V \times 0.50617) / (10e5 \times 0.1 \times 0.833)$$

$$L = 0.6 / (f \times 10e5 \times 0.1 \times 0.833)$$

$$L = 729.176 \mu H$$

5) Determine the output capacitor value

The output capacitor value can be calculated using the following formula:

$$C = (I_L \times D) / (f \times \Delta V)$$

where I_L is the inductor current, D is the duty cycle, f is the switching frequency, and ΔV is the output voltage ripple.

Assuming a 10% inductor current ripple and a 2% output voltage ripple, the output capacitor value can be calculated as follows:

$$C = (0.833A \times 0.50617) / (100 \text{ kHz} \times 0.02 \times 24)$$

$$C = 8.784 \mu F$$

6) Calculations for Input power calculation

$$\text{Input Power (W)} = \text{Input Voltage (V)} \times \text{Input Current (A)}$$

7) Calculations for Output power calculation

$$\text{Output Power (W)} = (\text{Experimental Output Voltage (V)})^2 / \text{Load resistance (Ohms)}$$

*Here the load resistance for the experimental setup is 99.1 Ohms

8) Calculation of efficiency

$$\text{Efficiency (\%)} = (\text{Output Power (W)} / \text{Input Power (W)}) \times 100$$

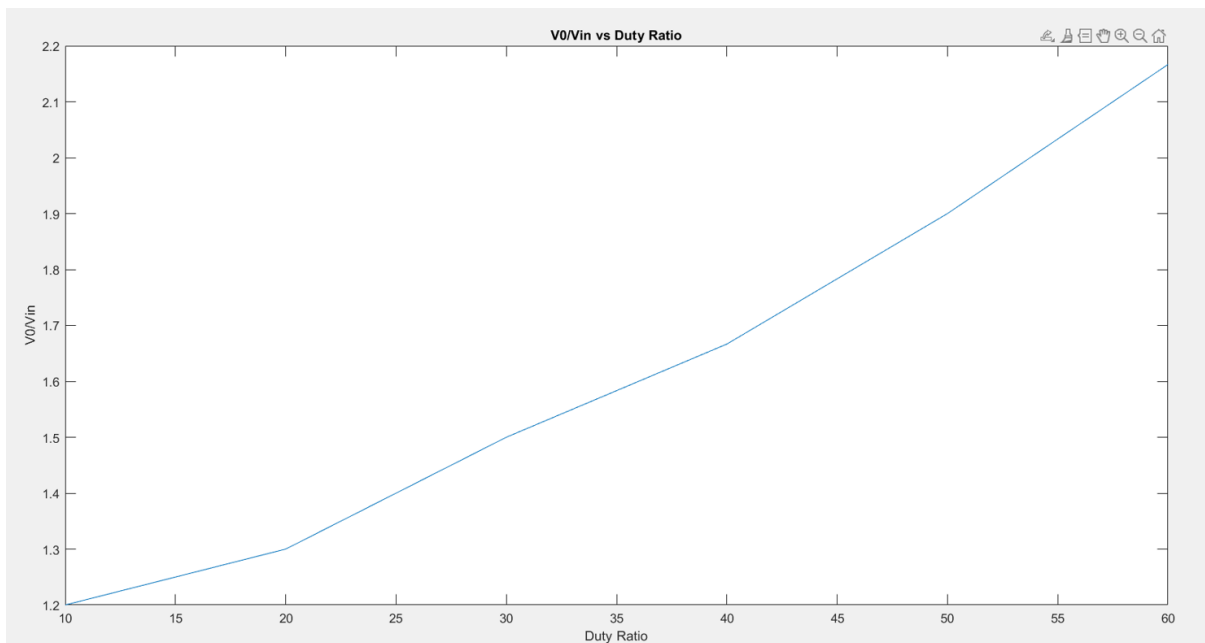
9) Peak to peak ripple obtained on the experiment day with the input voltage as 5V

$$I_{pp} = 566 \text{ mA}$$

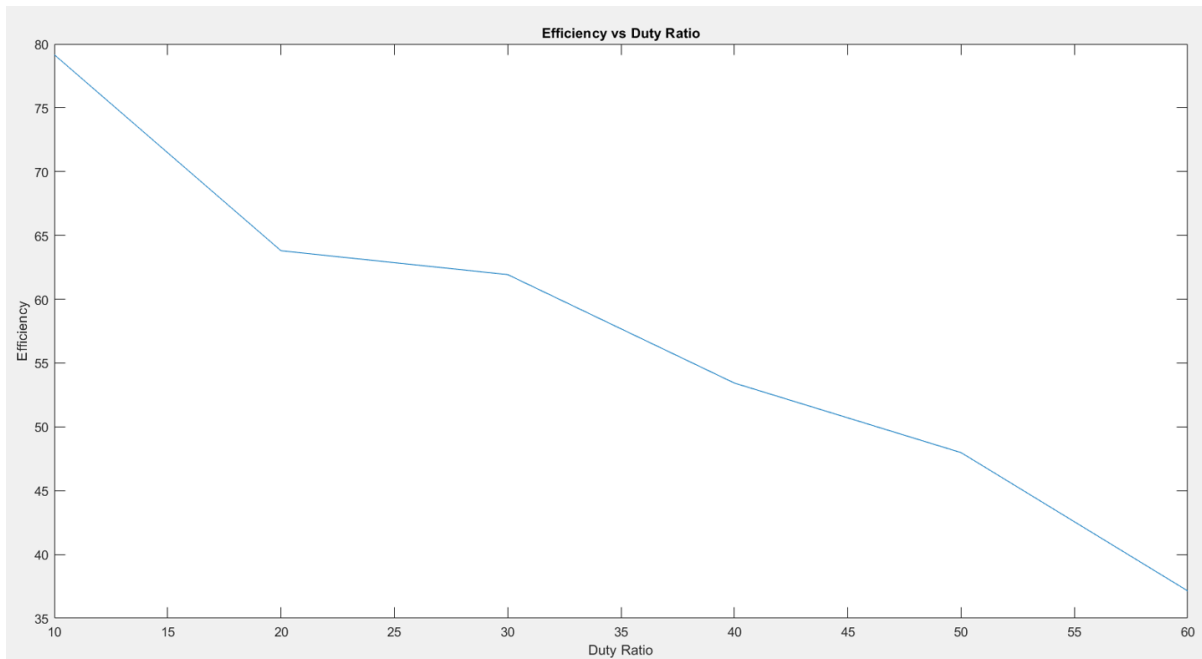
OBSERVATION TABLE:

Duty	Input Voltage	Experimental output Voltage	Calculated output voltage	Input Current	Input Power	Output Power	Efficiency
(%)	(Volts)	(Volts)	(Volts)	(Amperes)	(Watts)	(Watts)	(%)
10	12	14.4	13.33	0.22	2.64	2.09	79.16
20	12	15.6	15.00	0.32	3.84	2.45	63.80
30	12	18	17.14	0.44	5.28	3.27	61.93
40	12	20	20.00	0.63	7.56	4.04	53.43
50	12	22.8	24.00	0.91	10.92	5.24	47.98
60	12	26	30.00	1.53	18.36	6.82	37.14

GRAPHS:



V_o/V_{in} - Duty Ratio



Efficiency- Duty Ratio

Analysis and Discussions:

Here in this circuit we supplied a 12 V input voltage through which we tried to obtain a 24 V output voltage by using a Boost converter circuit. Here to obtain the Output through the calculations we found the Duty ratio to be 0.50617 which we then applied through pulses to the input waveform in order to get the desired output voltage of 24V.

We also through the data saw that the smaller the duty cycle (D) the more is the efficiency of the power obtained.

Conclusions:

All of the specifications stated previously have been met by this boost converter design. MATLAB simulation using calculated parameters were performed and corresponding waveforms were obtained. The output voltage across the output capacitor is nearly 24V with a maximum output ripple of 2%. Hardware design of BOOST CONVERTER was done. It is observed, by varying duty cycle output voltage also changes.

References:

- [*Basic Calculation of a Boost Converter's Power Stage \(Rev. D\)*](#)^a
- [*Boost Converter | Step Up Chopper | Electrical4U*](#)