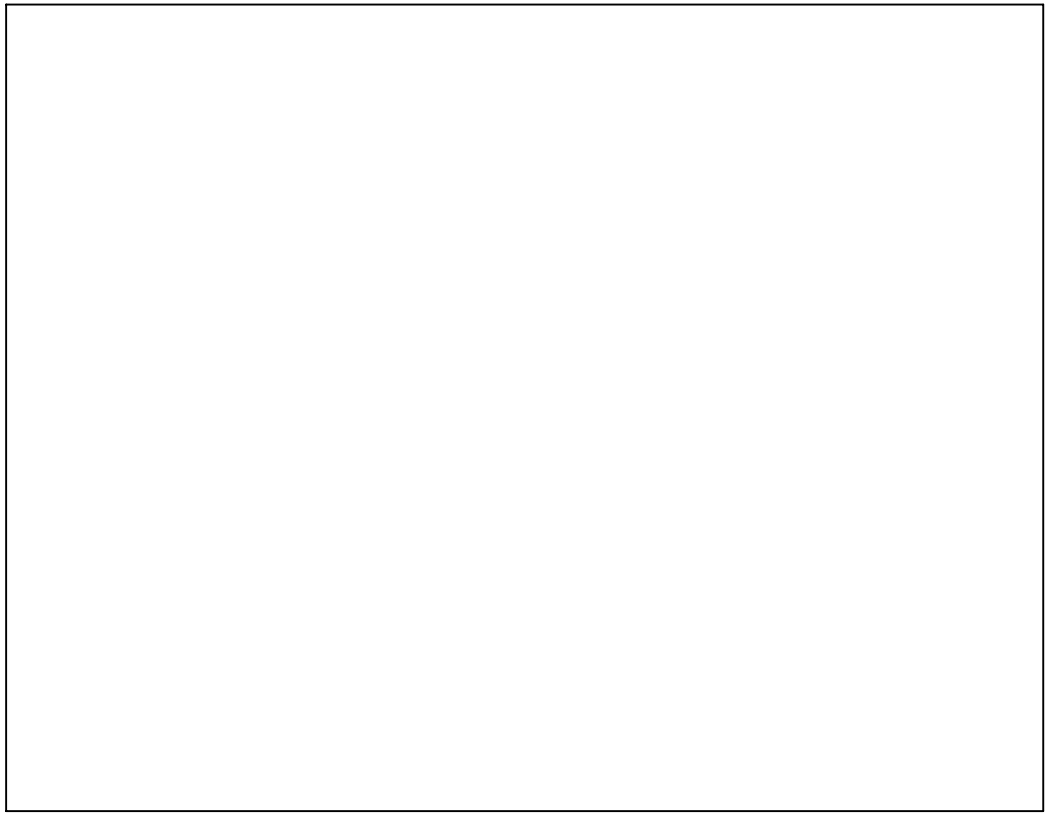
SRM200 JIG Schematics

Revision history

2019.05.08	V0.1	Initial Release.
2019.05.09	V0.11	VDD_SFM, VDD_Main connect.

Block Diagram



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TITLE: SRM200 EVB			
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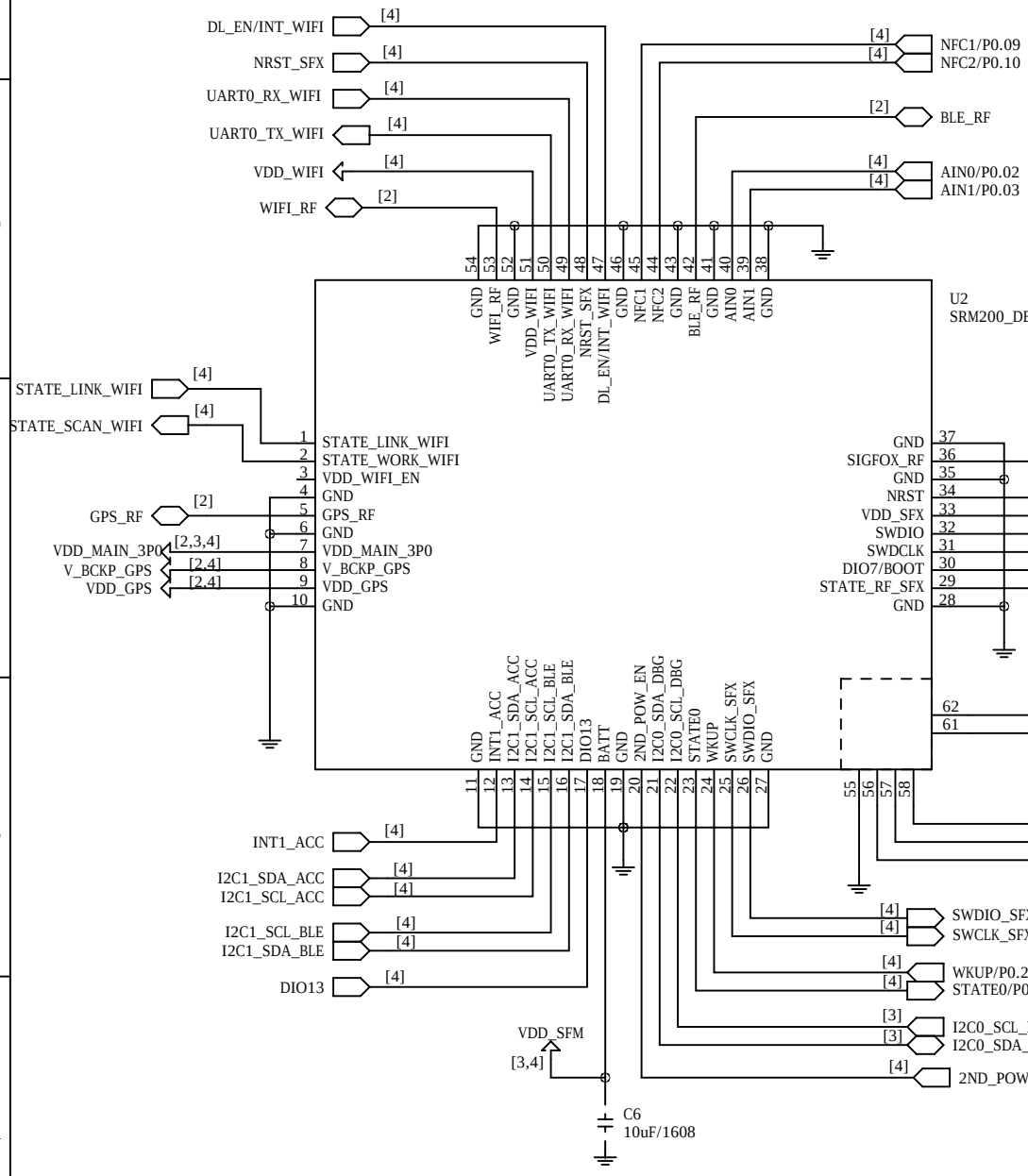
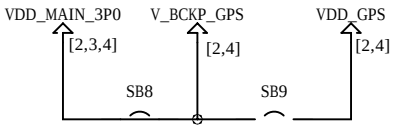
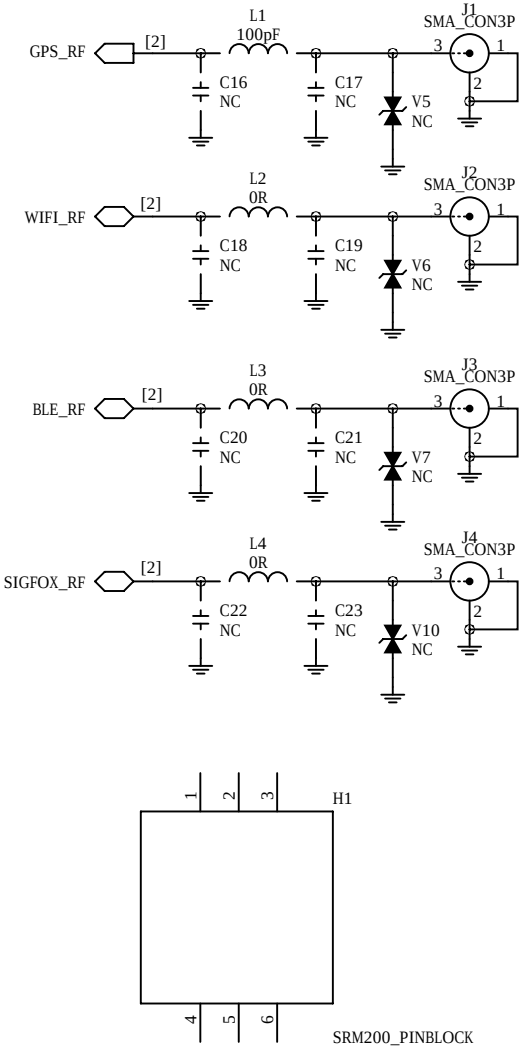
[SRM200A_PIN]



GPS Backup Power Set



RF_IN/OUT



UBlox

55 GND	56 TX	61 DM	62 DP
57 VDD	58 RX		

SW3_1 : ON(TX)
SW3_2 : OFF
SW3_3 : OFF
SW3_4 : ON(VDD)

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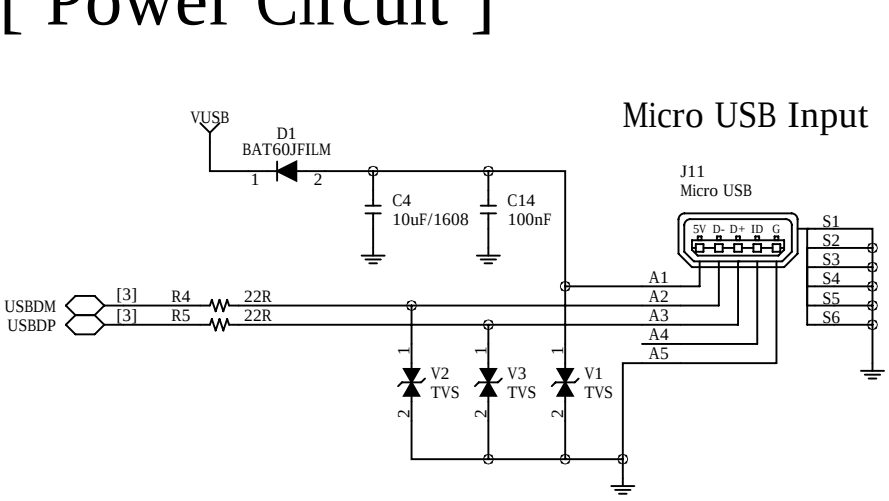
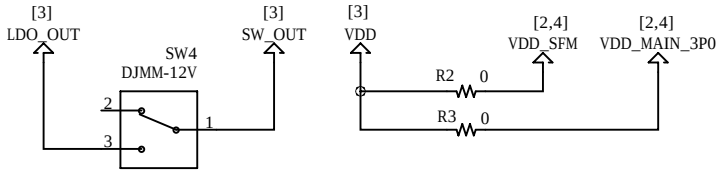
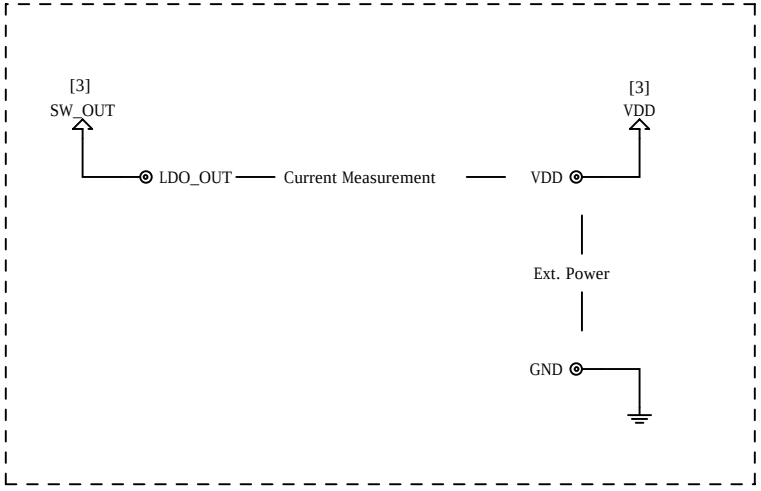
[Power Circuit]

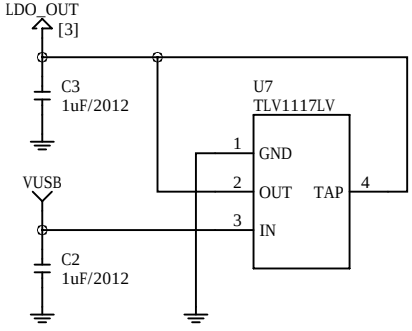


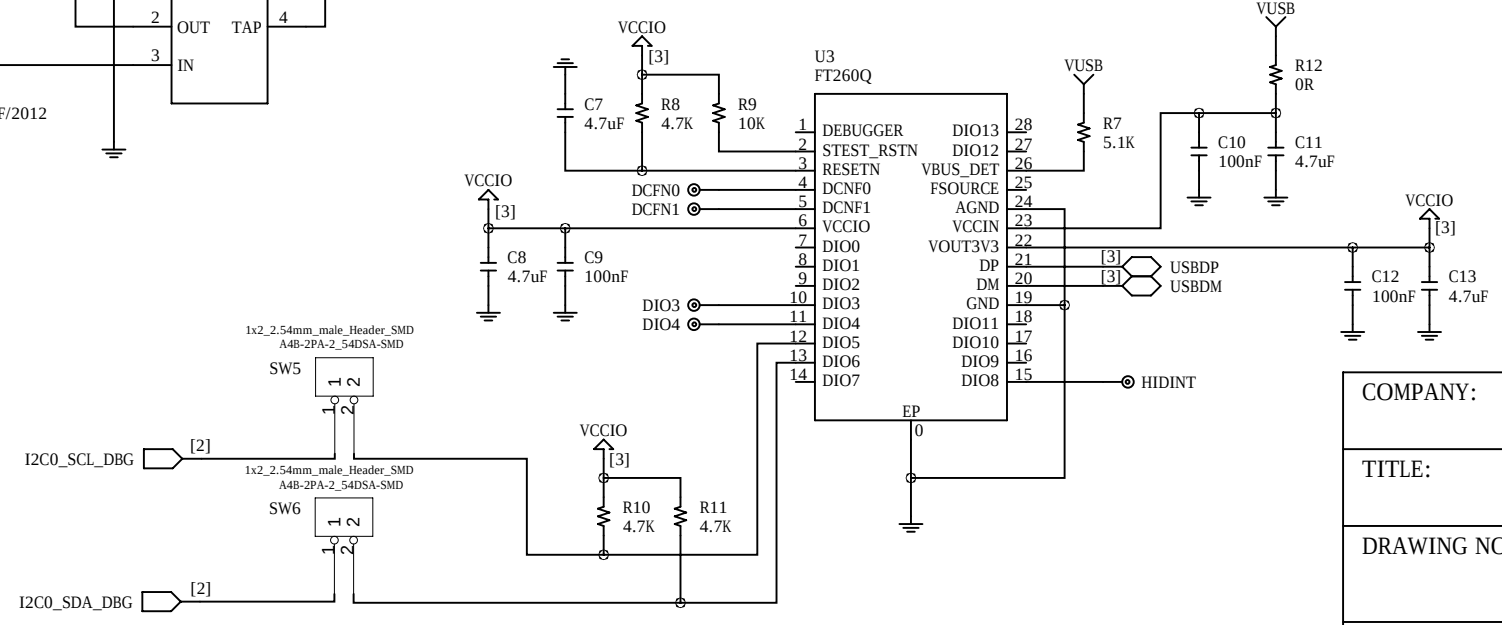


ON / OFF



USB Bridge Power





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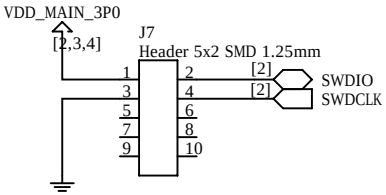
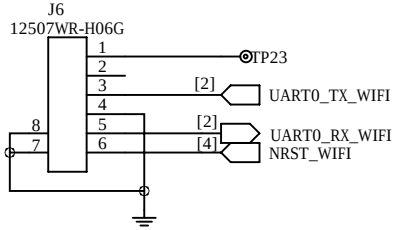
[Download / Debug]

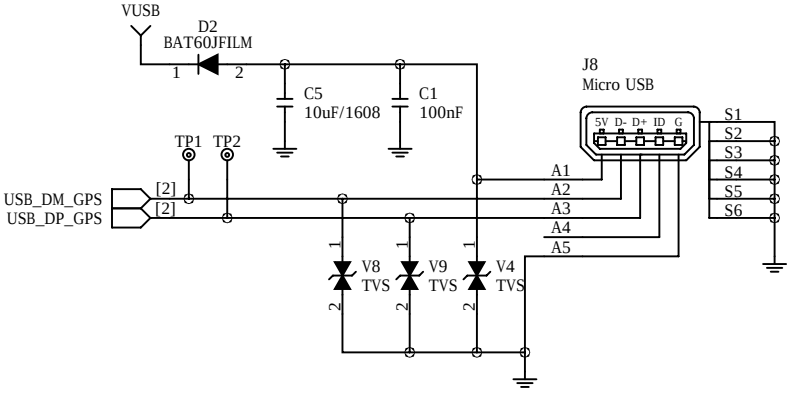
BLE DL



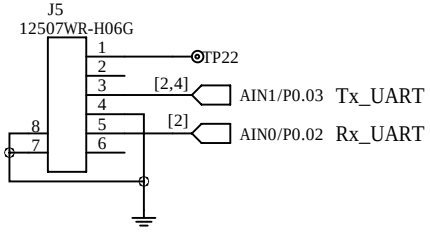
WiFi DL



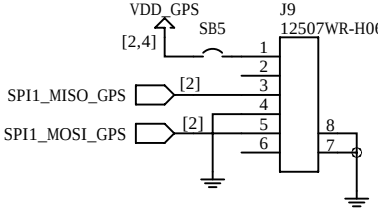
Ublox GPS Test USB



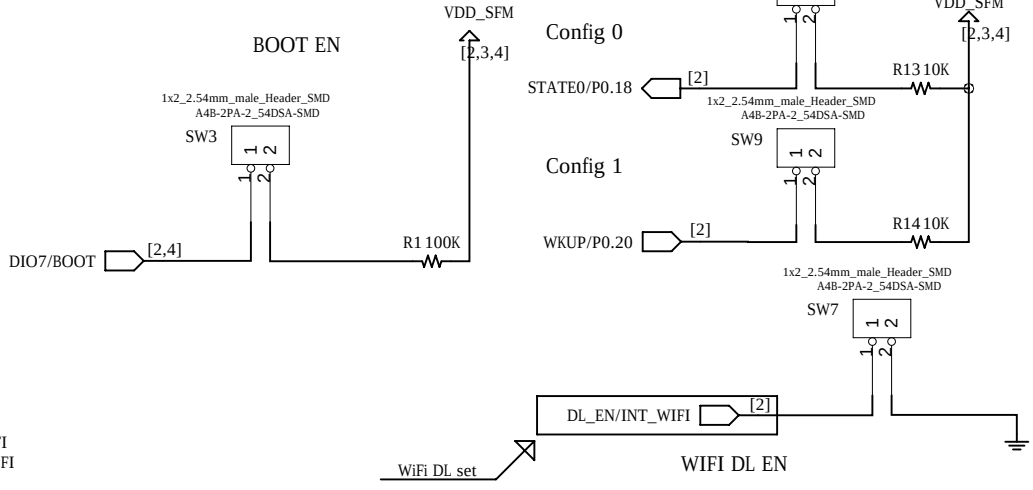
BLE TEST



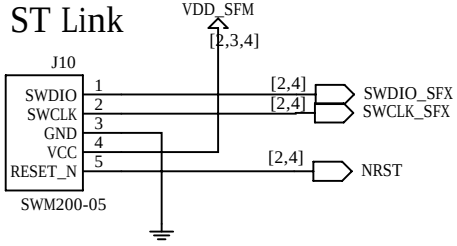
GPS DL



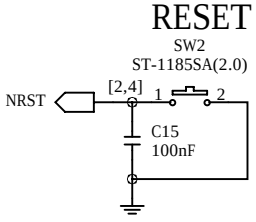
User Port Ext. Set



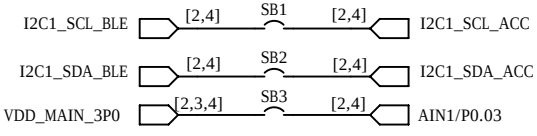
ST Link

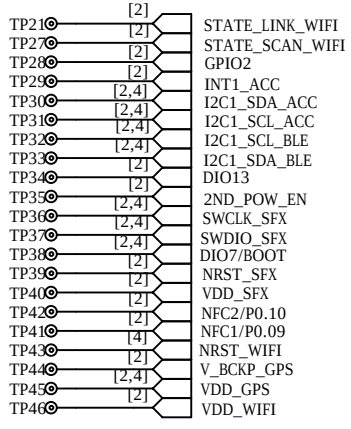


RESET



Deep Sleep Current measure





X1 NTH-3.2 X2 NTH-3.2 X3 NTH-3.2 X4 NTH-3.2

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