**Ihere m2 Manual & Specification**

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| **iHere series JTAG interface board** |
| **WISOL** |
| **January 2, 2018** |

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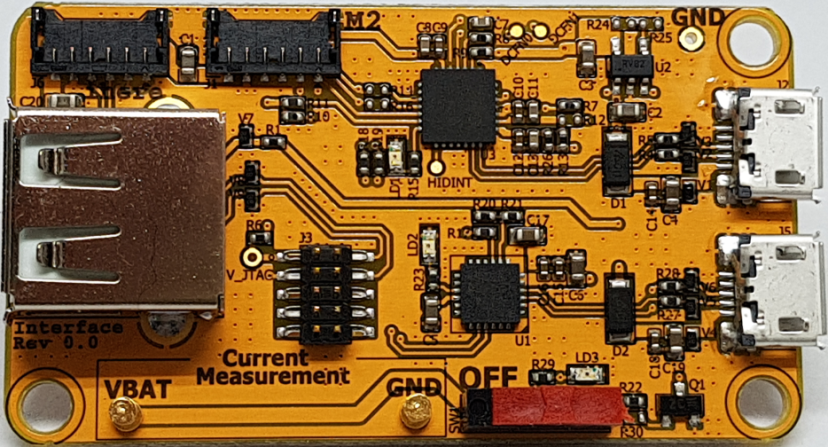
# Revision history

|  |  |  |  |
| --- | --- | --- | --- |
| **Date** | **Version** | **Name** | **Description** |
| Nov.28,2017 | 0.1 | Kidae Kim | First release product description for iHere interface |
| Jan.02,2018 | 0.2 | Kidae Kim | Added Block diagram and schematics |
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# Overview

* Connect iHere to Jtag
* Connect iHere to PC
* Connect M2(=IET20RCx) to Jtag
* UART to USB Bridge   
   - iHere M2 WIFI Firmware Download

# Appearance





# Block Diagram



# Pin description

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| **J1** , 1.2mm pitch Wafer Connector for M2 | | | **J6** , 1.2mm pitch Wafer Connector for iHere | | |
| NO. | Pin Name | Description | NO. | Pin Name | Description |
| 1 | I2C0\_SCL\_DBG | for PC TOOL | 1 | I2C0\_SCL\_DBG | for PC TOOL |
| 2 | I2C0\_SDA\_DBG | 2 | I2C0\_SDA\_DBG |
| 3 | UART0\_TX\_WIFI | for WIFI FIRMWARE DOWNLOAD | 3 | SWDCLK | for JTAG Debugging |
| 4 | UART0\_RX\_WIFI | 4 | SWDIO |
| 5 | Ground | Ground | 5 | Ground | Ground |
| 6 | VBAT | M2 power input 3.3V | 6 | VBAT | iHere power input 3.3V |

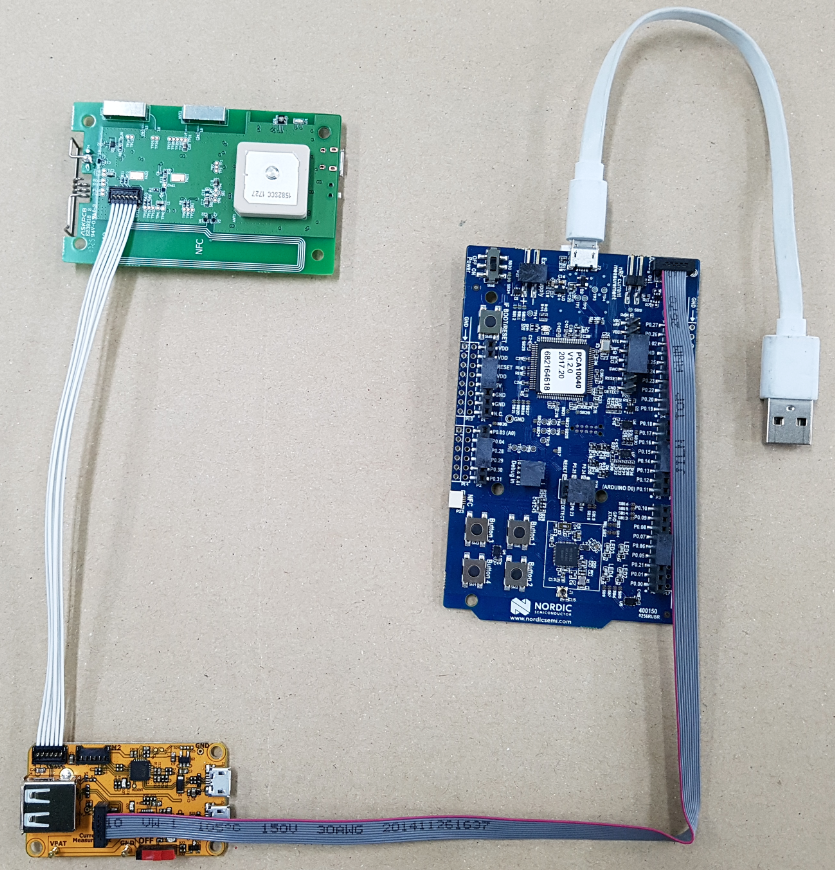
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| --- | --- | --- | --- | --- | --- |
| **J3** , 1.25mm pitch 5x2 Dual row header for JTAG | | | **J4** , USB Type A for M2 | | |
| NO. | Pin Name | Description | NO. | Pin Name | Description |
| 1 | V\_JTAG | JTAG power input 3.3V | 1 | VUSB | M2 power input 5.0V |
| 2 | SWDIO | for JTAG Debugging | 2 | SWDCLK | for JTAG Debugging |
| 3 | Ground | Ground | 3 | SWDIO |
| 4 | SWDCLK | for JTAG Debugging | 4 | Ground | Ground |
| 5~10 | NC |  |  |  |  |

# Schematics





# Connection example



# iHere to Jtag



# iHere to PC (for PC tool)



# IET20RCx to Jtag



# IET20RCx to PC (for WiFi F/W download)





**\*Green pads are ground**

**\*When the download is completed, it is necessary to remove the jump.**