

NAME : SURAJ RAJENDRA JAYBHAYE

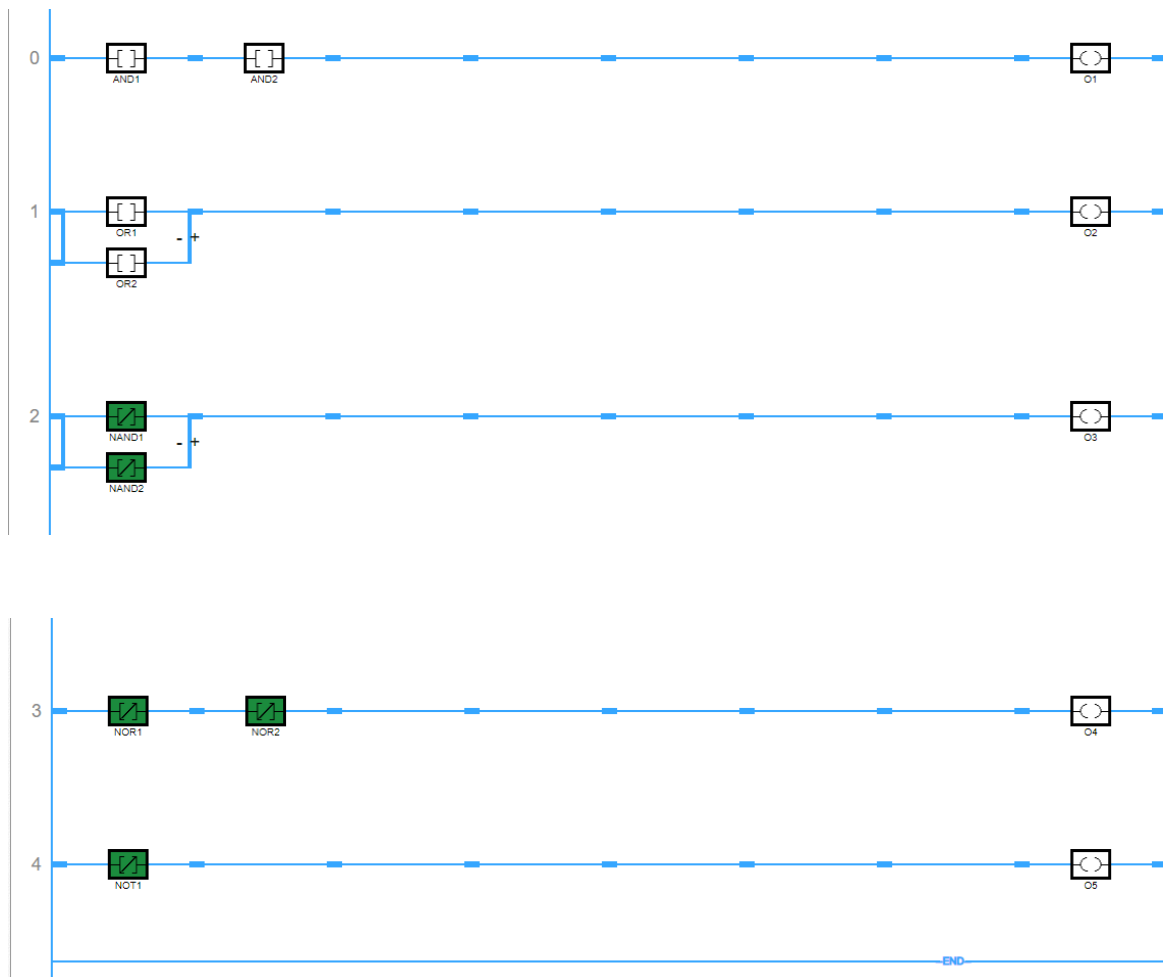
MIS : 112003055

BATCH :S4

1. IMPLEMENTATION OF LOGIC GATES

Logic gates are :

1. AND gate
2. OR gate
3. NAND gate
4. NOR gate
5. NOT gate



2. LADDER PROGRAM FOR DOL STARTER

