USN					

# RV COLLEGE OF ENGINEERING®

(An Autonomous Institution affiliated to VTU)
V Semester B. E. Fast Track Examinations Oct-2020

**Computer Science and Engineering** 

### MICROCONTROLLER AND EMBEDDED SYSTEMS

Time: 03 Hours Maximum Marks: 100

#### Instructions to candidates:

- 1. Answer all questions from Part A. Part A questions should be answered in first three pages of the answer book only.
- 2. Answer FIVE full questions from Part B. In Part B question number 2, 7 and 8 are compulsory. Answer any one full question from 3 and 4 & one full question from 5 and 6

#### PART-A

1	1.1	Indicate the values of carry, Auxiliary carry and parity flags after the	
		execution of following instructions?	
		MOV A, # 9CH	0.1
	1.0	ADD A, # 64H	01
	1.2	After the execution of $MOVC\ A$ , $@A + PC$ in the following code, what will	
		be the value in A:	
		ORG 0000H	
		MOV A, #8	
		MOV B, #7	
		MOV RO, #6	
		MOVCA, @A + PC	0.1
	1.0	TABLE: DB '0123456789'	01
	1.3	Indicate the total time taken to execute the following 8051 ALP	
		program (include all the instructions for the calculation), given crystal	
		frequency –24 MHz:	
		MOV R1, #3	
		CONT: NOP	0.1
		DJNZ R1, CONT	01
	1.4	Indicate the number of steps required to rotate the 2 phase, 4 winding	0.1
	1 -	200 steps per revolution motor by 216 degree.	01
	1.5	Assume single digit common Anode display is connected to P0, with	
		segment 'a' connected b P0.7 and 'dp' is connected to P0.0. Indicate the	0.1
	1.0	hex value to be output to the P0 to get display 'P'?	01
	1.6	Indicate the value to be loaded into the Timer1 register TH1, to get	
		2400 baudrate at the 8051 serial port, given crystal frequency	0.1
	1 /7	11.059 MHz	01
	1.7	Write the value in $R0(32$ bit number) after the execution of the	
		following ARM instructions:	
		MOV R0, #1	
	1.0	MVN R0, R0, LSL #3	02
	1.8	Name the registers of ARM architecture, which are called as LINK	
		Register and stack pointer.	02

1.9	Indicate the value to be loaded into match register <i>MR</i> 0, so that timer counter <i>T0TC</i> reaches <i>MR</i> 0 value after 10 milli seconds. Assume	
	PCLK = $19MHz$ , CCLK = $40MHz$ , $T0TC = 0$	02
1.10	Which port pins are set to 1, after the execution of the following	02
1.10	instructions?	
	$IOOSET = 1 << 2 \mid 1 << 24;$	
	IOOCLR = 1 << 24;	
	IO1SET = 1 << 28;	02
1.11	Indicate the analog voltage at the output of the $DAC - 08$ , connected	
1.11	to P0 of 8051 after the execution of the following program. Assume	
	DAC - 08 is hardware configured to work in the range of $0 - 5$ volts.	
	Unsigned int $a = ox140B$ ;	
	P0 = (Ca >> 8) & 0xFF;	02
1.12	Indicate the value in the Register A after the execution of the following	
	code:	
	ORG 0000H	
	MOV R0, #5	
	MOV R1, #5	
	MOV A, #00H	
	CONT:	
	PUSH 01	
	DJNZ R1, CONT	
	CONT1:	
	POP 01	
	ADD A, R1	
	DJNZ R0, CONT1	02
1.13	Mention the use of the following signals/pins of 8051:	
	EA and ALE	02

## PART-B

2	a	Draw the neat block diagram of 8051 microcontroller and list their	
		specifications.	06
	b	Name the Addressing modes of 8051 microcontroller with example	
		instructions.	06
	c	Describe the meaning of different bits of <i>PSW</i> register of 8051	04
3	а	Define and compare the interrupt driven and polling method of data	
		transfers. Tabulate any four differences.	06
	b	Describe the working of any two conditional and unconditional jump	
		instructions with an example.	06
		OD	
		OR	
4	а	Write an 8051 ALP to perform the linear searching on 'N' 8 bit	
'	u	numbers and indicate the result and match position.	06
	b	Write an 8051 ALP program to compute sum and average of 'N' 8 bit	
	~	numbers. Assume numbers and the value of $N$ is stored in code	
		memory. Store the result in data memory.	06
		monory, store the result in data memory.	~ 0

	С	Compute the answer after the execution of the following instructions, giving the details of arriving at the result.  MOV A, #99H  MOV B, #05H  ADD A, #81H  DA A  ADDC A, #00H  DIV AB	04
5	a b	Design interfacing circuit and develop application program using embedded $\mathcal{C}$ , to interface the stepper motor to 8051 microcontroller and rotate the motor by 180° Design the interfacing circuit and develop the application program for microcontroller based door locking system. User will feed pre-stored 4 digit key code (say 9875), if the keycode matches door must open. Opening and closing of door is controlled by stepper motor. Draw the complete interfacing circuit and embedded $\mathcal{C}$ code to perform the required work. Make suitable assumptions.	06
		OR	
6	a b	Interface 2 × 6 matrix keyboard to 8051 with schematic representation and develop embedded <i>C</i> program to read a key pressed. Send the code pressed to the pc using serial port.  Design the interfacing circuit and develop the application program for microcontroller based function generator (one which generates square and sine waveforms of different frequencies). One toggle key is provided to select the type of the waveform (square / sine). One rotating knob is provided to feed the frequency. Draw the complete interfacing circuit and embedded C code to perform the required work. Make suitable assumptions.	06
7	a b	Describe with neat diagram, the register organization of <i>ARM7</i> core. Mention the use of <i>CPSR</i> register.  Compute the output of the following program, indicating the register contents after execution of each instruction. <i>AREA RESET, CODE ENTRY MOV R0, #01 MVN R0, R0 MOV R1, R0, LSL #31 MOV R2, R0, LSL #2 MOV R3, R2, LSR #2 MOV R4, R2, ASR #1 STOP B STOP END</i>	10
8	a b	Describe the working and programming of Timer unit of <i>ARM</i> microcontroller <i>LPC</i> 2148 with an example program.  Design the interfacing circuit and develop the application program for <i>DC</i> Motor speed control system using <i>ARMLPC</i> 2148 microcontroller using <i>PWM</i> block. Draw the schematic diagram and embedded <i>C</i> code to perform the required work. Make suitable assumptions.	06