



AAA COLLEGE OF ENGINEERING & TECHNOLOGY

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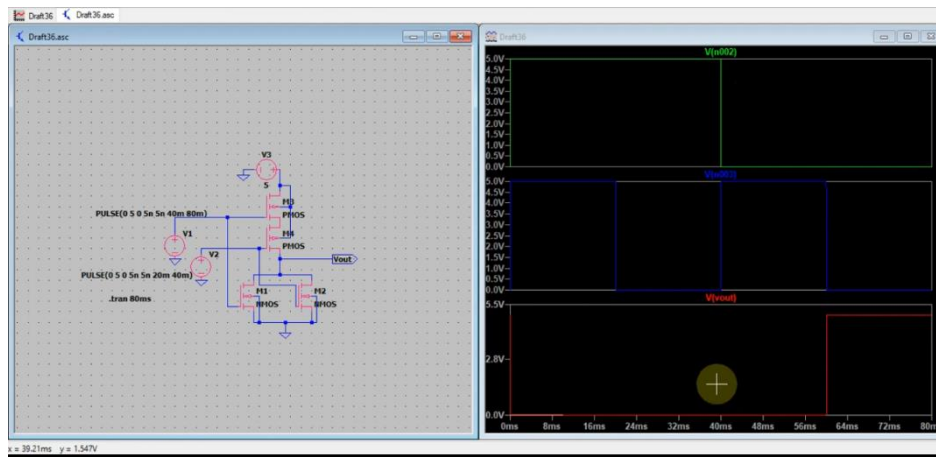
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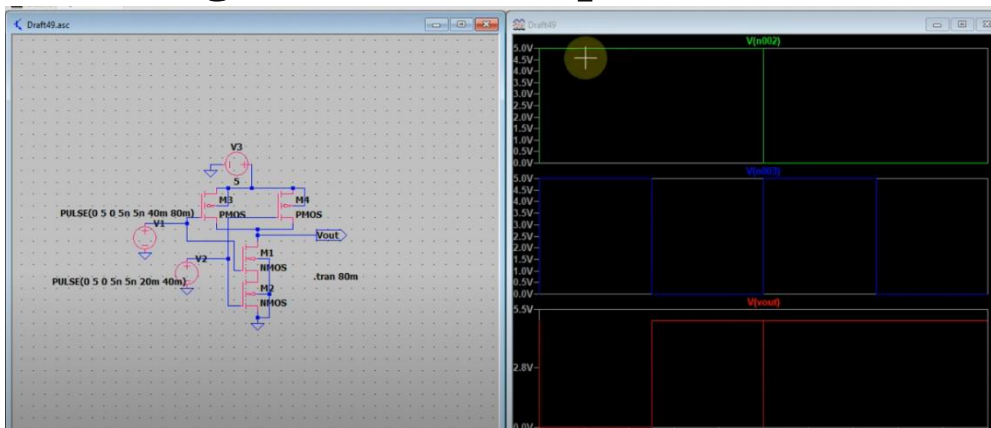
Amathur, Sivakasi - 626 005.

Department of Electronics and Communication
Engineering

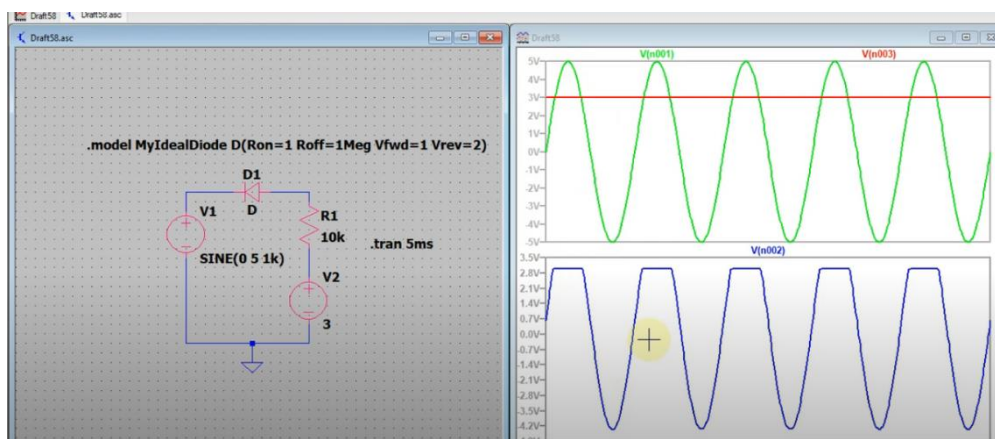
1. NOR gate using CMOS in LTSpice



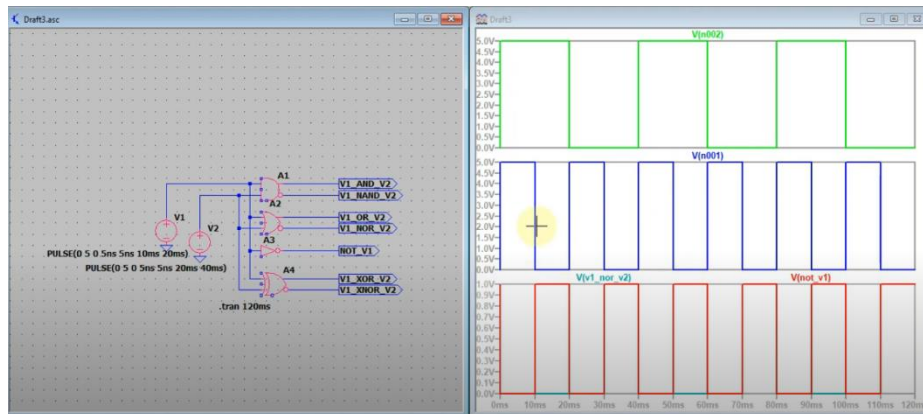
2. NAND using CMOS in LTSpice



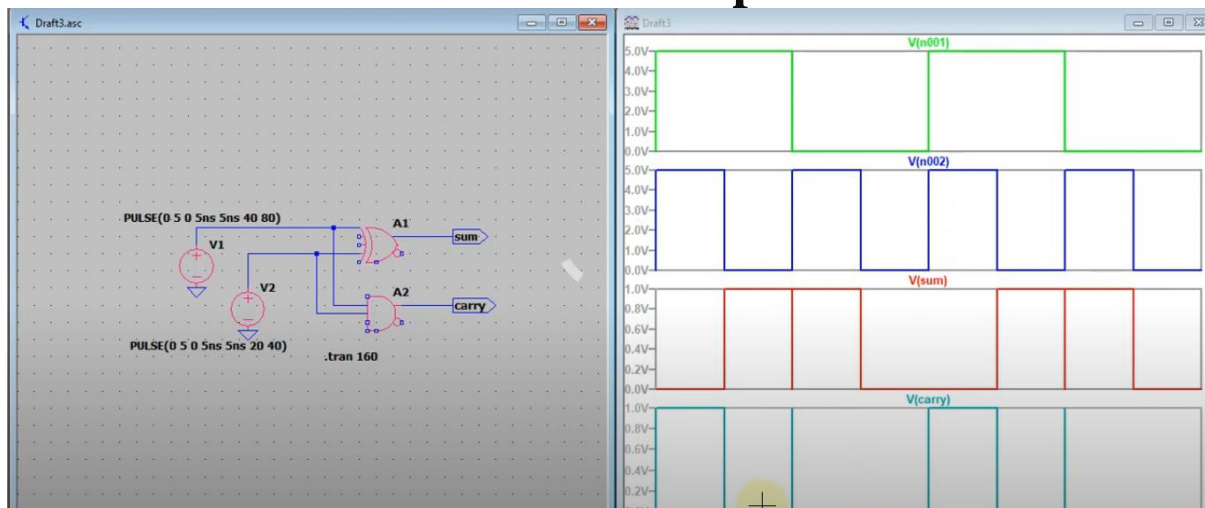
3. Clipper Circuit simulation in LTSpice



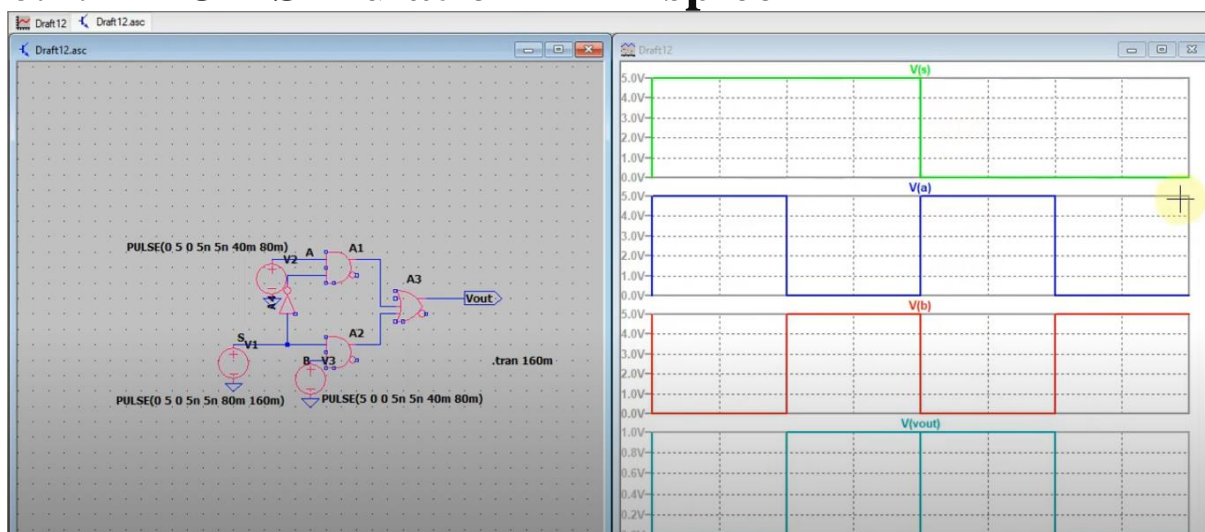
4. Basic 2-Input Logic Gates simulation in LTSpice



5. Half Adder simulation in LTSpice



6.2:1 MUX Simulation in LTSpice



7.LTspice Simulation of D Flip-flop using NAND gates

