

SDIO PRODUCT SPECIFICATION

IEEE 802.11 b/g/n 2.4GHz 1T1R WiFi with Bluetooth v2.1+EDR/Bluetooth 3.0/3.0+HS/4.0

RF-SM02 (Realtek RTL8723AS) Combo Module

Version 1.0



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PRODUCT DESCRIPTION

RF-SM02 is a small size and low profile of WiFi+BT combo module with LGA (Land-Grid Array) footprint, board size is 20mm*10mm with module height 2mm. It can be easily manufactured on SMT process and highly suitable for tablet PC, ultra book, mobile device and consumer product. It provides GSPI/SDIO interface for WiFi to connect with host processor and high speed UART interface for BT. It also has a PCM interface for audio data transmission with direct link to external audio codec via BT controller. The WiFi throughput can go up to 150Mbps in theory by using 1x1 802.11n b/g/n MIMO technology and Bluetooth can support BT2.1+EDR/BT3.0 and BT4.0.

RF-SM02 uses Realtek RTL8723AS, a highly integrated WiFi/BT single chip based on advanced COMS process. RTL8723AS almost integrates whole WiFi/BT function blocks into a chip, such as SDIO/UART, MAC, BB, AFE, RFE, PA, EEPROM and LDO/SWR, except fewer passive components remained on PCB.

PRODUCT FEATURES

- O Operate at ISM frequency bands (2.4GHz)
- O GSPI/SDIO for WiFi and UART for Bluetooth
- O IEEE standards support: IEEE 802.11b, IEEE 802.11g, IEEE 802.11n, IEEE 802.11d, IEEE 802.11e, IEEE 802.11h, IEEE 802.11i
- O Fully Qualified Bluetooth 2.1 + EDR specification including both 2Mbps and 3Mbps modulation mode
- O Fully qualified Bluetooth 3.0
- O Fully qualified Bluetooth 4.0 Dual mode
- O Full –speed Bluetooth operation with Piconet and Scatternet support.
- O Enterprise level security which can apply WPA/WPA2 certification for WiFi.
- O WiFi 1 transmitter and 1 receiver allow data rates supporting up to 150 Mbps downstream and 150 Mbps upstream PHY rates
- O Support sophisticated WiFi/BT coexistence mechanism to enhance collocation performance
- O Support antenna diversity for WiFi and BT antenna selection
- O Support Bluetooth adaptive power management mechanism
- O Full-featured software utility for easy configuration and management
- O RoHS compliance
- O Low Halogen compliance



PRODUCT SPECIFICATIONS

Main chipset

WiFi/BT Single Chip: Realtek RTL8723AS-CG

Functional Specifications

| Functional Specifications | | | |
|---------------------------|--|--|--|
| | WiFi: | | |
| Standards | IEEE 802.11b, IEEE 802.11g, Draft IEEE 802.11n, IEEE 802.11d, IEEE 802.11e, IEEE 802.11h, IEEE 802.11i | | |
| Standards | BT: | | |
| | V2.1+EDR/BT v3.0/BT v3.0+HS/BT v4.0 | | |
| Bus Interface | WiFi: GSPI/SDIO BT: UART | | |
| Form Factor | L*W*H = 20mm*10mm*2mm | | |
| | 802.11b: | | |
| | 11, 5.5, 2, 1 Mbps | | |
| | 802.11g: 54, 48, 36, 24, 18, 12, 9, 6 Mbps | | |
| | 002.11g. 34, 40, 30, 24, 10, 12, 3, 0 Mbps | | |
| | 802.11n: | | |
| Data Rate | MCS 0 to 7 for HT20MHz | | |
| | MCS 0 to 7 for HT40MHz | | |
| | BT: | | |
| | 1 Mbps for Basic Rate 2,3 Mbps for Enhanced Data Rate 6,9,12,18,24,36,48,54 Mbps for High Speed | | |
| | WiFi: | | |
| Media Access Control | CSMA/CA with ACK BT: | | |
| | AFH, Time Division | | |
| | 802.11b: | | |
| | CCK, DQPSK, DBPSK | | |
| | 802.11g: 64 QAM, 16 QAM, QPSK, BPSK 802.11n: | | |
| | chniques | | |
| Modulation Techniques | | | |
| | 64 QAM, 16 QAM, QPSK, BPSK | | |
| | BT: | | |
| | 8DPSK, π/4 DQPSK, GFSK | | |
| Network Architecture | WiFi: | | |



| | Ad-hoc mode (Peer-to-Peer) | | |
|---|--|--|--|
| | Infrastructure mode | | |
| | BT: | | |
| | Pico Net | | |
| | Scatter Net | | |
| | WiFi 2.4GHz: | | |
| | 11: (Ch. 1-11) – United States | | |
| Operating Channel | 13: (Ch. 1-13) – Europe 14: (Ch. 1-14) – Japan | | |
| | BT 2.4GHz: | | |
| | Ch. 0 ~78 | | |
| Frequency Range | 2.400GHz ~ 2.4835 GHz | | |
| | 802.11b@11Mbps 16dBm | 802.11g@6Mbps 16dBm | 802.11n 16dBm (MCS 0_HT20) |
| Transmit Output Power – 1x1 (Tolerance: ±1.5dBm) | | 802.11g @54Mbps 14dBm | 13dBm (MCS 7_HT20) 13dBm (MCS 0_HT40) 13dBm (MCS 7_HT40) |
| | BT: Max +10dBm | | |
| | 802.11b@11Mbps -84dBm | 802.11g@54Mbps -73dBm | 802.11n -69dBm (MCS 7_HT20) |
| Receiver Sensitivity | -66dBm (MCS 7_HT40) BT: | | -66dBm (MCS 7_HT40) |
| | | | |
| | -89dBm@1Mbps, -90dBm@2Mbps, -83dBm@3Mbps | | |
| | WiFi: | | |
| WPA, WPA-PSK, WF | | WPA2, WPA2-PSK, WEP 64bit & 128bit, IEEE | |
| Security | 802.11x, IEEE 802.11i BT: Simple Paring | | |
| | | | |
| Operating Voltage | 3.3 V ±9% I/O supply voltage | | |
| | | | |



WiFi :

TX Mode: (Conituous mode) 260mA (MCS7/BW40/13dBm)

RX Mode: (Conituous mode) 190mA (MCS7/BW40/-60dBm)

Associated Idle:

4mA

Unassociated Idle:

2.9mA

Power Consumption (3.3V) (Typical)

RF disable Mode:

3mA

BT:

Inquiry & Page Scan:

1.7mA

ACL no traffic:

15mA

SCO HV3:

20mA

Parked 1.28s beacon:

1.12mA

Reset:

0.05mA

Mechanical

| | Length | Width | Height |
|-----------------|--------------------|--------------------|--------------------|
| Dimensions (mm) | 22.25 | 11.745 | 1.7 |
| , , | (Tolerance:±0.2mm) | (Tolerance:±0.2mm) | (Tolerance:±0.2mm) |

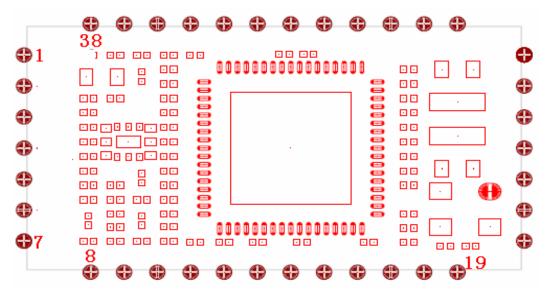


Fig.1 Top Layer (Top View)

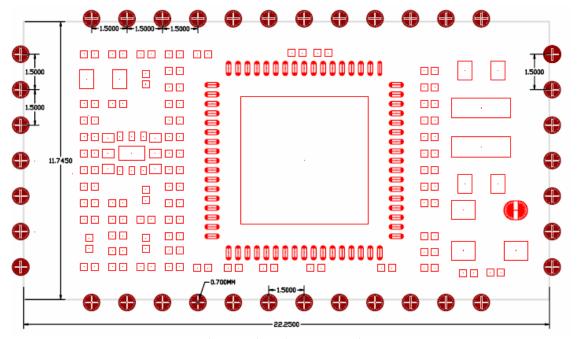


Fig.2 Size chart (Top View)



Block Diagram

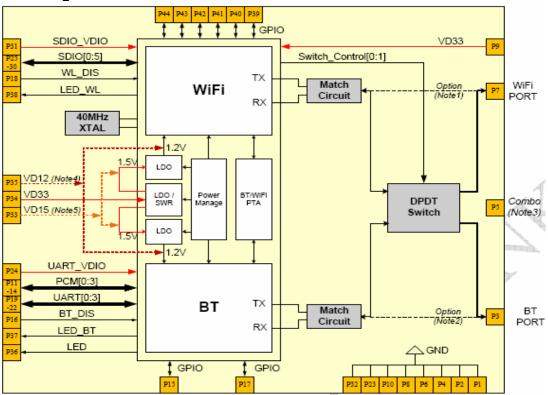


Fig.3 Block Diagram with Dual RF Port

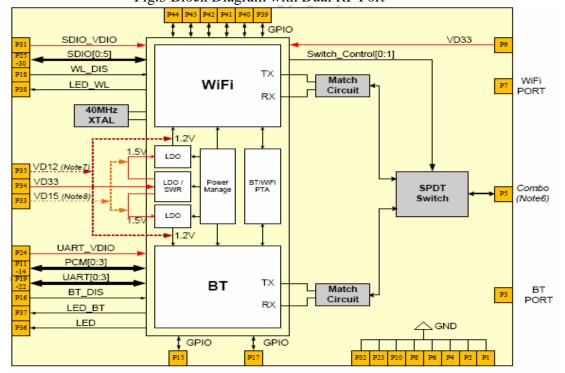


Fig.4 Block Diagram with Single RF Port



(2) This module also reserves flexibility to support separate WiFi/BT RF fixed path without DPDT.

Note3.6:

(1) Option for single antenna. WiFi/BT shares the single RF port and a SPDT required for switching between BT and WiFi.

Note4,5,6,7:

- (1) Default this module only require 3.3V single power source and core voltage generated by internal voltage regulator.
- (2) This module reserves flexibility for external power source if system can provide VD12/VD15 for this module

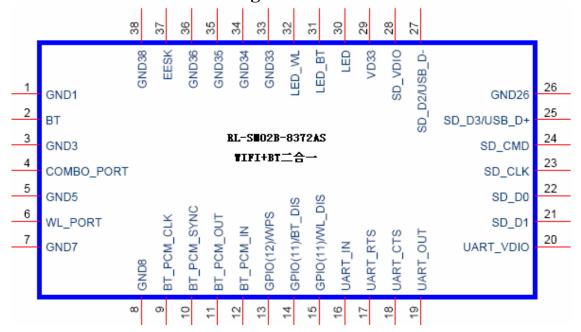
MODULE PIN ASSIGNMENT

| | Function | Pin | Function | Pin |
|------|--|-----|---|-----|
| | UART VDIO | 20 | GND | 1 |
| s | VDD for UART Pin, the power supply is | | | |
| | same as the signal level of | | | |
| | UART bus (3.3V ~ 1.8V) | 0.4 | DT DE | |
| | SD_D1 | 21 | BT_RF | 2 |
| | SD_D0 | 22 | GND | 3 |
| | SD_CLK | 23 | COMBO_RF | 4 |
| - | SD_CMD | 24 | GND | 5 |
| | SD_D3/USB_D+ | 25 | WL_RF | 6 |
| | GND | 26 | GND | 7 |
| | SD_D2/USB_D- | 27 | GND | 8 |
| | SD_VDIO | 28 | BT_PCM_CLK | 9 |
| same | VDD for SDIO Pin, the power supply is same | | General Purpose Input/Output Pin | |
| | as the signal level of | | | |
| | SDIO bus (3.3V ~ 1.8V) | | DT DOM OVALO | 10 |
| | VD33 | 29 | BT_PCM_SYNC | 10 |
| | LED | 30 | BT_PCM_OUT | 11 |
| | LED Pins (Active Low) | | Trap function: weak pull low to enable | |
| | | | integrated switching regulator; weak pull high to enable integrated linear | |
| | | | regulator. | |
| | | | General Purpose Input/Output Pin | |
| | LED_BT | 31 | BT_PCM_IN | 12 |
| | | | Trap function: weak pull low to enable | |
| | | | RTL8723 to enter normal operation | |
| | | | mode. | |
| | LED_WL | 32 | General Purpose Input/Output Pin GPIO(12)/WPS | 13 |
| | LLD_VVL | 32 | This pin is for WIFI function to wakeup | 13 |
| | | | host when remote wake function is | |
| | | | enabled. The Polarity can be defined | |
| | | | by customer. | |
| | GND | 33 | GPIO(11)/BT_DIS | 14 |
| | | | - I | |
| | | | | |
| | | | | |
| | | | | |
| | | | Radio-off function with host interface | |
| | | | remaining connected. | |
| | GND | 34 | GPIO(11)/WL_DIS | 15 |
| | | | This Pin Can Externally Shutdown the | |
| | | | ` * | |
| | | | | |
| | | | | |
| | | | | |
| | | | | |
| | | | Tomaning connected. | |
| | | | This Pin Can Externally Shutdown the RTL8723AS (no requirement for Extra Power Switch) when WL_DISn is pulled low This pin can also support the BT Radio-off function with host interface remaining connected. GPIO(11)/WL_DIS | |

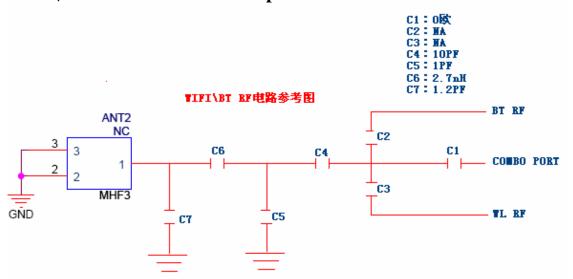


| 16 | UART_IN | 35 | GND |
|----|--------------------------|----|------|
| | High-Speed UART Data In | | |
| 17 | UART_RTS | 36 | GND |
| | High-Speed UART RTS | | |
| 18 | UART_CTS | 37 | EESK |
| | High-Speed UART CTS | | |
| 19 | UART_OUT | 38 | GND |
| | High-Speed UART Data Out | | |

Module PIN feet definition figure



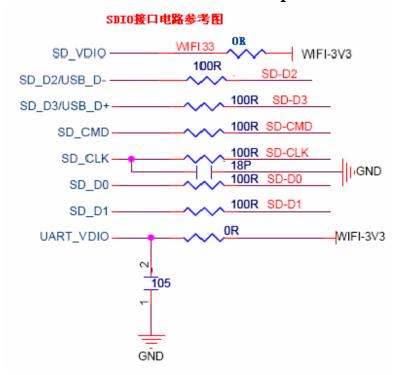
WIFI\BT RF Circuit reference pictures



注:以上RF 走线要做50欧的阻抗,走线不能走90度,走线不能长于15MM。



SDIO interface Circuit reference pictures



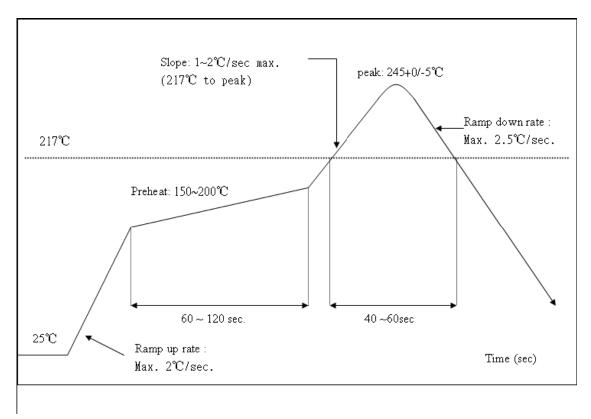
BT interface Circuit reference pictures





Recommended Reflow Profile

Referred to IPC/JEDEC standard. Peak Temperature : <250°C Number of Times : ≤2 times



ID SETTING INFORMATION



| Reg Domain | World Wide 13 Channels 1-11 with active scan Channels 12,13 with passive scan Channel 14 with no scan |
|---------------------|---|
| Reg Domain Code | 0x0A |
| Vendor ID | WiFi: 0x10EC BT: 0x0BDA |
| Device ID | WiFi: 0x8723 BT: 0x8723 (PID) |
| Subsystem Device ID | 0x8723 (Realtek demoboard) |
| Subsystem Vendor ID | 0x10EC |

ENVIRONMENTAL

Operating

Operating Temperature: 0°C to +70 °C

Relative Humidity: 5-90% (non-condensing)

Storage

Temperature: -40°C to +80°C (non-operating)
Relevant Humidity: 5-95% (non-condensing)

MTBF caculation

Over 150,000hours