

CIS 240: MICROCOMPUTER ARCHITECTURE & PROGRAMMING

HW2: 2's complement and building with gates

1. 2's complement

Fill in this table. Do row by row like we did in class. All answers need to be

Row #	Base-10	Positive binary	Hexadecimal	2's complement
1	-12 ₁₀	Can't do	0xF4	111101002
2	2110	000101012	0x15	000101012
3	132 1291	100001002	0x84	100001002
4	-15	Carit 10	OXFI	111100012
(5)	9	0000 (0012	0009	000010012

12844

2. If you need more practice, find an on-line calculator for any of these representations and test yourself. Be careful with the 2's complement calculators. Some of them flip any number you put into the calculator. Look for one that's decimal to 2's complement like: https://www.exploringbinary.com/twos-complement-converter/

3. SOP (sum of product) from truth table. Find the logic for the digital circuit described by the following truth tables.

a.

			AL AD	$L \Delta \cdot R$
Inp	uts	Output	Out = A · B	TRU
Α	В	Out		
0	0	0	TA=0	2=1
0	1	1 🦶	- \ B=(0, 1
1	0	0		
1	1	1		

b.

	Inputs		Output	
Α	В	С	Out	
0	0	0	0	
0	0	1	0	
0	1	0	0	
0	1	1	- 1	
1	0	0	0	
1	0	1	0	
1	1	0	- 1 6	
1	1	1	- 1 (

out=A.B.C+A.B.C

c. Make a truth table that converts 3-bit thermometer code to binary. Thermometer code counts up:

$$000_2 \rightarrow 001_2 \rightarrow 011_2 \rightarrow 111_2$$

In thermometer code, 990_2-0_{10} , $001_2=1_{10}$, $011_2=2_{10}$, and $111_2=3_{10}$. HINT: In the truth table, any row not used can have an output of X (for don't care).



d. Find the logic for your thermometer code truth table.

$$03 = \overrightarrow{A} \cdot \overrightarrow{B} \cdot \overrightarrow{C}$$

$$03 = \overrightarrow{A} \cdot \overrightarrow{B} \cdot \overrightarrow{C} + \overrightarrow{A} \cdot \overrightarrow{B} \cdot \overrightarrow{C} + \overrightarrow{A} \cdot \overrightarrow{B} \cdot \overrightarrow{C}$$