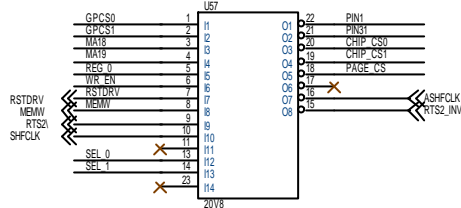


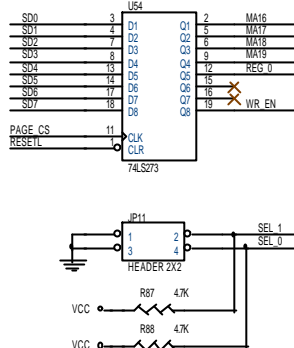
## GPCS

SOC5\_3.PDS



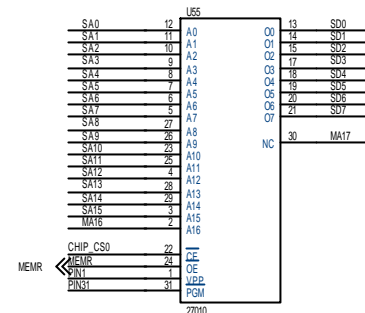
SEL1	0	FUNCTION	GPCS0	GPCS1
0	0	DiskOnChip	CHIP1	CHIP2
0	1	ROMDISK	CHIP1/2	PAGE_CS
1	0	DISABLE	X	X
1	1	FLASHDISK	CHIP1/2	PAGE_CS

PAGE\_CS

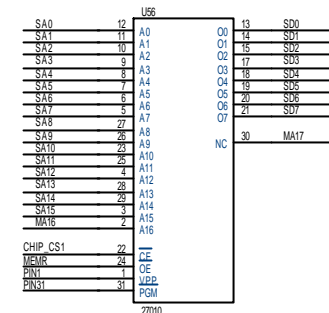


## DiskOnChip/

## ROM/FLASHDISK

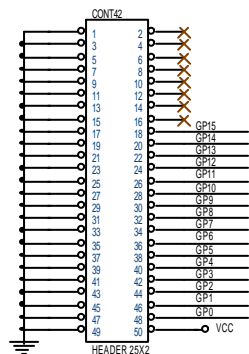
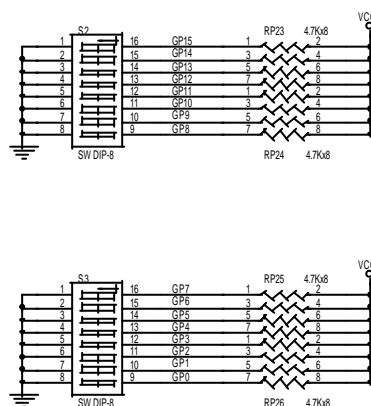
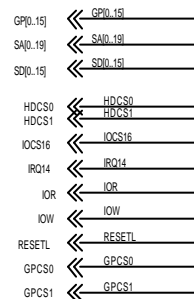


CHIP1



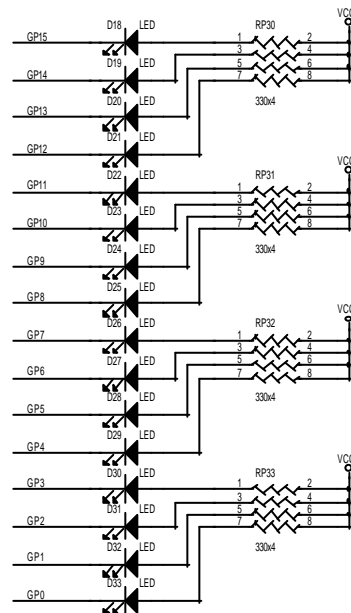
CHIP2

## GPIO INPUT

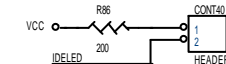
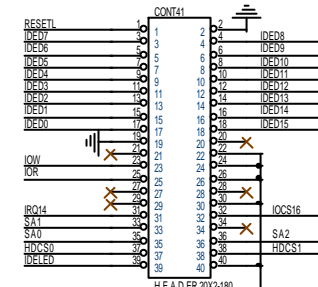
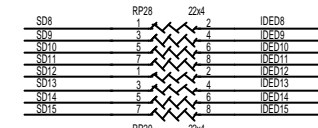
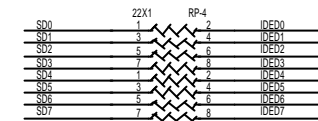


GPIO CONN.

## GPIO OUTPUT



## IDE



M6117D ENHANCE SIGNALS DEMO

DM&amp;P JAN YIN CHAN ELECTRONICS CO.LTD.

DiskOnChip/ROM/FLASHDISK

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