



Set M6117D ISA Clock

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User can set various ISA clock with 1EH register in M6117D. Those codes will show you more:

```
//INDEX 1EH default 00H
//
//Bit Description
//7~3 reserved
//
//2~0 ATCLK1 Definition
//High Freq. ATCLK[2:0] Normal Freq.
//14.318/2    0 0 0          14.318/2
//PCLK2/2     0 0 1          PCLK2/3
//PCLK2/3     0 1 0          PCLK2/4
//PCLK2/4     0 1 1          PCLK2/5
//PCLK2/5     1 0 0          PCLK2/6
//PCLK2/6     1 0 1          PCLK2/8
//PCLK2/8     1 1 0          PCLK2/10
//PCLK2/10    1 1 1          PCLK2/12

#include <conio.h>
void m6117d_write(unsigned char reg, unsigned char data)
{
    outp(0x22, reg);
    outp(0x23, data);
}
unsigned char m6117d_read(unsigned char reg)
{
    outp(0x22, reg);
    return inp(0x23);
}
main()
{
    // unlock m6117d configure register first
    m6117d_write(0x13, 0xc5);

    // set speed to PCLK2/5 at Normal Freq.
    m6117d_write(0x1e, (m6117d_read(0x1e)&0xF8) | 0x03);
```



```
// lock m6117d configure register  
m6117d_write(0x13, 0x00);  
}
```

Technical Support

For more technical support, please visit <http://www.dmp.com.tw/tech> or mail to tech@dmp.com.tw.