



Note to Read CMOS Time

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We should check update flag when we read time from RTC. The bit 7 of RTC Status Register A of RTC CMOS RAM is update in progress flag. When set an update cycle is in progress and the clock/calendar cannot be accessed. When clear, at least 244 microseconds are available to access clock/calendar bytes. We provide assembler code for your reference:

```
again:
    mov  al,80h
    out  70h,al
    jmp  $+2
    in   al,71h
    test al,80h
    jnz  again
    mov  al,[index]
    out  70h,al
```

Technical Support

For more technical support, please visit <http://www.dmp.com.tw/tech> or mail to tech@dmp.com.tw.