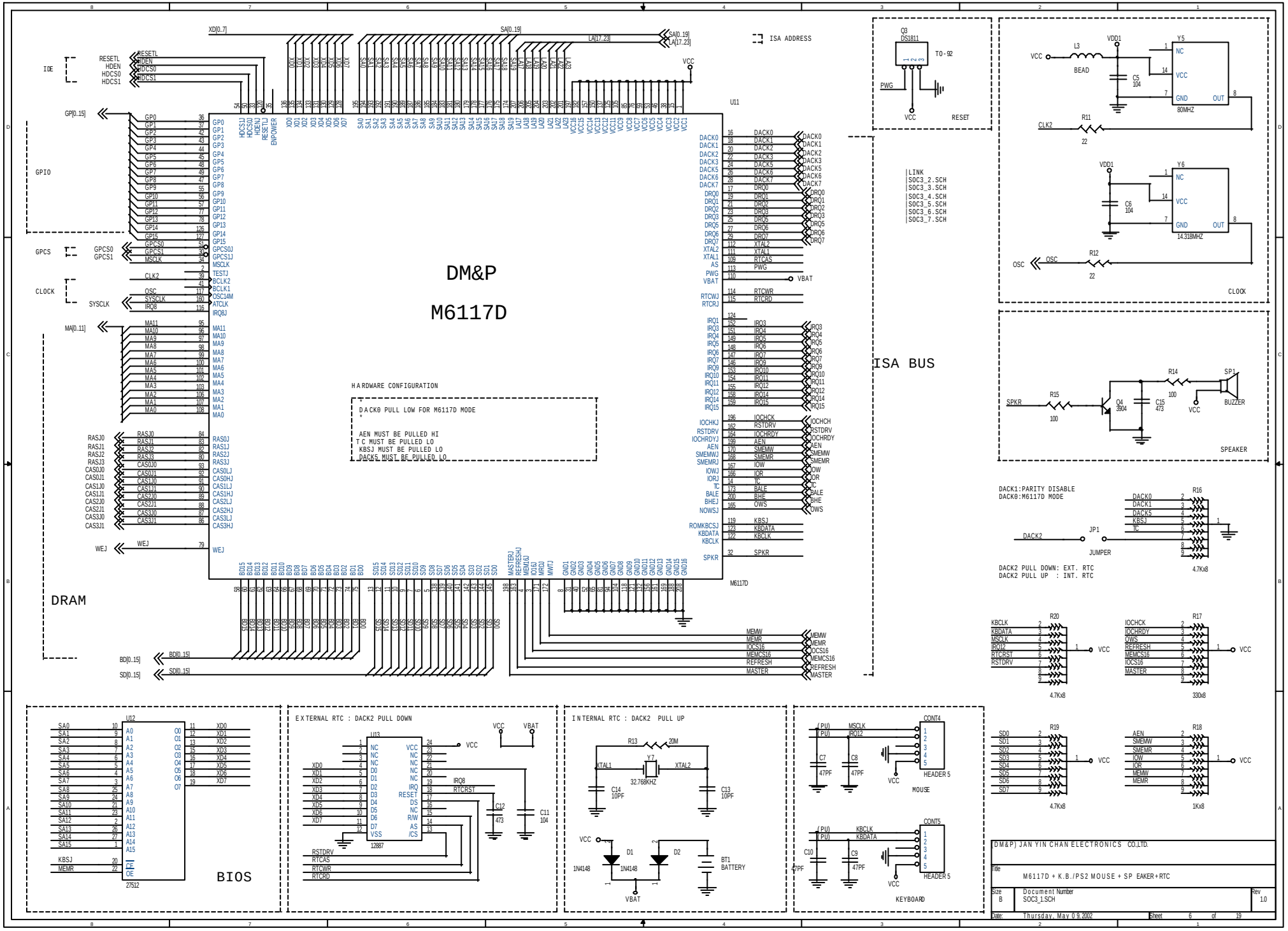
DM&P
M6117D
U11
M6117D
IDE
RESETL
HDEN
HDCS0
HDCS1
GPIO
GP[0..15]
GPCS
GPCS0
GPCS1
CLOCK
CLK2
OSC
SYSCLK
MA[0..11]
DRAM
RASJ0
RASJ1
RASJ2
RASJ3
CAS0J0
CAS0J1
CAS1J0
CAS1J1
CAS2J0
CAS2J1
CAS3J0
CAS3J1
WEJ
BD[0..15]
SD[0..15]
XD[0..7]
SA[0..19]
LA[17..23]
VCC
VBAT
ISA ADDRESS
HARDWARE CONFIGURATION
DACK0 PULL LOW FOR M6117D MODE
AEN MUST BE PULLED HI
TC MUST BE PULLED LO
KBSJ MUST BE PULLED LO
DACK5 MUST BE PULLED LO
MEMW
MEMR
IOCS16
MEMCS16
REFRESH
MASTER
Q3
DS1811
TO-92
PWG
RESET
ISA BUS
LINK
SOC3_2.SCH
SOC3_3.SCH
SOC3_4.SCH
SOC3_5.SCH
SOC3_6.SCH
SOC3_7.SCH
L3
BEAD
C5
104
Y5
80MHZ
R11
22
C6
104
Y6
14.318MHZ
R12
22
CLOCK
R14
100
SP1
BUZZER
R15
100
Q4
3904
C15
473
SPEAKER
DACK1:PARITY DISABLE
DACK0:M6117D MODE
R16
JP1
JUMPER
4.7Kx8
DACK2 PULL DOWN: EXT. RTC
DACK2 PULL UP : INT. RTC
R20
R17
330x8
R19
R18
1Kx8
U12
27512
BIOS
U13
12887
EXTERNAL RTC : DACK2 PULL DOWN
INTERNAL RTC : DACK2 PULL UP
R13
20M
Y7
32.768KHZ
C14
10PF
C13
10PF
D1
D2
1N4148
BT1
BATTERY
CONT4
HEADER 5
MOUSE
CONT5
KEYBOARD
C7
C8
C9
C10
47PF
C11
104
C12
473
(DM&P) JAN YIN CHAN ELECTRONICS CO.,LTD.
M6117D + K.B./PS2 MOUSE + SP EAKER+RTC
Size
B
Document Number
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Rev
1.0
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Thursday, May 09, 2002
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