Access CMOS RAM on M6117D

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The free space of index address starts from 6B Hex to 7F Hex. These 21 Bytes are check-free by Checksum. Users can flexibly use it for password or any parameter of applications. To access these free space that customer can use assembly or C language to read/write port address Hex 70 and Hex 71 for data.

Below is technical table list of CMOS RAM for your reference:

Hex 70 for Index	Hex 71 for Data
Total Space: Hex 00 ~ 7F (128 Bytes)	1 Byte for each Read / Write
Free Space: Hex 6B ~ 7F (21 Bytes)	1 Byte for each Read / Write

Warning: Index Hex 00 to Hex 6A are used by system. Any data change in this area may cause the system work abnormal or crash.

Assembler Example Code

```
.286
.model small
.code            ;
;----- Demo how to read data from CMOS RAM
mov al,6Bh   ;
out 70h,al   ; Send index 6B Hex to port 70 Hex for latch
in 71h,al    ; Read 1 Byte data of index 6B Hex from port 71 Hex
;----- Demo how to write data to CMOS RAM
mov al,6Ch   ;
out 70h,al   ; Send index 6C Hex to port 70 Hex for latch
mov al,0ffh  ;
out 71h,al   ; Send data 0FF Hex to index 6C Hex from port 71 Hex
;....... continue program ……
```

C Example Code

```
#include <dos.h>
void write_CMOS(unsigned char index, unsigned char data)
{
  outportb(0x70, index);
  outportb(0x71, data);
}
void main()
```

```
{
  /* write data 0FF Hex to index 6C Hex of CMOS RAM*/
  write_CMOS(0x6C, 0xff);
  /* read data from index 6B Hex of CMOS RAM*/
  outportb(0x70, 0x6B);
  inportb(0x71);
}
```

Technical Support

For more technical support, please visit http://www.dmp.com.tw/tech or mail to tech@dmp.com.tw.