

Pinout diagram for the 32-bit HD44780 LCD module. The diagram shows two rows of pins. The top row (pins 1-16) includes RSTDRV, RQ9, DRQ2, OWS, SMEMW, SMEMR, IOW, IOR, DACK3, DRQ3, DACK1, DRQ1, REFRESH, STSCLK, RSCLK, RQ7, RQ6, RQ5, RQ4, RQ3, DACK2, IC, BALE, OSC, and 40MHz. The bottom row (pins 17-32) includes RSTDRV, RQ9, DRQ2, OWS, SMEMW, SMEMR, IOW, IOR, DACK3, DRQ3, DACK1, DRQ1, REFRESH, STSCLK, RSCLK, RQ7, RQ6, RQ5, RQ4, RQ3, DACK2, IC, BALE, OSC, and 40MHz. The diagram also shows the connection of the 32-bit data bus (D0-D31) to the LCD's data pins (D0-D31). The 32-bit data bus is connected to the LCD's data pins (D0-D31) via a 32-bit header. The 32-bit data bus is connected to the LCD's data pins (D0-D31) via a 32-bit header. The 32-bit data bus is connected to the LCD's data pins (D0-D31) via a 32-bit header.

[illegible]

~~SINGLE-CHIP-PC-CONNECTOR.~~

