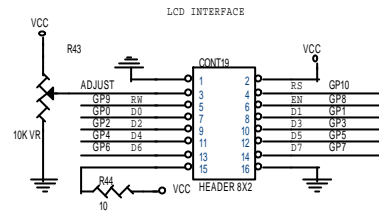
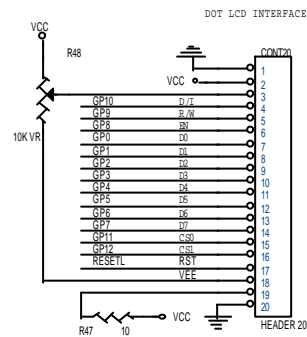


GP[0..15] <<< GP[0..15]
RESETL <<< RESETL



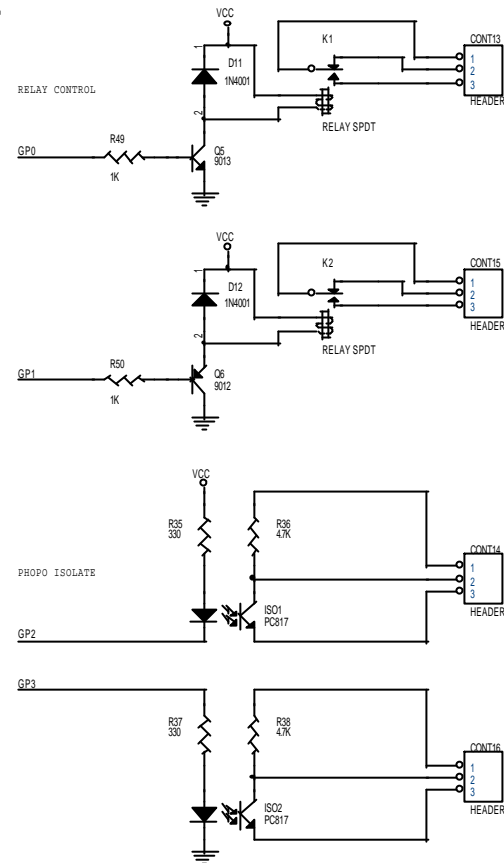
LCD 16x2 or 20x2



LCD 128x64 DOTs

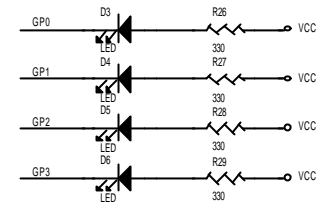
LCD

OUTPUT

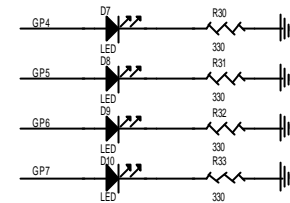


LED DISPLAY

LED



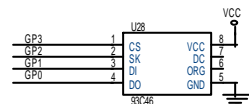
ACTIVE LOW



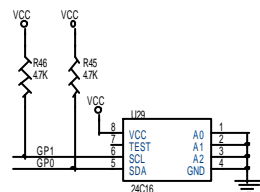
ACTIVE HIGH

EEPROM

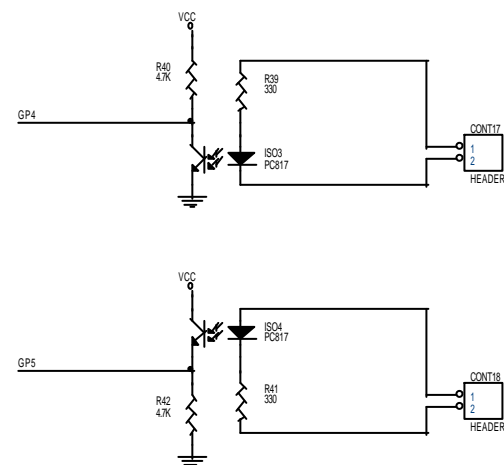
INPUT



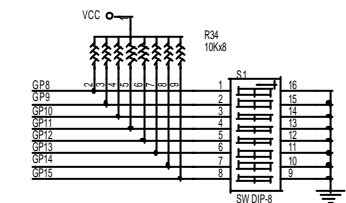
4 WIRE EEPROM



2 WIRE EEPROM



DIP SWITCH INPUT



DM&P/JAN YIN CHAN ELECTRONICS CO.,LTD.

GENERAL PURPOSE I/O

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Date: Thursday, May 03, 2002

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