

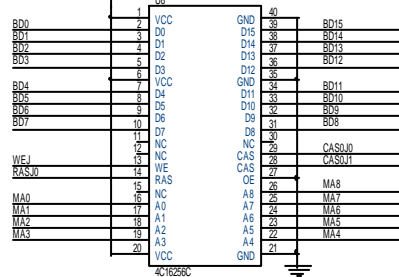
DM&P M6117D

HARDWARE CONFIGURATION

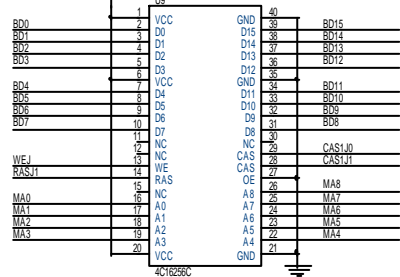
DAK0 PULL LOW FOR M6117D MODE
AEN MUST BE PULLED HI
TC MUST BE PULLED LO
FBSJ MUST BE PULLED LO
PATEZ MUST BE PULLED LO

DRAM

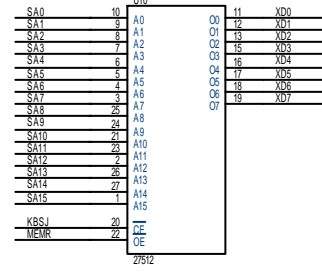
EDO/FPM



256KBx16

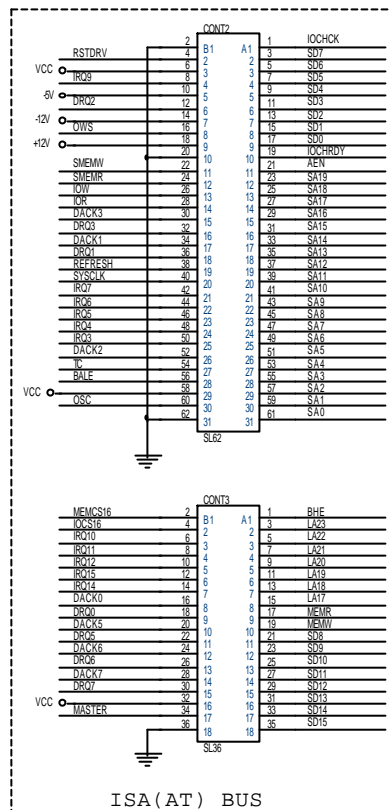


256KBx16 (OPTION)

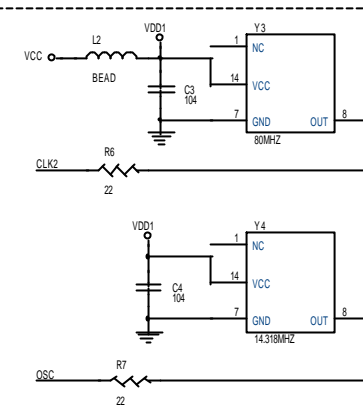


BIOS

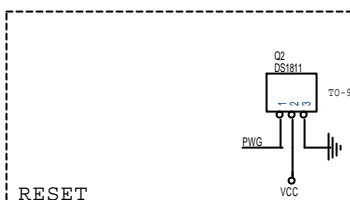
MINIMUM COMPONENT FOR M6117D+ISA
CLOCK 80MHZ, 14.318MHZ
POWER GOOD DETECT(DS1811)
DRAM 512KB(1MB)
BIOS 64KB
ISA 8/16 BIT INTERFACE
5 VOLTS ONLY



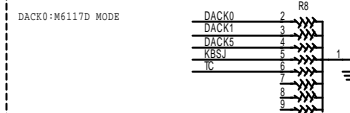
ISA(AT) BUS



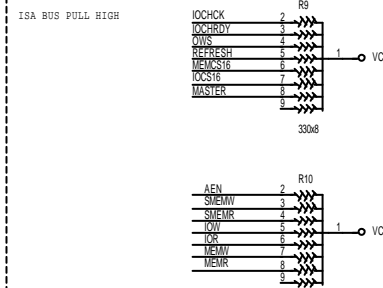
CLOCK



RESET



HARDWARE CONFIGURATION



ISA BUS PULL HIGH

DM&P, JAN YIN CHAN ELECTRONICS CO.LTD.

MINIMUM COMPONENT FOR M6117D+ISA

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