# **Embedded Systems Lab**

CPE 325-02

Timers on the MSP430

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Lab Due: October 21, 2020

Demonstration Due: October 21, 2020

### Introduction

This lab introduces the concept of timers with the MSP430.

# Theory

#### Topic 1: Watchdog Timer

During normal operation, the computer regularly resets the watchdog timer to prevent it from elapsing, or "timing out". If, due to a hardware fault or program error, the computer fails to reset the watchdog, the timer will elapse and generate a timeout signal. The timeout signal is used to initiate corrective actions. The corrective actions typically include placing the computer system in a safe state and restoring normal system operation.

#### Topic 2: Timers

- "A timer is a specialized type of clock which is used to measure time intervals. A timer that counts from zero upwards for measuring time elapsed is often called a stopwatch. It is a device that counts down from a specified time interval and used to generate a time delay, for example, an hourglass is a timer. A counter is a device that stores (and sometimes displays) the number of times a particular event or process occurred, with respect to a clock signal. It is used to count the events happening outside the microcontroller. In electronics, counters can be implemented quite easily using register-type circuits such as a flip-flop." - Source

Topic 3: Timer Mode Control

Mode

Stop

Up

Continuous

Up/down

Description
The timer is halted.
The timer repeatedly counts from zero to the value of TAxCCR0
The timer repeatedly counts from zero to OFFFFh

The timer repeatedly counts from zero up to the value of TAxCCR0 and back down to zero.

Table 1-1. Timer Modes

MC

00

01

10

11

## Lab Assignment

- 1. Pulse width modulation (PWM) can be used to control power supplied to some devices from the microcontroller. For example, using PWM you can change the brightness of your LEDs. If the frequency of the PWM signal supplied to an LED is high enough, you will not notice any blinking, but will be able to adjust the brightness by changing the duty cycle of the signal. Write a C program that implements the following functionality:
  - a. LED1 is initially off. Then it starts gradually turning on until it gains the full brightness. Upon that it should start gradually turning off. Then the cycle repeats. The time between fully on and fully off states should be 3 seconds (full cycle takes 6 seconds).
  - SW1 should stop the brightness change process. After it is pressed, LED1 should keep its current brightness.
  - c. SW2 should start the brightness change process from the point where it was stopped by SW1. You do not have to use interrupts to process switches.
  - d. Use TimerA to produce the PWM signal for controlling LED1 brightness. Make it control the LED. Choose appropriate counting and output modes to do that.
  - e. Use interrupts from the watchdog timer in the interval mode to change LED1 brightness.
  - f. The process of changing brightness should be smooth. That is, the transition between adjacent brightness levels should be barely noticeable.
- 2. Write a program in C that achieves the following:
  - a. Initializes watchdog timer to raise interrupt every 0.5 mili-seconds.
  - b. On every 1000th interrupt you raise an interrupt on the pin where SW1 is interfaced.
  - c. The ISR for SW1 toggles LED2.

### **Observations**

All code satisfies the lab requirements. For question 2, since we are delaying for 0.5 ms 1000 times, that results in 0.5 seconds per toggle. Thus, LED2 blinks at 1Hz in question 2.

### Conclusion

This lab was successful in introducing me to using timers on the MSP430. Demo link

# **Appendix**

Appendix 1: Lab\_07\_Q1

```
#include <msp430.h>
#define SW1 ((P2IN&BIT1) == 0) // Define switch 1
#define SW2 ((P1IN&BIT1) == 0) // Define switch 2
#define REDLED 0x01
void main(void)
     _EINT();
     WDTCTL = WDT_MDLY_32; // Set interval to 32 milliseconds
     SFRIE1 |= WDTIE;
     P1DIR |= REDLED;
     P10UT &= ~REDLED; // Turn LED1 off at start
     P2DIR &= ~BIT1;
     P2REN |= BIT1;
     P20UT |= BIT1;
     P1DIR &= ~BIT1;
     P1REN |= BIT1;
     P10UT |= BIT1;
     TAOCCTLO = CCIE; // TAO count triggers interrupt
     TAOCCRO = 96;
     TAOCCTL1 = CCIE;
```

```
TAOCCR1 = 96;
     TAOCTL = TASSEL_1 | MC_3; // ACLK is clock source, UP/DOWN mode
     int i = 0;
     while(1)
           if(SW1)
                 for(i = 2000; i >= 0; i--); // Debounce
                 if (SW1)
                       WDTCTL = WDTPW + WDTHOLD; // Stop WDT
           if(SW2)
                 for(i = 2000; i >= 0; i--); // Debounce
                 if(SW2)
                       WDTCTL = WDT_MDLY_32; // Resume interval
#pragma vector = WDT_VECTOR
__interrupt void wdtISR(void)
     static int counter = 96;
     static int pwm = 1;
     if(pwm == 0 )
           if(counter == 96)
                 pwm = 1;
           counter++;
           TA0CCR1 = counter;
     if(pwm == 1)
```

```
{
    if(counter == 0)
    {
        pwm = 0;
    }
    counter--;
    TA0CCR1 = counter;
}

#pragma vector = TIMER0_A0_VECTOR
    __interrupt void timerISR(void)
{
    P10UT |= REDLED;
    TA0CCTL0 &= ~CCIFG;
}

#pragma vector = TIMER0_A1_VECTOR
    __interrupt void timerISR2(void)
{
    P10UT &= ~REDLED;
    TA0CCTL1 &= ~CCIFG;
}
```

#### Appendix 2: Lab\_07\_Q2

```
WDTCTL = WDT_MDLY_0_5;
     P4DIR |= GREENLED;
     _EINT();
     SFRIE1 |= WDTIE;
     P2IE |= BIT1;
     P2IES |= BIT1;
     P2IFG &= ~BIT1;
     _BIS_SR(LPM0_bits + GIE); // Enter LPM0 with interrupt
#pragma vector = PORT2_VECTOR
__interrupt void PORT2_ISR(void)
     P2IFG &= ~BIT1;
     P4OUT ^= GREENLED;
#pragma vector = WDT_VECTOR
__interrupt void watchdog_timer(void)
     static int i = 0;
     i++;
     if (i == 1000)
          P2IFG |= BIT1; // Clear the flag
           i = 0;
```