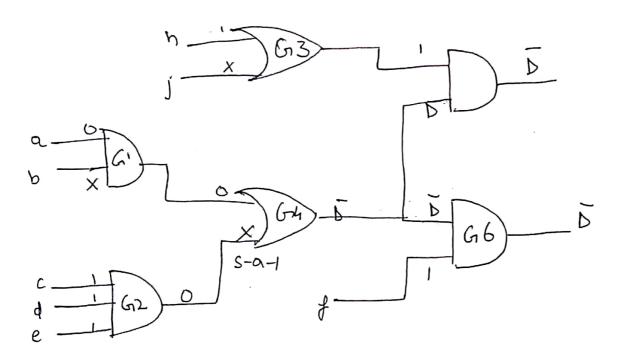
	NAME- DHRUV GOLANI
	1D-2017UEC1056
	EC 1 PAGE MO.:
	VLSI ASSIGNMENT-3
i)_	To implement PODEM Algorithm -
	h — 15
	) 63
	X
Activities of the second secon	a DI
	(G1)
	b x
	S-2-1 (56)
	· · · · · · · · · · · · · · · · · · ·
	Initial objective: 62 = 0 so as to activate the
	S-a-1 fault.
	Back tracing through (12 => C=1
0	del
	e=1, Objective achieved
	Objective: G1=0
	Backtracing Ahrough GI => a = 0
	b=x -> one passibility
	Objective orchived
	Objective: 63=1
	Backtoning through 63=5h=1
	1=x-) one possibility
	objective achieved
	Teacher's Signature

Propagating through 65: Test generated: abodef hj = 0 × 111 x 1 ×

## ii) To implement FAN also virtim



Initial objective: 6,2=0 so as to activate sa-1 jourt

Implication: Assigning C=1, d=1, e=1 Objective: propagate through 64, objective G1=0

Implication: a = 0, b= X

Objective: propogale Mrough 65, objective 63=1

Emplication: h=1, j=X

Test generated: abcdefhj = 0x/11x/x

Objective: Peropogate Morough GG, objective fil

Impairation: 1=1

Test generated: abc defh j= 0 ×1111xx