

| | | | | | | | | | COURSE COMPLETION CERTIFICATE | | | | | |

The certificate is awarded to

Suryansh Khatri

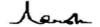
for successfully completing the course

VLSI Digital Design - Chip Design and Verilog Programming

on March 5, 2024



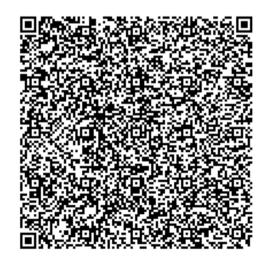
Congratulations! You make us proud!



Thirumala Arohi
Senior Vice President and Head

Education, Training and Assessment (ETA)

Infosys Limited



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