

# ||| COURSE COMPLETION CERTIFICATE |||

The certificate is awarded to

**Suryansh Khatri**

for successfully completing the course

**VLSI Digital Design - Chip Design and Verilog Programming**

on March 5, 2024

Infosys | Springboard

*Congratulations! You make us proud!*



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To verify, scan the QR code at <https://verify.onwingspan.com>

A handwritten signature in black ink, belonging to Thirumala Arohi.

Thirumala Arohi  
Senior Vice President and Head  
Education, Training and Assessment (ETA)  
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